

June, 1998 Preliminary

**AMI 0.8 micron CMOS
Double Poly**

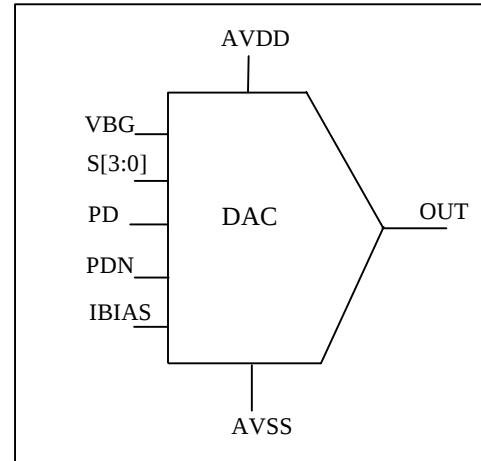
4 Bit RDAC

Features

- 4 bit resolution
- Resistor string implementation
- Power down capability

Description

4 bit Resistive DAC with an output range of 1.32V to 2.0V



PIN DESCRIPTION

NAME	TYPE	DESCRIPTION
AVDD	Analog Supply	Nominal 5V
AVSS	Analog Supply	Ground
IBIAS	Analog input	Current bias, nominally 20uA
VBG	Analog Input	Voltage reference, nominally 1V
S[3:0]	Digital Input	ADC digital select
OUT	Analog Input	ADC analog output
PD	Digital Input	Power down signal
PDN	Digital Input	Power down signal

AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITION	MIN	TYP	MAX	UNITS
AVDD	Analog Power		4.5	5	5.5	V
IDD	Supply current			330		uA
IPD	Standby current				33.0	nA
T	Temperature		-40		85	°C
R	Resolution			4 bit		
TP	Propagation Delay				3.0	us