| | | First Edition | Approved by | Production Div. |
|----------|-------------------------|----------------|-------------|-------------------------|
| | | June 25, 1998 | Checked by | Quality Assurance Div. |
| L | CD Module Specification | Final Revision | | |
| | | ***** | Checked by | Design Engineering Div. |
| Type No. | DMC40218NY* | | Prepared by | Production Div. |
| | | | | |

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Revision History

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| DM | IC40218NY* (CU) I | No.98-0169 | OPTREX CORPORATION | Page 1/14 |

1. General Specifications

Operating Temp. : min. 0° C \sim max. 50° C

Storage Temp. : min. -20° C \sim max. 70° C

Display Format : $40 \text{ characters} \times 2 \text{ lines}$

Display Fonts : $5 \times 8 \text{ dots (1 character)}$

Viewing Area : 152.5 (W) \times 16.5 (H) mm

Outline Dimensions : 182.0 (W) \times 33.5 (H) \times 11.0 max. (D) mm

LCD Type : NSD-7069

(STN / Yellow-mode / reflective)

Viewing Angle : 6:00

Backlight : None

Drawings : Dimensional Outline UE-33026D

2. Electrical Specifications

2.1. Absolute Maximum Ratings

 $V_{SS}=0V$

| Parameter | Symbol | Conditions | Min. | Max. | Units |
|----------------|----------------------------------|------------|------|----------------------|-------|
| Supply Voltage | Vcc-Vss | _ | -0.3 | 7.0 | V |
| (Logic) | | | | | |
| Supply Voltage | V _{CC} -V _{EE} | _ | 0 | 13.0 | V |
| (LCD Drive) | | | | | |
| Input Voltage | VI | _ | -0.3 | V _{CC} +0.3 | V |
| | | | | | |

2.2.DC Characteristics

 $Ta=25^{\circ}C$, $V_{SS}=0V$

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
|----------------|----------------------------------|---------------------------|-------------|------|----------|-------|
| Supply Voltage | V _{CC} -V _{SS} | _ | 4.5 | _ | 5.5 | V |
| (Logic) | | | | | | |
| Supply Voltage | V _{CC} -V _{EE} | | Shown in 3. | 1 | | V |
| (LCD Drive) | | | | | | |
| High Level | V_{IH} | $V_{CC}=5.0V\pm10\%$ | 2.2 | _ | V_{CC} | V |
| Input Voltage | | | | | | |
| Low Level | V_{IL} | $V_{CC}=5.0V\pm10\%$ | 0 | _ | 0.6 | V |
| Input Voltage | | | | | | |
| High Level | V _{OH} | I _{OH} =-0.205mA | 2.4 | _ | V_{CC} | V |
| Output Voltage | | | | | | |
| Low Level | Vol | I _{OL} =1.2mA | 0 | _ | 0.4 | V |
| Output Voltage | | | | | | |
| Supply Current | Icc | Vcc-Vss=5.0V | _ | 3.3 | 4.5 | mA |
| | | | | | | |

2.3.AC Characteristics

 $V_{CC}\!\!=\!\!5.0V\!\pm\!10\%$

| Parameter | Symbol | Conditions | Min. | Max. | Units |
|-----------------------|------------------|------------|------|------|-------|
| Enable Cycle Time | $t_{ m CYC}$ | Fig.1, 2 | 500 | _ | ns |
| Enable Pulse Width | PW _{EH} | Fig.1, 2 | 230 | _ | ns |
| Enable Rise/Fall Time | ter, tef | Fig.1, 2 | _ | 20 | ns |
| Address Setup Time | tas | Fig.1, 2 | 40 | _ | ns |
| Address Hold Time | $t_{ m AH}$ | Fig.1, 2 | 10 | _ | ns |
| Write Data Setup Time | $t_{ m DSW}$ | Fig.1 | 80 | _ | ns |
| Write Data Hold Time | $t_{ m DHW}$ | Fig.1 | 10 | _ | ns |
| Read Data Delay Time | $t_{ m DDR}$ | Fig.2 | _ | 160 | ns |
| Read Data Hold Time | $t_{ m DHR}$ | Fig.2 | 5 | _ | ns |

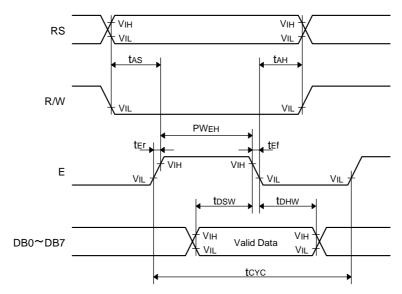


Fig.1 Write Operation Timing

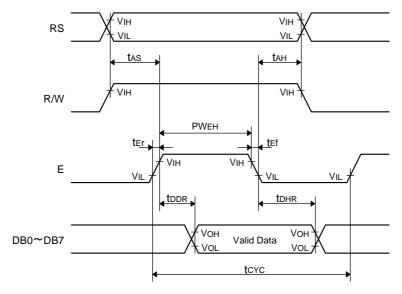


Fig.2 Read Operation Timing

3. Optical Specifications

3.1.LCD Driving Voltage

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
|---------------------|----------------------------------|------------|------|------|------|-------|
| Recommended | | Ta=0°C | | | 5.3 | V |
| LCD Driving Voltage | V _{CC} -V _{EE} | Ta=25°C | 4.4 | 4.7 | 5.0 | V |
| Note 1 | | Ta=50°C | 4.1 | _ | _ | V |

Note 1: Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2. Optical Characteristics

Ta=25°C, 1/16 Duty, 1/5 Bias, V_D =4.7V (Note 4), $\theta = 0^{\circ}$, $\phi = -^{\circ}$

| Pa | rameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
|---------------|--------------|--------|---|------|------|------|-------|
| Contrast Rat | tio Note 1 | CR | $\theta = 0^{\circ}$, $\phi = -^{\circ}$ | _ | 8 | _ | |
| Viewing Angle | | | Shown in 3.3 | | | | |
| Response | Rise Note 2 | Ton | _ | _ | 140 | 210 | ms |
| Time | Decay Note 3 | Тоғғ | | _ | 180 | 270 | ms |

Note 1: Contrast ratio is definded as follows.

 $CR = L_{OFF} / L_{ON}$

Lon: Luminance of the ON segments

Loff: Luminance of the OFF segments

Note 2: The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3: The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

Note 4: Definition of Driving Voltage V_D

VD=VCC-VADJ-VBE

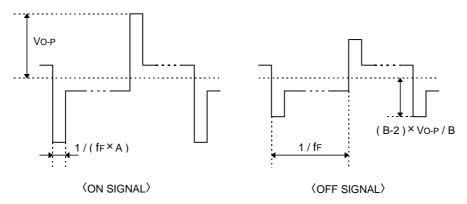
Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A: Duty Number, B: Bias Number). Driving voltage V_D is definded as follows.

 $V_D = (Vth1+Vth2) / 2$

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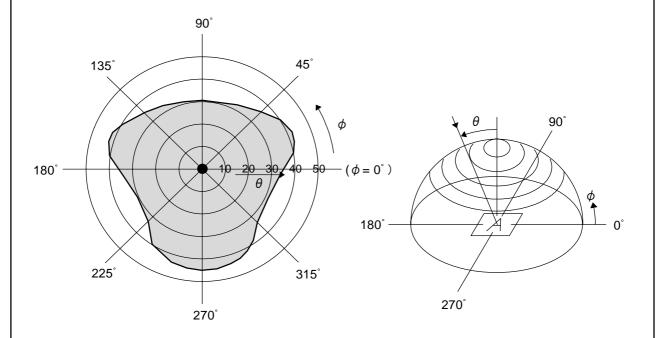
Vth1 : The voltage V_{O-P} that should provide 50% of the satulation level in the luminance at the segment which the ON signal is applied to.

 $\label{eq:Vth2} While The voltage V_{O\text{-P}} \ that should provide 50\% \ of the satulation level in the luminance at the segment which the OFF signal is applied to.$



3.3. Definition of Viewing Angle and Optimum Viewing Area

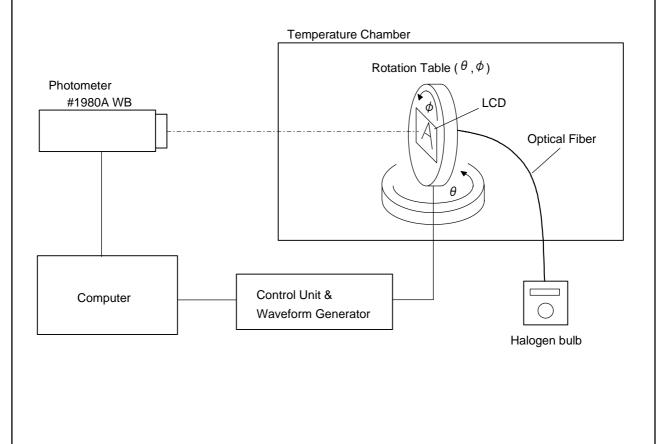
- •Point lacktriangle shows the point where contrast ratio is measured. : $\theta = 0^{\circ}$, $\phi = -^{\circ}$
- Driving condition : 1/16 Duty, 1/5 Bias, V_D =4.7V, f_F =84.4Hz



3.4. System Block Diagram

shows typ. $CR \ge 2$

·Area



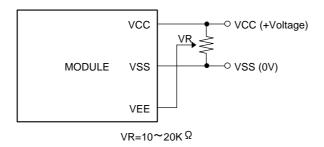
4.<u>I/O Terminal</u>

4.1.Pin Assignment

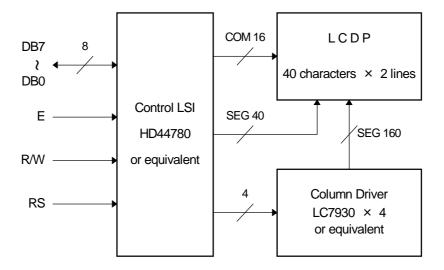
| No. | Symbol | Level | Function | |
|-----|--------------|-------|---|--|
| 1 | V_{SS} | | Power Supply (0V, GND) | |
| 2 | V_{CC} | | Power Supply for Logic | |
| 3 | $V_{\rm EE}$ | _ | Power Supply for LCD Drive | |
| 4 | RS | H/L | Register Select Signal | |
| 5 | R/W | H/L | Read/Write Select Signal H: Read L: Write | |
| 6 | E | H,H→L | Enable Signal (No pull-up Resister) | |
| 7 | DB0 | H/L | Data Bus Line / Non-connection at 4-bit operation | |
| 8 | DB1 | H/L | Data Bus Line / Non-connection at 4-bit operation | |
| 9 | DB2 | H/L | Data Bus Line / Non-connection at 4-bit operation | |
| 10 | DB3 | H/L | Data Bus Line / Non-connection at 4-bit operation | |
| 11 | DB4 | H/L | Data Bus Line | |
| 12 | DB5 | H/L | Data Bus Line | |
| 13 | DB6 | H/L | Data Bus Line | |
| 14 | DB7 | H/L | Data Bus Line | |

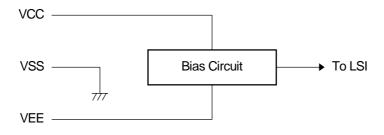
4.2. Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.



4.3.Block Diagram





5.Test

No change on display and in operation under the following test condition.

| No. | Parameter | Conditions | Notes |
|-----|----------------------------|---|-------|
| 1 | High Temperature Operating | $50^{\circ}\text{C} \pm 2^{\circ}\text{C}$, 96hrs (operation state) | |
| 2 | Low Temperature Operating | $0^{\circ}\text{C} \pm 2^{\circ}\text{C}$, 96hrs (operation state) | 3 |
| 3 | High Temperature Storage | 70°C ±2°C, 96hrs | 4 |
| 4 | Low Temperature Storage | -20°C ±2°C, 96hrs | 3, 4 |
| 5 | Damp Proof Test | 40°C ±2°C, 90∼95%RH, 96hrs | 3, 4 |
| 6 | Vibration Test | Total fixed amplitude : 1.5mm | 5 |
| | | Vibration Frequency : 10∼55Hz | |
| | | One cycle 60 seconds to 3 directions of X, Y, Z for | |
| | | each 15 minutes | |
| 7 | Shock Test | To be measured after dropping from 60cm high on | |
| | | the concrete surface in packing state. | |
| | | Dropping method corner dropping A corner : once Edge dropping B,C,D edge : once Face dropping E,F,G face : once | |

Note 1: Unless otherwise specified, tests will be conducted under the following condition.

Temperature : $20\pm5^{\circ}$ C Humidity : $65\pm5\%$

Note 2: Unless otherwise specified, tests will be not conducted under functioning state.

Note 3: No dew condensation to be observed.

Note 4 : The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5: Vibration test will be conducted to the product itself without putting it in a container.

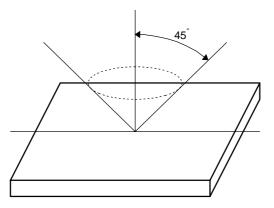
6. Appearance Standards

6.1.Inspection conditions

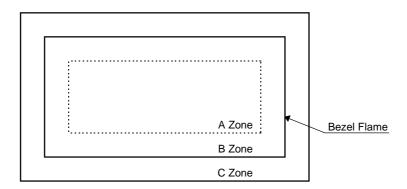
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



6.2. Definition of applicable Zones



A Zone : Active display area

B Zone: Area from outside of "A Zone" to validity viewing area

C Zone: Rest parts

A Zone + B Zone = Validity viewing area

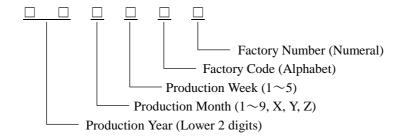
6.3.Standards

| No. | Parameter | Criteria | | | | |
|-----|--------------------|-----------------|------------------|---------------|---------------|-----|
| 1 | Black and | (1) Round Sha | ipe | | | |
| | White Spots, | | Zone | Ac | ceptable Numl | ber |
| | Foreign Substances | Dimension | (mm) | A | В | С |
| | | | $D \leq 0.1$ | * | * | * |
| | | 0.1 < | D ≤ 0.2 | 5 | 5 | * |
| | | 0.2 < | $D \leq 0.3$ | 0 | 1 | * |
| | | 0.3 < | D | 0 | 0 | * |
| | | D = (Long | g + Short) / 2 | * : Disregar | ·d | |
| | | (2) Line Shape | e | | | |
| | | | Zone | Ac | ceptable Numl | ber |
| | | X(mm) | Y(mm) | A | В | С |
| | | _ | 0.02≧W | * | * | * |
| | | 2.0≧L | 0.03≧W | 3 | 3 | * |
| | | 1.0≧L | 0.04≧W | 1 | 2 | * |
| | | 1.0≧L | 0.05≧W | 0 | 2 | * |
| | | _ | 0.05 < W | In | the same way | (1) |
| | | X : Length | Y: Width | * : Disregard | i | |
| | | Total defects s | shall not exceed | 5. | | |
| 2 | Air Bubbles | | | | | |
| | (between glass | | Zone | Ac | ceptable Numl | ber |
| | & polarizer) | Dimension | (mm) | A | В | С |
| | | | D ≦0.15 | * | * | * |
| | | 0.15< | D ≦0.3 | 2 | 3 | * |
| | | 0.3 < | D ≦0.5 | 1 | 2 | * |
| | | 0.5 < | D ≦1.0 | 0 | 1 | * |
| | | * : Disrega | ard | | | • |
| | | Total defects s | hall not exceed | 3. | | |

| No. | Parameter | Criteria | | |
|-----|---------------------|--|--|--|
| 3 | The Shape of Dot | (1) Dot Shape (with Dent) 0.15 ≧ As per the sketch of left hand. | | |
| | | (2) Dot Shape (with Projection) Should not be connected to next dot. (3) Pin Hole | | |
| | | (3) Fill Hole $(X+Y)/2 \le 0.2 \text{mm}$ (Less than 0.1 mm is no counted.) Total defects shall not exceed 5. | | |
| 4 | Polarizer Scratches | Not to be conspicuous defects. | | |
| 5 | Polarizer Dirts | If the stains are removed easily from LCDP surface, the module is not defective. | | |
| 6 | Color Variation | Not to be conspicuous defects. | | |

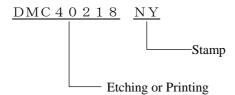
7. Code System of Production Lot

The production lot of module is specified as follows.



8. Type Number

The type number of module is specified on the back of module as follows.



9. Applying Precautions

Please contact us when questions and/or new problems not specified in this specifications arise.

10. Handling Precautions

Optrex Products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment, measurement devices and etc..

Optrex Products are not designed, intended, or authorized for use in any application in which the failure of the product could result in a situation where personal injury or death may occur. These applications include, but are not limited to, life-sustaining equipment, nuclear control devices, aerospace equipment, devices related to hazardous or flammable materials, etc. (If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications, Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.) Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application (without such consent), Buyer shall indemnify and hold Optrex and its officers, employees, subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses, and reasonable attorney's fees, arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part.

- 1) LCD may be broken because it is made of glass.
- 2) Polarizer is a soft material and can easily be scratched.
- 3) Please avoid static electricity.
 - ① Please be sure to ground human body and electric appliances during work.
 - ② It is preferable to use conductive mat on table and wear cotton clothes or conduction processed fiber. Synthetic fiber is not recommended.
 - ③ Please slowly peel off protective film, because static electricity may be charged.
- 4) If it is necessary to store LCD modules for a long time, please comply with the following procedures. If storage condition is not satisfactory, display (especially polarizer) may be deteriorated or soldering I/O terminals may become difficult (some oxide is generated at I/O terminals plating).
 - ① Store as delivered by Optrex
 - ② If you store as unpacked, put in anti-static bag, seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
 - ③ Store at temperature 0 to $+35^{\circ}$ C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.
- 5) The module does not contain excess current limiter.

 Please design the limiter to cut excess current in your power supply circuit.
- 6) Liquid crystal may be leaked when display is broken. Never taste it. If your hands or clothes touch it, please immediately wash using soap.
- 7) The connection between the bezel and Vss (GND) is not specified in the module. (Some module do not maintain connection between them.) Please consult OPTREX to specify the connection.

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