**Identification**

DTM63617 64Mx72
DTM63616 32Mx72
DTM63629 32Mx72
DTM63615 16Mx72

Performance range

PC2100 266MHz/CL=2.5
200MHz/CL=2

Features

Utilizes 133MHz DDR SDRAM

Auto & self refresh capability
(63617, 63616, - 8192 cycles/64 ms)
(63629, 63615 - 4096 cycles/64 ms)

SSTL_2 compatible inputs and outputs

VDD/VDDQ= 2.5V +/- 0.2V

MRS cycle, with address key, programs Latency (Access from Column address) Burst length (2,4, and 8 page)

Data scramble(Sequential & Interleave)

Serial Presence Detect

184-pin JEDEC DIMM, double sided assembly, 5.25" wide by 1.20" high

Commands entered on each positive CK edge

DQS edge-aligned with Data for Reads; center-aligned with Data for Writes

Two Data accesses per clock cycle

DTM63617 and DTM63629 are two Physical Banks

DTM63616 and DTM63615 are one Physical Bank

DTM63617 and DTM63616 SDRAM Addressing (13/10/2) (ROW/COL/BANK)

DTM63629, 63615 SDRAM Addressing (12/10/2) (ROW/COL/BANK)

Description

The Dataram DTM63617 assembly is a 1.2" high Registered 64Mx72 DDR Synchronous Dynamic RAM high-density memory module. The DTM63617 consists of eighteen CMOS monolithic 32Mx8 bit DDR Synchronous DRAMs in a 66-pin TSOP-II 400 mil package, two 14-bit Registers, one PLL and one 2K EEPROM used for serial presence detect (SPD).

The Dataram DTM63629 assembly is a 1.2" high Registered 32Mx72 DDR Synchronous Dynamic RAM high-density memory module. The DTM63629 consists of eighteen CMOS monolithic 16Mx8 bit DDR Synchronous DRAMs in a 66-pin TSOP-II 400 mil package, two 14-bit Registers, one PLL and one 2K EEPROM used for serial presence detect (SPD).

The Dataram DTM63616 assembly is a 1.2" high Registered 32Mx72 DDR Synchronous Dynamic RAM high-density memory module. The DTM63616 consists of nine CMOS monolithic 32Mx8 bit DDR Synchronous DRAMs in a 66-pin TSOP-II 400 mil package, two 14-bit Registers, one PLL and one 2K EEPROM used for serial presence detect (SPD).

The Dataram DTM63615 assembly is a 1.2" high Registered 16Mx72 DDR Synchronous Dynamic RAM high-density memory module. The DTM63615 consists of nine CMOS monolithic 16Mx8 bit DDR Synchronous DRAMs in a 66-pin TSOP-II 400 mil package, two 14-bit Registers, one PLL and one 2K EEPROM used for serial presence detect (SPD).



DTM63617, DTM63629, DTM63616, DTM63615

512MB-64Mx72, 256MB-32Mx72, 256MB-32Mx72, 128MB-
16Mx72, 184-Pin Registered DDR SDRAM DIMM

Pin Configurations

Front side				Back side			
1 VREF	24 DQ17	47 DQS8	70 VDD	93 VSS	116 VSS	139 VSS	162 DQ46
2 DQ0	25 DQS2	48 A0	71 NC	94 DQ4	117 DQ21	140 DQS17	163 DQ47
3 VSS	26 VSS	49 CB2	72 DQ48	95 DQ5	118 A11	141 A10	164 VDDQ
4 DQ1	27 A9	50 VSS	73 DQ49	96 VDDQ	119 DQS11	142 CB6	165 DQ52
5 DQS0	28 DQ18	51 CB3	74 VSS	97 DQS9	120 VDD	143 VDDQ	166 DQ53
6 DQ2	29 A7	52 BA1	75 NC	98 DQ6	121 DQ22	144 CB7	167 NC
7 VDD	30 VDDQ	53 DQ32	76 NC	99 DQ7	122 A8	145 VSS	168 VDD
8 DQ3	31 DQ19	54 VDDQ	77 VDDQ	100 VSS	123 DQ23	146 DQ36	169 DQS15
9 NC	32 A5	55 DQ33	78 DQS6	101 NC	124 VSS	147 DQ37	170 DQ54
10 /RESET	33 DQ24	56 DQS4	79 DQ50	102 NC	125 A6	148 VDD	171 DQ55
11 VSS	34 VSS	57 DQ34	80 DQ51	103 A13***	126 DQ28	149 DQS13	172 VDDQ
12 DQ8	35 DQ25	58 VSS	81 VSS	104 VDDQ	127 DQ29	150 DQ38	173 NC
13 DQ9	36 DQS3	59 BA0	82 VDDID	105 DQ12	128 VDDQ	151 DQ39	174 DQ60
14 DQS1	37 A4	60 DQ35	83 DQ56	106 DQ13	129 DQS12	152 VSS	175 DQ61
15 VDDQ	38 VDD	61 DQ40	84 DQ57	107 DQS10	130 A3	153 DQ44	176 VSS
16 NC	39 DQ26	62 VDDQ	85 VDD	108 VDD	131 DQ30	154 RAS	177 DQS16
17 NC	40 DQ27	63 WE	86 DQS7	109 DQ14	132 VSS	155 DQ45	178 DQ62
18 VSS	41 A2	64 DQ41	87 DQ58	110 DQ15	133 DQ31	156 VDDQ	179 DQ63
19 DQ10	42 VSS	65 CAS	88 DQ59	111 CKE1*	134 CB4	157 S0	180 VDDQ
20 DQ11	43 A1	66 VSS	89 VSS	112 VDDQ	135 CB5	158 S1*	181 SA0
21 CKE0	44 CB0	67 DQS5	90 WP***	113 NC	136 VDDQ	159 DQS14	182 SA1
22 VDDQ	45 CB1	68 DQ42	91 SDA	114 DQ20	137 CK0	160 VSS	183 SA2
23 DQ16	46 VDD	69 DQ43	92 SCL	115 A12**	138 /CK0	161 DQ46	184 VDDSPD

NC = No connect

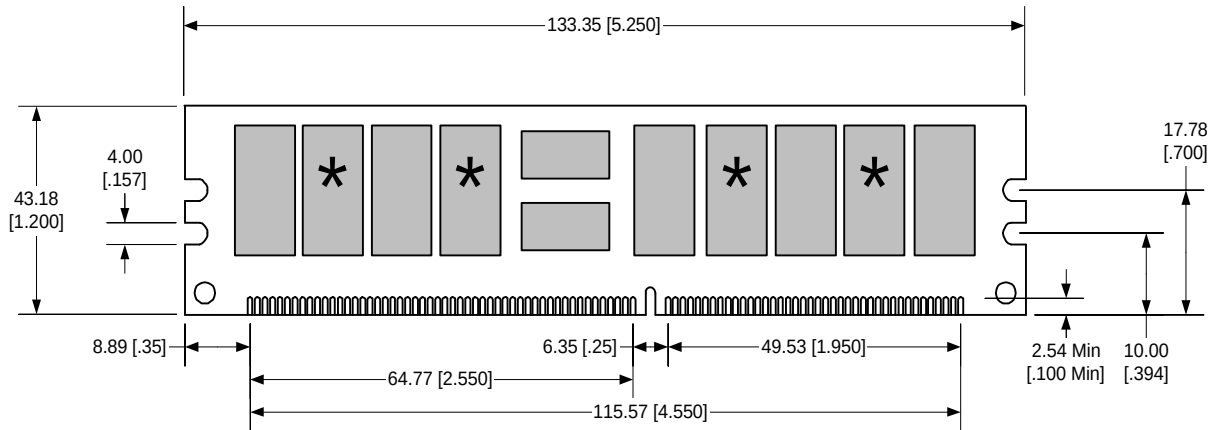
* = Not used on the DTM63616 and DTM63615

** = Not used on the DTM63629 and DTM63615

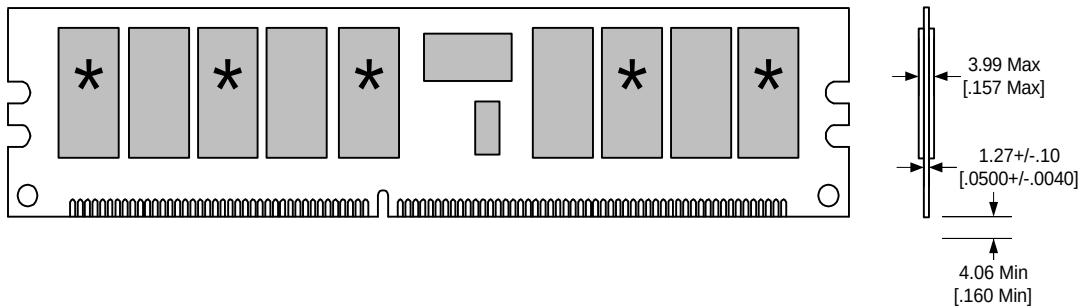
*** = Not used on the DTM63617, DTM63629, DTM63616, and
DTM63615

Pin Names

Pin Names	Function
RAS	Row address strobe
CAS	Column address strobe
WE	Write enable
CS[1:0]	Chip select
CK0,/CK0	Differential Clock inputs
CKE[1:0]	Clock enable input
BA[1:0]	Bank select input
A[13:0]	Address input(Multiplexed)
VREF	SSTL_2 reference input
VDDID	VDD identification flag
SCL	Serial clock
SDA	Serial Data I/O
SA[2:0]	Address EEPROM
CB[7:0]	Data I/Os: Check bits
DQS[17:0]	Data strobes
DQ[63:0]	Data I/Os: Data bus
VDDQ	DQ power supply: 2.5V +/- .2V
VDD	Power supply: 2.5V +/- .2V
VSS	Ground
VDDSPD	Serial EEPROM power supply
/RESET	Reset Enable
NC	No connects



Front



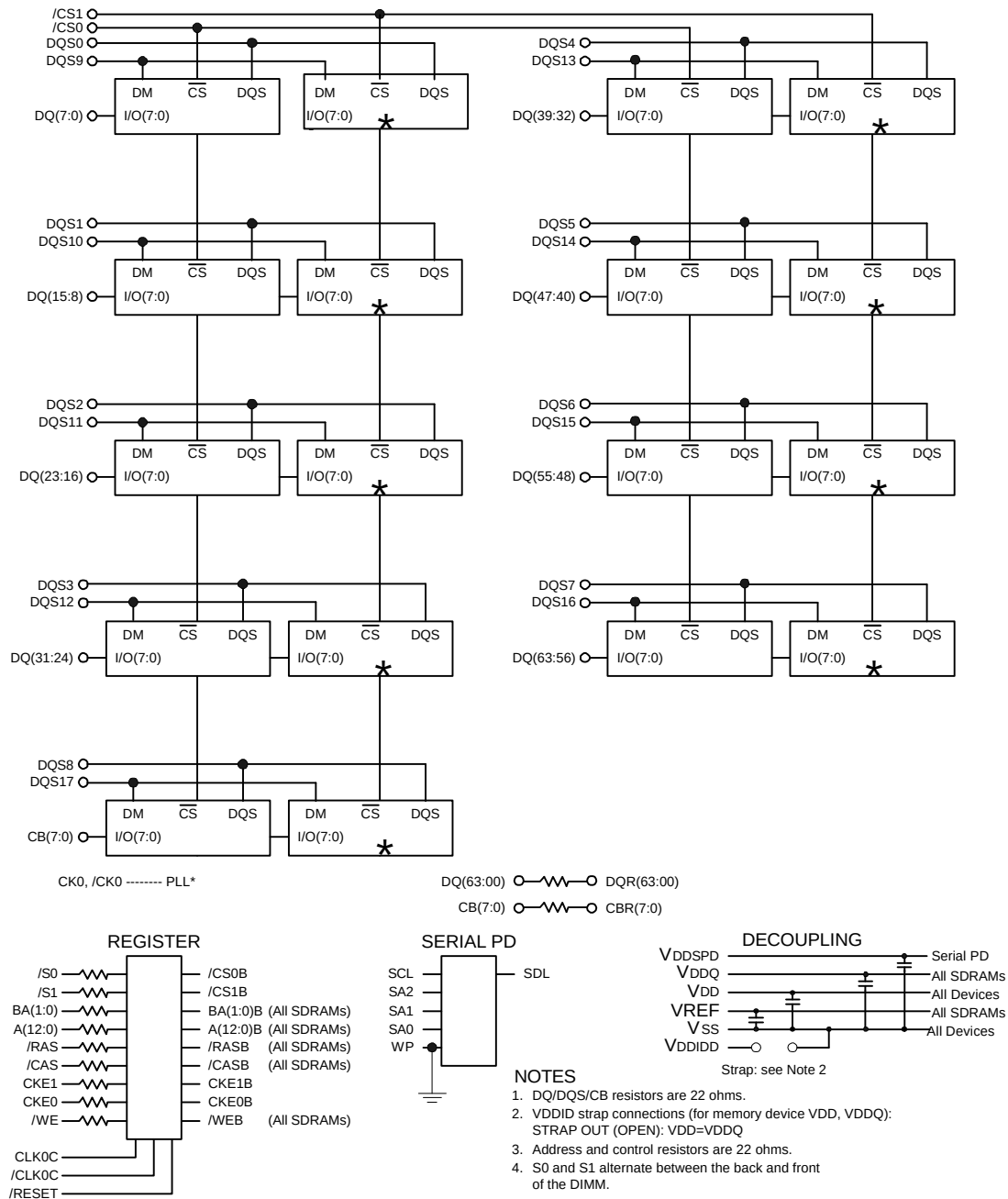
Back

Side

Notes

All dimensions are expressed: millimeters [inches].
Tolerances on all dimensions except where
otherwise indicated are $\pm .13$ [.005].

* Remove for DTM63616 and DTM63615



* Remove for DTM63616 and DTM63615