## **DESCRIPTION:**

The *M-Densus* series is a family of interchangeable memory modules. The 128 Megabit SDRAM is a member of this family which utilizes the new and innovative space saving TSOP stacking technology. The modules are constructed with 16 Meg x 4 SDRAMs.

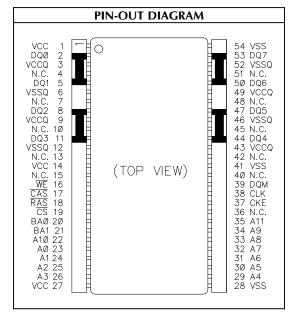
This 64 Megabit based *M-Densus* module, the DPSD16MX8RY5 has been designed to fit in the same footprint as the 16 Meg x 8 SDRAM TSOP monolithic and 64 Megabit SDRAM based family of *M-Densus* modules. This allows the memory board designer to upgrade the memory in their products without redesigning the memory board, thus saving time and money.

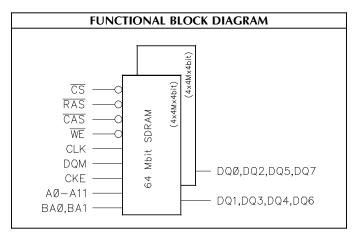
## **FEATURES:**

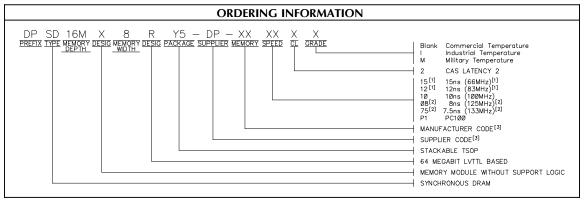
- Configuration Available:
   16 Meg x 8 (4M x 4 x 8 bits)
- Clock Frequency: 66<sup>[1]</sup>, 83<sup>[1]</sup>, 100, 125<sup>[2]</sup>, 133<sup>[2]</sup> MHz (max.)
- PC100 and PC133 Compatable
- 3.3V Supply
- LVTTL Compatible I/O
- Four Bank Operation
- Programmable Burst Type, Burst Length, and CAS Latency
- 4096 Cycles / 64 ms
- Auto and Self Refresh
- Package: TSOP Leadless Stack

NOTES: [1] Available in Military and Industrial Temperature Ranges Only. [2] Available in Commercial Temperature Range Only.

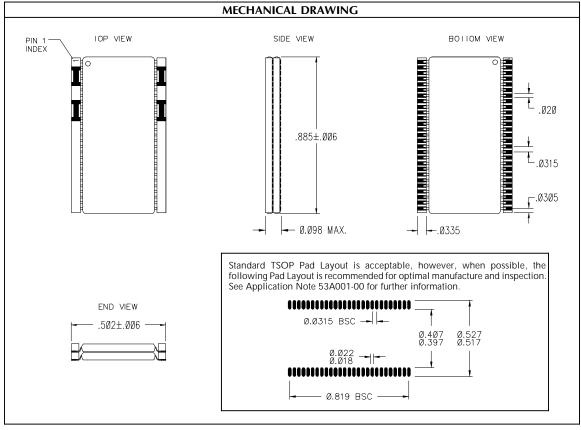
PIN NAMES	
A0 - A11	Row Address: A0 - A11 Column Address: A0 - A9
BAO, BA1	Bank Select Address
DQ0 - DQ7	Data In / Data Out
CAS	Column Address Strobes
RAS	Row Address Enables
WE	Data Write Enable
DQM	Data Input/Output Mask
CKE	Clock Enable
CLK	System Clock
CS	Chip Select
V <sub>CC</sub> /V <sub>SS</sub>	Power Supply/Ground
V <sub>CCQ</sub> /V <sub>SSQ</sub>	Data Output Power/Ground
N.C.	No Connect







- NOTES: [1] Available in Military and Industrial Temperature Ranges Only.
  [2] Available in Commercial Temperature Range Only.
  [3] Contact your sales representative for supplier and manufacturer codes.



## **Dense-Pac Microsystems, Inc.** 7321 Lincoln Way, Garden Grove, California 92841-1431

♦ (800) 642-4477 ♦ FAX: (714) 897-1772 ♦ http://www.dense-pac.com (714) 898-0007