

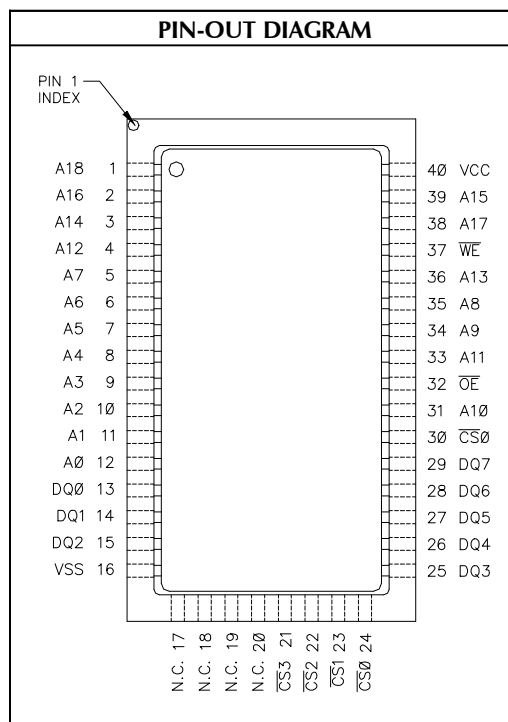
DESCRIPTION:

The *LP-Stack™* series is a family of interchangeable memory modules. The 4 Megabit SRAM is a member of this family which utilizes the new and innovative space saving TSOP technology. The modules are constructed with 512K x 8, 3.3 Volt SRAM's.

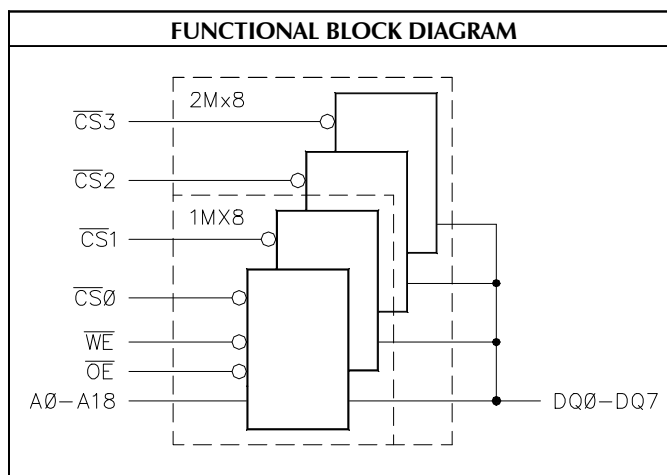
The 4 Megabit based *LP-Stack™* modules have been designed to fit in the same footprint as the 512K x 8 SRAM TSOP monolithic. This allows the memory board designer to upgrade the memory in their products without redesigning the memory board, thus saving time and money.

FEATURES:

- Configurations Available:
 - 8 Megabit: 1 Meg x 8
 - 16 Megabit: 2 Meg x 8
- Access Times: 70, 85, 100ns (max.)
- Fully Static Operation - No Clock or Refresh Required
- Equal Access and Cycle Times
- 3.3 Volt Supply
- TTL Compatible Inputs and Outputs
- Package: 40 Pin Leadless LP-Stacks™
 - DP3S1MX8MY5 (2-High LP-Stack)
 - DP3S2MX8MY5 (4 High LP-Stack)



PIN NAMES	
A0 - A18	Address
DQ0 - DQ7	Data In / Data Out
CS0 - CS3	Chip Selects
WE	Write Enable
OE	Output Enable
VDD	Power Supply (+3.3V)
VSS	Ground
N.C.	No Connect



4 Megabit 3.3 Volt SRAM Based

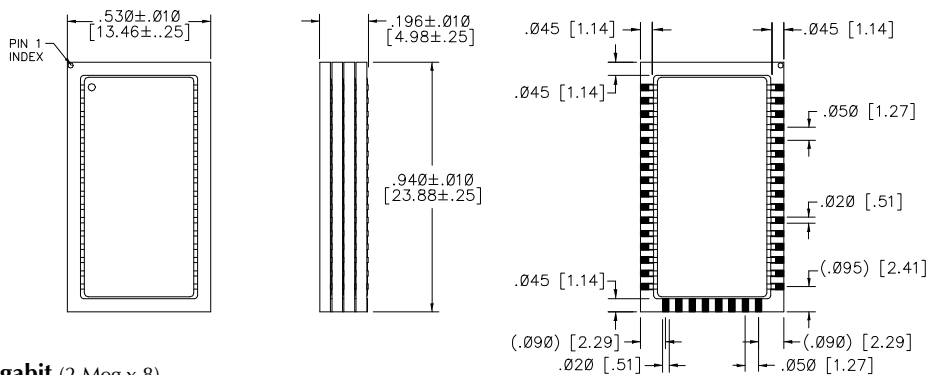
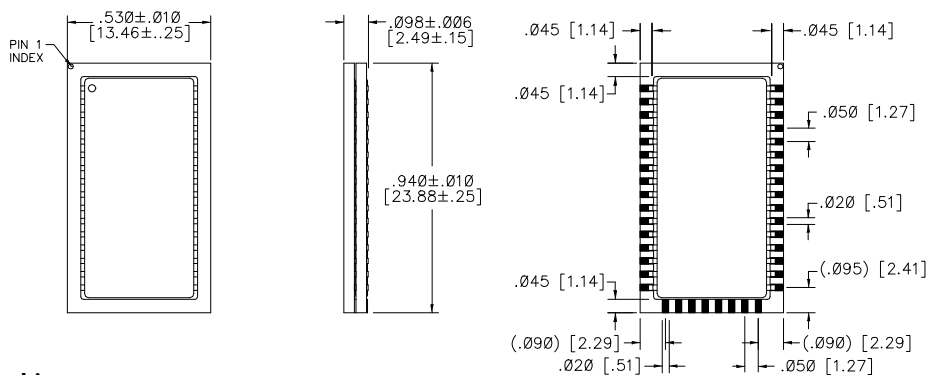
Dense-Pac Microsystems, Inc.

PRELIMINARY

ORDERING INFORMATION

DP	3S	n	X	8	M	Y5	-	n	C	
PREFIX	TYPE	MEMORY DEPTH	DESIG	MEMORY WIDTH	DESIG	PACKAGE		SPEED	GRADE	
										C COMMERCIAL 0°C to +70°C
										CI INDUSTRIAL TEMP -40°C to +85°C
								70	70ns	
								80	80ns	
								10	100ns	
										LP-STACK
										4 MEGABIT BASED
										MEMORY MODULE WITHOUT SUPPORT LOGIC
										2M 2 MEGABIT
										1M 1 MEGABIT
										CMOS 3.3 VOLT SRAM

MECHANICAL DRAWING



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