

# DA108S1 DA112S1

# Application Specific Discretes A.S.D.<sup>TM</sup>

# **DIODE ARRAY**

#### **APPLICATION**

Protection of logic side of ISDN S-interface. Protection of I/O lines of microcontroller. Signal conditioning.

#### **FEATURES**

- ARRAY OF 8 OR 12 DIODES
- LOW INPUT CAPACITANCE
- SUITABLE FOR DIGITAL LINE PROTECTION

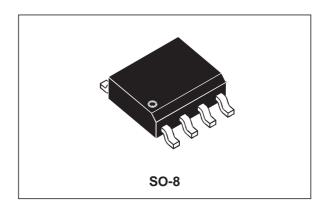
#### **DESCRIPTION**

ARRAY of 8 or 12 diodes configured by cells of 2 diodes, each cell being used to protect signal line from transient overvoltages by clamping action.

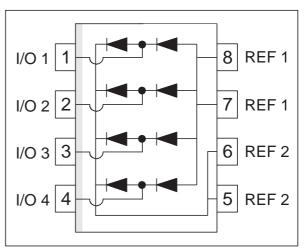
#### **COMPLIES WITH FOLLOWING STANDARDS:**

IEC1000-4-22 level 4: 15kV (air discharge)

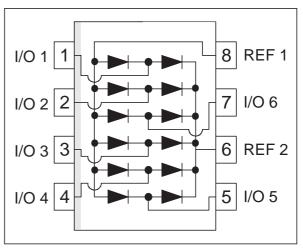
8kV (contact discharge)



#### **FUNCTIONAL DIAGRAM: DA108S1**



#### **FUNCTIONAL DIAGRAM: DA112S1**



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# DA108S1 / DA112S1

# **ABSOLUTE MAXIMUM RATINGS** $(T_{amb} = 25^{\circ}C)$

Symbol	Parameter		Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage (for one single diode)		18	V
I <sub>PP</sub>	Repetitive peak forward current * 8/20 μs		12	А
Р	Power dissipation		0.73	W
Tstg Tj	Storage temperature range Maximum operating junction temperature		- 55 to + 150 150	°C
TL	Maximum lead temperature for soldering during 10s.		260	°C

<sup>\*</sup> The surge is repeated after the device returns to ambient temperature

### THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R <sub>th (j-a)</sub>	Junction to ambient	170	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>amb</sub> = 25°C)

Symbol	Parameter		Max.	Unit	
V <sub>FP</sub>	Peak forward voltage	I <sub>PP</sub> = 12A, 8/20 μs	DA108S1 DA112S1	9 12	V
V <sub>F</sub>	Forward voltage	I <sub>F</sub> = 50 mA		1.2	V
I <sub>R</sub>	Reverse leakage current	V <sub>R</sub> = 15V		2	μА

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Fig.1: Input capacitance

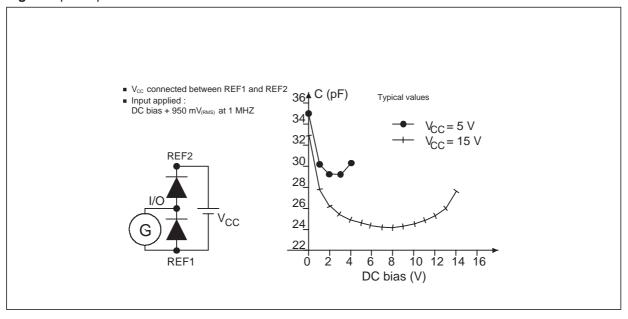
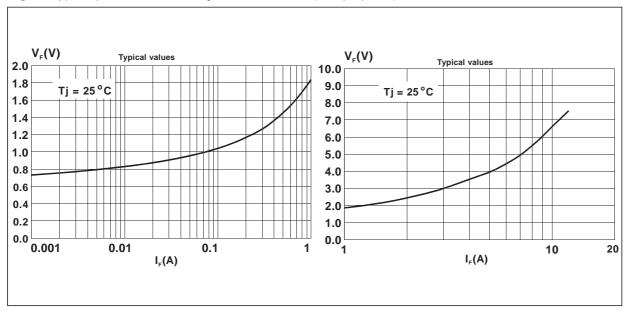
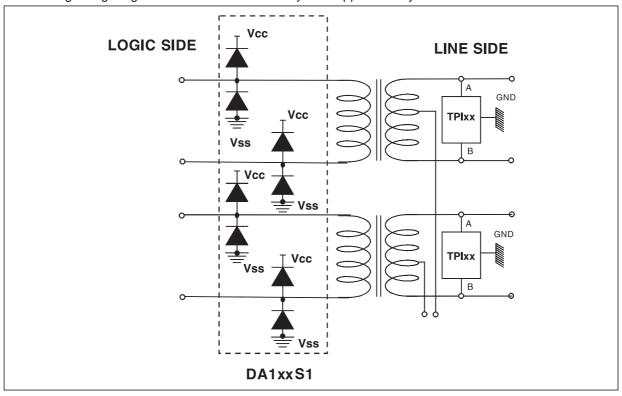


Fig.2: Typical peak forward voltage characteristics (8/20µs pulse)

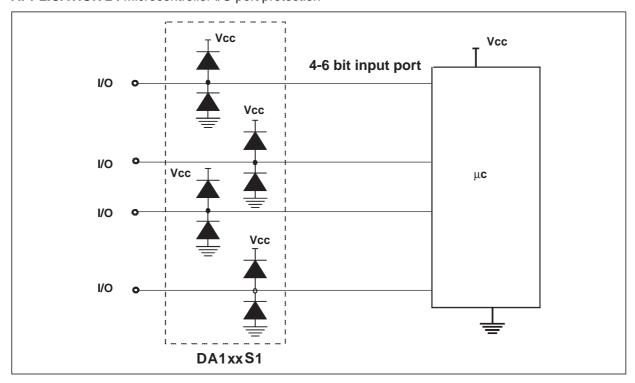


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**APPLICATION 1 :** ISDN Interface Protection Residual lightning surges at transformer secondary are suppressed by DA108S1



APPLICATION 2: Microcontroller I/O port protection

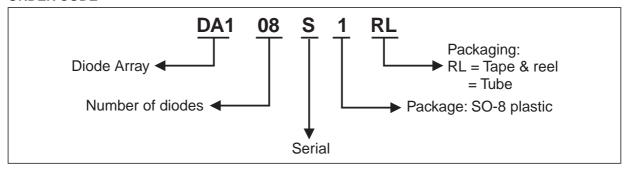


**IMPORTANT**: DA108S1 must imperatively be connected to the reference voltages by REF1 and REF2.

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#### DA108S1 / DA112S1

#### **ORDER CODE**



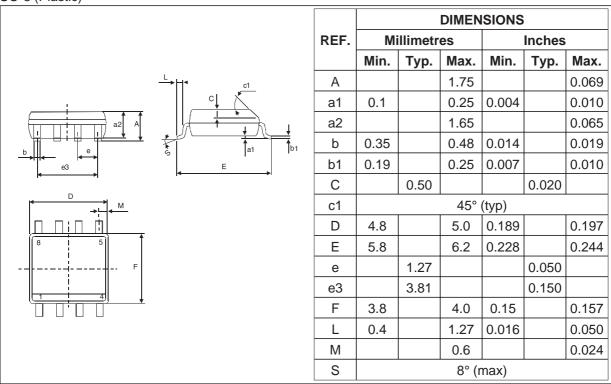
MARKING: Logo, Data Code,

DA108S1	DA108S
DA112S1	DA112S

**Packaging:** Preference packaging is tape and reel.

#### **PACKAGE MECHANICAL DATA**

SO-8 (Plastic)



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