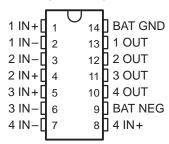
- Designed for –52-V Battery Operation
- 50-mA Output Current Capability
- Input Compatible With TTL and CMOS
- High Common-Mode Input Voltage Range
- Very Low Input Current
- Fail-Safe Disconnect Feature
- Built-in Output Clamp Diode
- Direct Replacement for National DS3680 and Fairchild μA3680

description

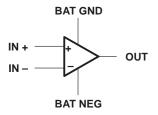
The DS3680 telephone relay driver is a monolithic integrated circuit designed to interface -48-V relay systems to TTL or other systems in telephone applications. It is capable of sourcing up to 50 mA from standard -52-V battery power. To reduce the effects of noise and IR drop between logic ground and battery ground, these drivers are designed to operate with a common-mode input range of ±20 V referenced to battery ground. The common-mode input voltages for the four drivers can be different, so a wide range of input elements can be accommodated. The high-impedance inputs are compatible with positive TTL and CMOS levels or negative logic levels. A clamp network is included in the driver outputs to limit high-voltage transients generated by the relay coil during switching. The complementary inputs ensure that the driver output is off as a fail-safe condition when either output is open.

The DS3680 is characterized for operation from 0°C to 70°C.

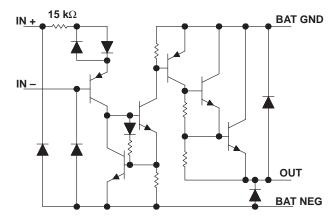
D OR N PACKAGE (TOP VIEW)



symbol (each driver)



schematic diagram (each driver)



All resistor values shown are nominal.

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage range at BAT NEG, V _{BAT} (see Note 1)	–70 V to 0.5 V
Input voltage range with respect to BAT GND	70 V to 20 V
Input voltage range with respect to BAT NEG	0.5 V to 70 V
Differential input voltage, V _{ID} (see Note 2)	±20 V
Output current, IO: Resistive load	–100 mA
Inductive load	–50 mA
Inductive output load	5 H
Continuous total dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A	0°C to 70°C
Storage temperature range, T _{stq}	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds	260°C

NOTES: 1. All voltages are with respect to BAT GND, unless otherwise specified.

2. Differential input voltages are at the noninverting input terminal IN+ with respect to the inverting input terminal IN-.

DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{A}} \le 25^{\circ}\mbox{C}$ POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING
D	950 mW	7.6 mW/°C	608 mW
N	1150 mW	9.2 mW/°C	736 mW

recommended operating conditions

	MIN	MAX	UNIT
Supply voltage, V _{BAT} _	-10	-60	V
Input voltage, either input	-20†	20	V
High-level differential input voltage, V _{IDH}	2	20	V
Low-level differential input voltage, V _{IDL}	-20†	0.8	V
Operating free-air temperature, T _A	0	70	°C

[†] The algebraic convention, in which the less positive (more negative) limit is designated minimum, is used in this data sheet for input voltage levels.

electrical characteristics over recommended operating free-air temperature range, $V_{BAT-} = -52 \text{ V}$ (unless otherwise noted)

PARAMETER		TEST CON	IDITIONS	MIN TYP‡	MAX	UNIT	
	V _{ID} = 2 V			40	100		
lιΗ	High-level input current (into IN+)	V _{ID} = 7 V		375	1000	μA	
1	Low level input current (into IN L)	V _{ID} = 0.4 V		0.01	5	^	
l IIL	_ow-level input current (into IN+)	V _{ID} = -7 V		-1	-100	μΑ	
V _{O(on)}	On-stage output voltage	$I_{O} = 50 \text{ mA},$	V _{ID} = 2 V	-1.6	-2.1	V	
	Off-stage output current VO = VBAT-	VO = VBAT-	V _{ID} = 0.8 V	-2	-100		
IO(off)			Inputs open	-2	-100	μΑ	
I _R	Clamp diode reverse current	V _O = 0		2	100	μΑ	
VOK	Output clamp voltage	I _O = 50 mA		0.9	1.2	V	
		$I_0 = -50 \text{ mA},$	$V_{BAT-} = 0$	-0.9	-1.2	V	
I _{BAT(on)}	On-state battery current	All drivers on		-2	-4.4	mA	
I _{BAT} (off)	Off-state battery current	All drivers off		-1	-100	μΑ	

[‡] All typical values are at T_A = 25°C.



switching characteristics V_{BAT-} = -52 V, T_A = 25°C

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
ton	Turn-on time	V _{ID} = 3-V pulse,	$R_L = 1 k\Omega$,		1	10	μs
toff	Turn-off time	L = 1 H,	See Figure 2		1	10	μs

PARAMETER MEASUREMENT INFORMATION

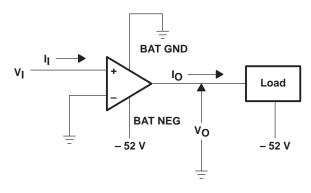


Figure 1. Generalized Test Circuit, Each Driver

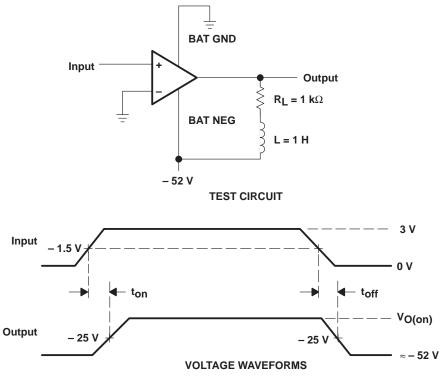


Figure 2. Test Circuit and Voltage Waveforms, Each Driver

APPLICATION INFORMATION

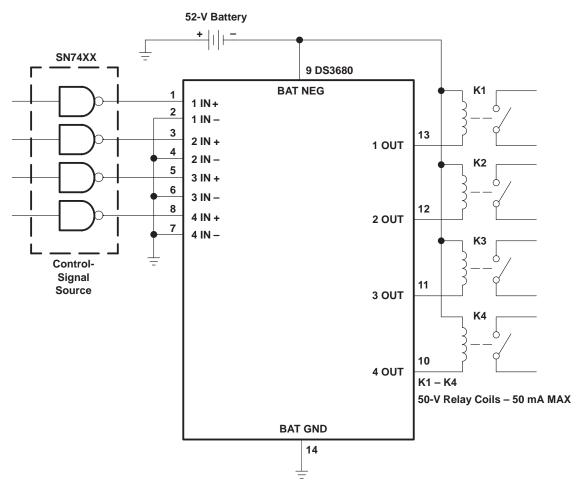


Figure 3. Relay Driver

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