

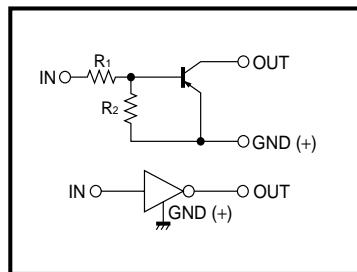
# Digital transistors (built-in resistors)

## DTA115EM / DTA115EE / DTA115EUA / DTA115EKA / DTA115ESA

### ●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on/off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

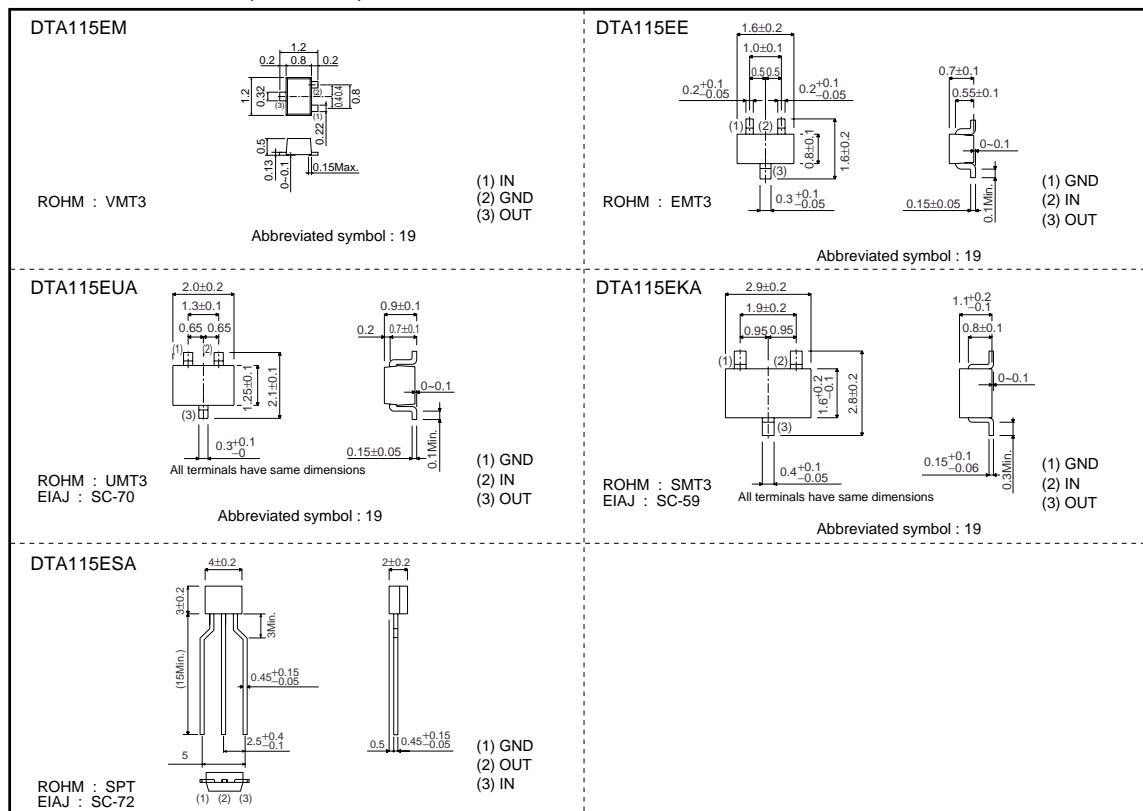
### ●Equivalent circuit



### ●Structure

PNP digital transistor (Built-in resistor type)

### ●External dimensions (Units : mm)



## Transistors

# DTA115EM / DTA115EE / DTA115EUA

# DTA115EKA / DTA115ESA

### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>I</sub>	-40~+10	V
Output current	I <sub>O</sub>	-20	mA
Power dissipation	I <sub>C(MAX.)</sub>	-100	
	P <sub>D</sub>	150	
		200	mW
DTA115ESA		300	
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature	T <sub>STG</sub>	-55~+150	°C

### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(OFF)</sub>	—	—	-0.5	V	V <sub>CC</sub> =-5V, I <sub>O</sub> =-100μA
	V <sub>I(ON)</sub>	-3	—	—		V <sub>O</sub> =-0.3V, I <sub>O</sub> =-1mA
Output voltage	V <sub>O(ON)</sub>	—	-0.1	-0.3	V	I <sub>O</sub> =-5mA, I <sub>E</sub> =-0.25mA
Input current	I <sub>I</sub>	—	—	-0.15	mA	V <sub>I</sub> =-5V
Output current	I <sub>O(OFF)</sub>	—	—	-0.5	μA	V <sub>CC</sub> =-50V, V <sub>O</sub> =0V
DC current gain	G <sub>I</sub>	82	—	—		I <sub>O</sub> =-5mA, V <sub>O</sub> =-5V
Input resistance	R <sub>I</sub>	70	100	130	kΩ	—
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	—	—
Transition frequency	f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> =10V, I <sub>E</sub> =-5mA, f=100MHz

\* Transition frequency of the device.

### ●Package, marking, and packaging specifications

Type	DTA115EM	DTA115EE	DTA115EUA	DTA115EKA	DTA115ESA
Package	VMT3	EMT3	UMT3	SMT3	SPT
Marking	19	19	19	19	—
Packaging code	T2L	TL	T106	T146	TP
Basic ordering unit (pieces)	8000	3000	3000	3000	5000