

DM74LS386 Quad 2-Input Exclusive-OR Gates

General Description

This device contains four independent gates each of which performs the logic exclusive-OR function.

Connection Diagram

Dual-In-Line Package V_{CC} 4B 4A 4Y 3Y 3B 3A 14 13 12 11 10 9 8 1 2 3 4 5 6 7 1A 1B 1Y 2Y 2A 2B GND

Order Number DM74LS386N See NS Package Number N14A

Function Table

$$\mathbf{Y} = \mathbf{A} \oplus \mathbf{B} = \overline{\mathbf{A}}\mathbf{B} + \mathbf{A}\overline{\mathbf{B}}$$

Inputs		Output		
Α	В	Υ		
L	L	L		
L	Н	Н		
Н	L	Н		
Н	Н	L		

$$\begin{split} \mathsf{H} &= \mathsf{High} \; \mathsf{Logic} \; \mathsf{Level} \\ \mathsf{L} &= \; \mathsf{Low} \; \mathsf{Logic} \; \mathsf{Level} \end{split}$$

TL/F/11321-1

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 7V Storage Temperature Range -65°C to $+150^{\circ}\text{C}$

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device can not be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter		Units		
	T di diffeter	Min	Nom	Max	Office
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			V
V_{IL}	Low Level Input Voltage			0.8	V
I _{OH}	High Level Output Current			-0.4	mA
l _{OL}	Low Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics over recommended operating free air temperature unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max,$ $V_{IL} = Max, V_{IH} = Min$	2.7	3.4		V
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max,$ $V_{IL} = Max, V_{IH} = Min$		0.35	0.5	V
		$I_{IL} = 4 \text{ mA}, V_{CC} = \text{Min}$		0.25	0.4	
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.2	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$			40	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.8	mA
Ios	Short Circuit Output Current	V _{CC} = Max (Note 2)	-20		-100	mA
Icc	Supply Current with Outputs Low	V _{CC} = Max, (Note 3)			10	mA

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

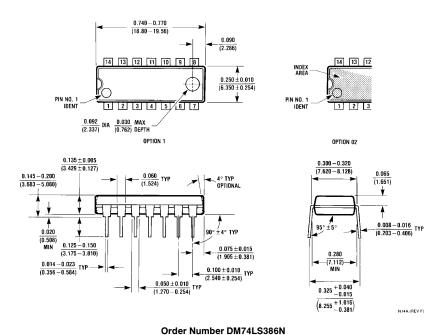
Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 3: I_{CC} is measured with all outputs open, and all inputs grounded.

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

	Parameter	Conditions	$R_L = 2 k\Omega$ $C_L = 50 pF$		Units
Symbol					
			Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output	Other Input Low		23	ns
t _{PHL}	Propagation Delay Time High to Low Level Output			17	ns
t _{PLH}	Propagation Delay Time Low to High Level Output	Other Input High		30	20
t _{PHL}	Propagation Delay Time High to Low Level Output			22	ns

Physical Dimensions inches (millimeters)



NSC Package Number N14A

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