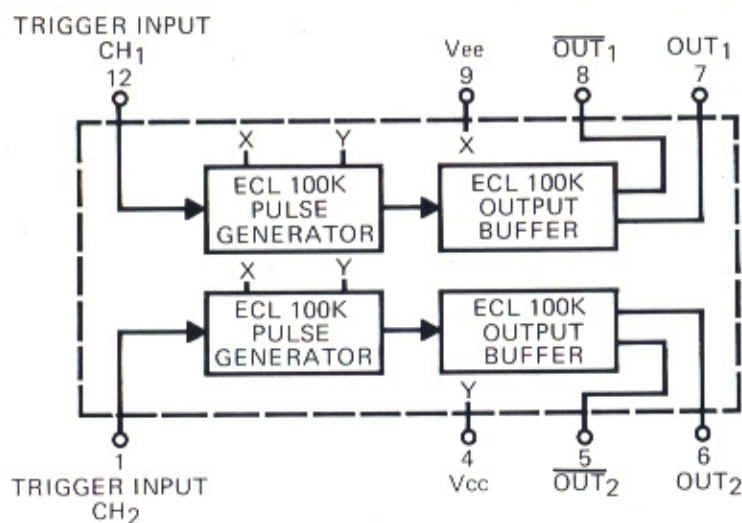
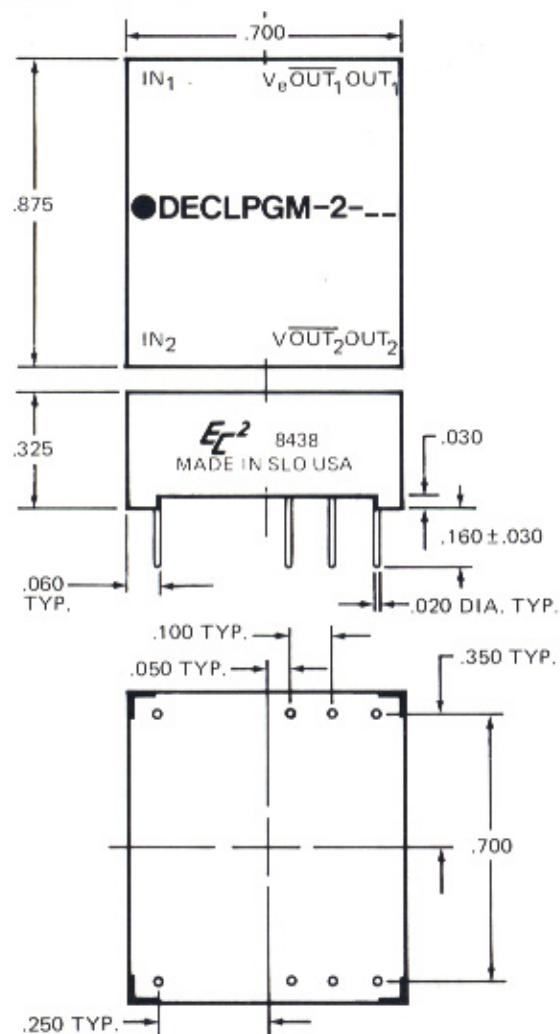


BLOCK DIAGRAM IS SHOWN BELOW



MECHANICAL DETAIL IS SHOWN BELOW



TEST CONDITIONS

1. All measurements are made at 25°C.
2. Vee supply voltage is maintained at -4.5V DC.
3. All units are tested using a positive input pulse provided by a standard open emitter ECL 100K gate. The input and output utilizes a 50 ohm pull-down resistor to -2V; the output is also loaded with one ECL 100K gate.
4. Input pulse width used is 2ns for all modules; repetition rate is in accordance with the data specified in the Part Number Table.

OPERATING SPECIFICATIONS

- *Supply Voltage: -4.5V ±5% to Vee
- Supply Current: 155ma typical
- Logic 1 Input at 25°C.
 - Voltage -1.165V min.
 - Current 350ua max.
- Logic 0 Input at 25°C.
 - Voltage -1.475V max.
 - Current5ua min.
- Logic 1 Output at 25°C. -1.025V min.
- Logic 0 Output at 25°C. -1.620V max.
- Operating temperature range: 0 to +85°C.
- Storage temperature: -55 to +125°C.

- * Pulse width increases or decreases approximately 1.5% for a respective increase or decrease of 5% in supply voltage.

PART NUMBER TABLE

Part Number	Pulse Width (in ns)	Maximum Pulse Rate (in Mhz)
DECLPGM-2-2	2 ±.2	200
DECLPGM-2-3	3 ±.3	133
DECLPGM-2-4	4 ±.4	100
DECLPGM-2-5	5 ±.5	80
DECLPGM-2-6	6 ±.6	66
DECLPGM-2-7	7 ±.7	57
DECLPGM-2-8	8 ±.8	50
DECLPGM-2-9	9 ±.9	44
DECLPGM-2-10	10 ±1	40
DECLPGM-2-12	12 ±1	33
DECLPGM-2-14	14 ±1	28
DECLPGM-2-16	16 ±1	25
DECLPGM-2-18	18 ±1	22
DECLPGM-2-20	20 ±1	20
DECLPGM-2-25	25 ±1	16
DECLPGM-2-30	30 ±1	13
DECLPGM-2-35	35 ±1	11
DECLPGM-2-40	40 ±1	10
DECLPGM-2-45	45 ±1	9
DECLPGM-2-50	50 ±1	8

Special modules can be readily manufactured to improve accuracies and/or provide customer specified random pulse widths for specific applications.