

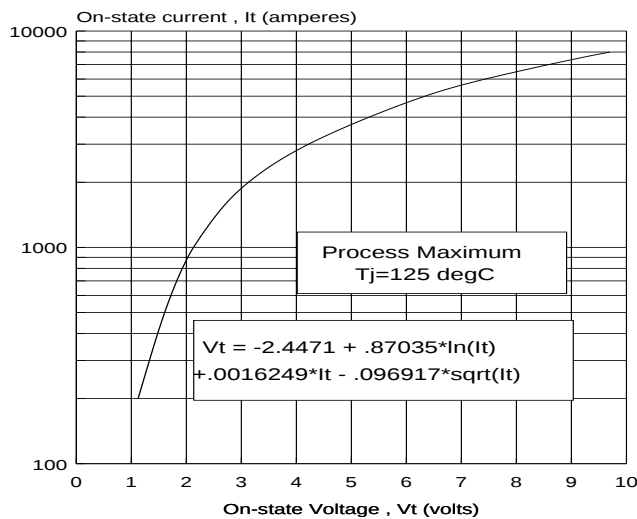
# SPCO

## C718 53mm / 5.0 kV THYRISTOR

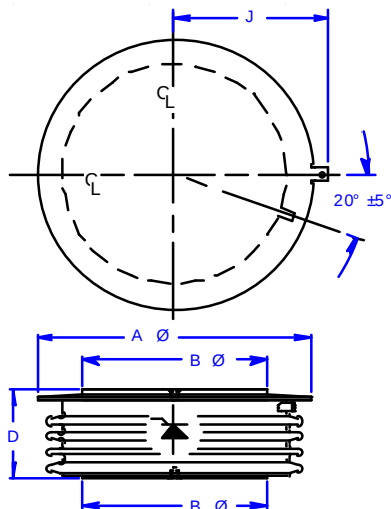
Type C718 thyristor is suitable for phase control applications such as HVDC valves, static VAR compensators and synchronous motor drives.

The silicon junction is manufactured by the proven multi-diffusion process and is supplied in an industry standard disc-type package, ready to mount to forced or naturally cooled heat dissipators using commercially available mechanical clamping hardware.

C718 Thyristor  
ON-STATE CHARACTERISTIC



### MECHANICAL OUTLINE



$A\Phi = 2.96 \text{ in (75.2 mm)}$   
 $B\Phi = 1.90 \text{ in (48.3 mm)}$   
 $D = 1.07 \text{ in (27.2 mm)}$

### PRINCIPAL RATINGS AND CHARACTERISTICS

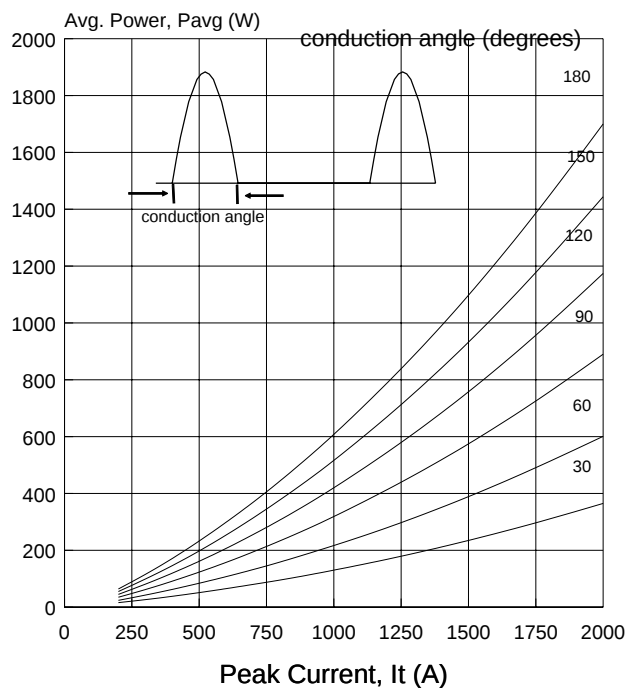
|                                            |                        |                                                                 |                              |            |
|--------------------------------------------|------------------------|-----------------------------------------------------------------|------------------------------|------------|
| Repetitive peak off-state & reverse volts  | $V_{DRM}$<br>$V_{RRM}$ | $T_j=0$<br>to $125^\circ\text{C}$                               | up to<br>5000                | V          |
| Repetitive working crest voltage           | $V_{DWM}$<br>$V_{DRM}$ | $T_j=0$<br>to $125^\circ\text{C}$                               | $0.8V_{DRM}$<br>$0.8V_{RRM}$ |            |
| Off-state & reverse leakage current        | $I_{DWM}$<br>$I_{RWM}$ | $T_j=0$<br>to $125^\circ\text{C}$                               | 75<br>75                     | ma         |
| Average on-state current                   | $I_{T(AV)}$            | $T_{case} = 70^\circ\text{C}$                                   | 750                          | A          |
| Peak half-cycle non-rep surge current      | $I_{TSM}$              | 60 Hz<br>50 Hz                                                  | 7<br>6.5                     | kA         |
| On-state voltage                           | $V_{TM}$               | $I_T=1\text{kA}$<br>$t_p=8\text{ms}$<br>$T_j=125^\circ\text{C}$ | 2.1                          | V          |
| Critical rate of rise of on-state current  | $di/dt$<br>rep         | $T_j=125^\circ\text{C}$<br>60 Hz                                | 75                           | A/us       |
| Critical rate of rise of off-state voltage | $dv/dt$                | $T_j=125^\circ\text{C}$<br>$V_D=.67V_{DRM}$                     | 1000                         | V/us       |
| Recovery current                           | $I_{RM}$               | $T_j=125^\circ\text{C}$<br>2A/us<br>5A/us                       | 60<br>100                    | A          |
| Turn-on delay                              | $t_d$                  | $V_d=.5V_{DRM}$                                                 | 3                            | us         |
| Turn-off time                              | $T_{off}$              | 5A/us, -100V<br>20V/us to 2000V                                 | 500                          | us         |
| Thermal resistance                         | $R_{thJC}$             |                                                                 | .025                         | c/w        |
| Externally applied clamping force          | F                      |                                                                 | 5500<br>24.5                 | lbs.<br>kN |

### REPETITIVE PEAK REVERSE AND OFF-STATE BLOCKING VOLTAGE

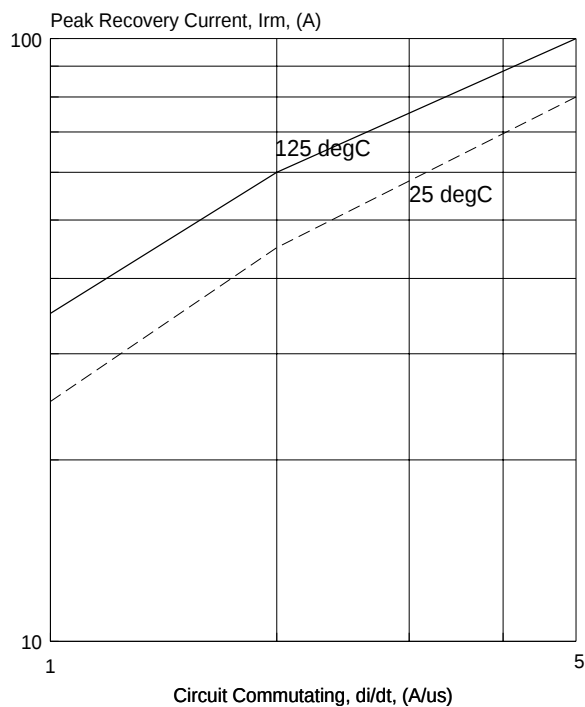
$T_j = 0 \text{ to } 125^\circ\text{C}$

| MODEL  | $V_{DRM}$<br>(volts) | $V_{RRM}$<br>(volts) |
|--------|----------------------|----------------------|
| C718EP | 5000                 | 5000                 |
| C718DT | 4900                 | 4900                 |
| C718DN | 4800                 | 4800                 |
| C718DS | 4700                 | 4700                 |
| C718DM | 4600                 | 4600                 |
| C718DE | 4500                 | 4500                 |

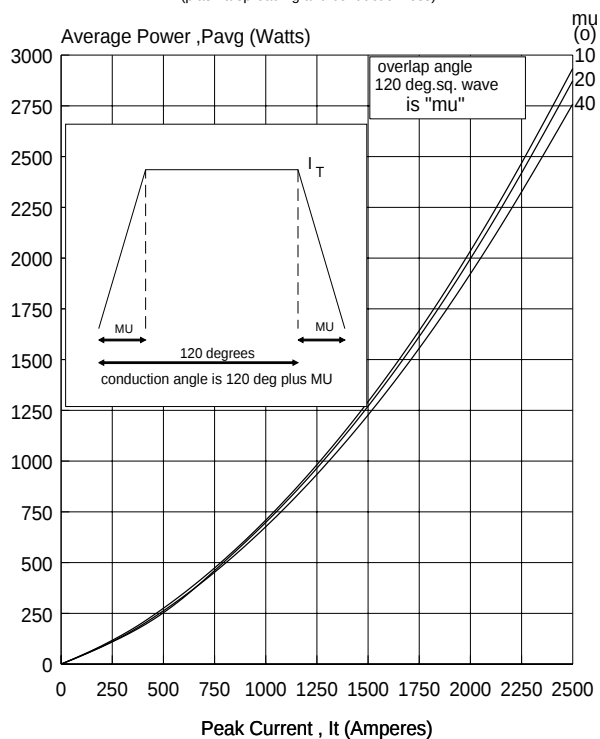
Full Cycle Average Power Loss  
per C718 Thyristor



MAXIMUM PEAK RECOVERY CURRENT  
versus COMMUTATING  $di/dt$



FULL CYCLE AVERAGE POWER LOSS  
versus  
PEAK CURRENT at 50/60 Hz  
(plasma spreading and conduction loss)



## GATE SUPPLY REQUIREMENTS

|                       |       |
|-----------------------|-------|
| Open circuit voltage  | 30 V  |
| Short circuit current | 3 A   |
| - rise time           | 0.5us |
| Pulse duration (min)  | 20 us |