BU9877AFV <Under development>

Description

The BU9877AFV is a 2k bit EEPROM memory with write-protect function, developed for a DIMM(Dual Inline memory Module) expansion memory module that uses syncronous DRAM memory.
In order for the Plug & Play feature of Windows 95 to operate, the IC reads ID of components automatically.

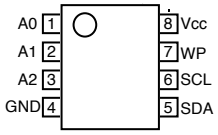
Features

- 2k bit serial EEPROM organized as 256 × 8bit
- SPD data format
- I^2C Bus™ interface compatible
- One-time ROM and write protect mode
- Page write function : 16byte
- Address auto-increment function during read operation
- Low current consumption
  - Active : MAX 3mA (Vcc=5.5V)
  - Standby : MAX 2μA (Vcc=5.5V)
- Small packages of SSOP-B8 pin
- Address auto-increment function during read operation
- 100,000 write cycle typical
- 10 years data retention

Applications

Synchronous DRAM modules ; 168-pin DIMM for desktop PCs and 144-pin DIMM for notebook PCs

Pin Configuration

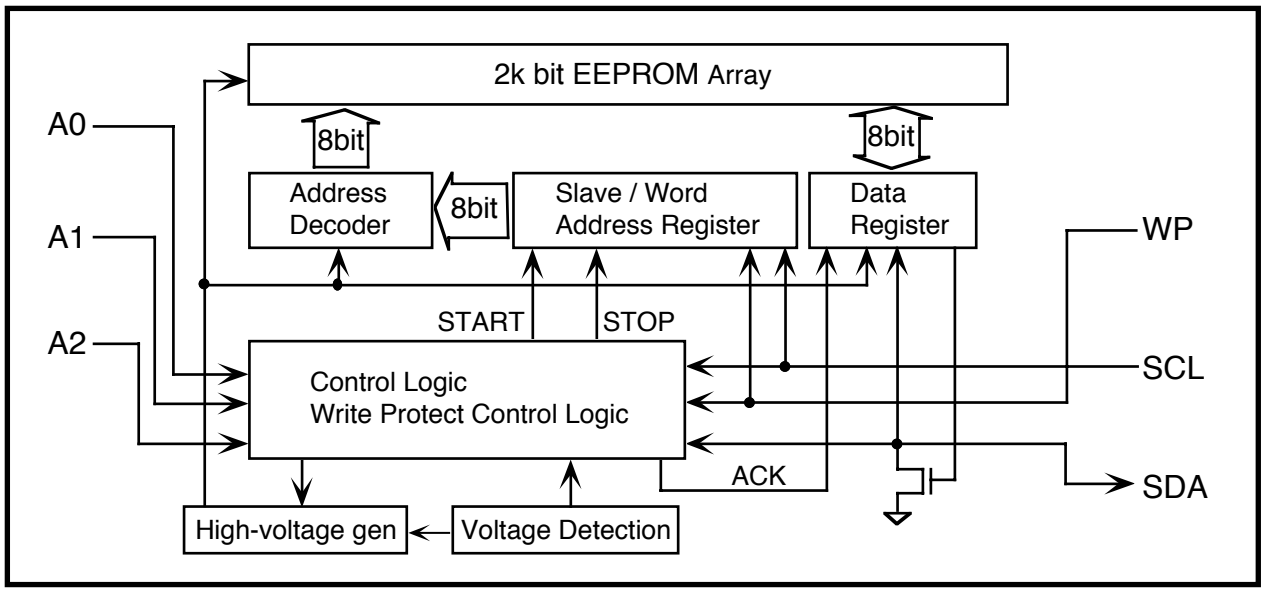


SSOP-B8

Pin Functions

Pin Names	Functions
A0, A1, A2	Slave Address Inputs
GND	Ground
SDA	Slave / Word Address Serial Data Input/Output
SCL	Serial Data Clock
WP	Write Protect
Vcc	Power Supply

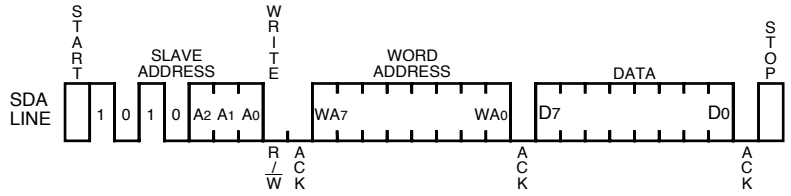
Block Diagram



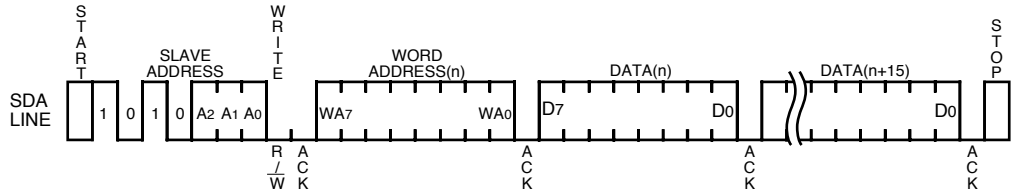
Plug & Play Feature Applicable Serial Interface IC for DIMM

Timing chart

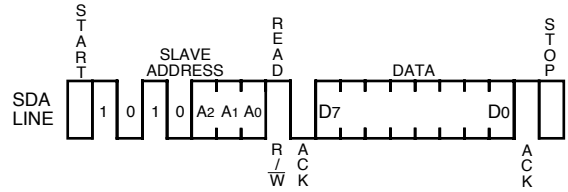
■ Byte write cycle



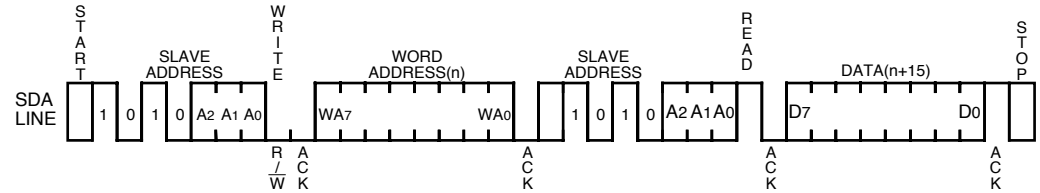
■ Page write cycle



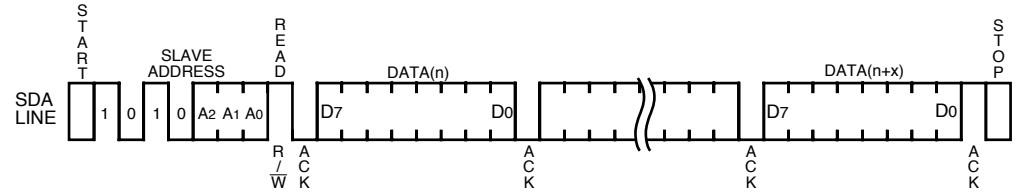
■ Current read cycle



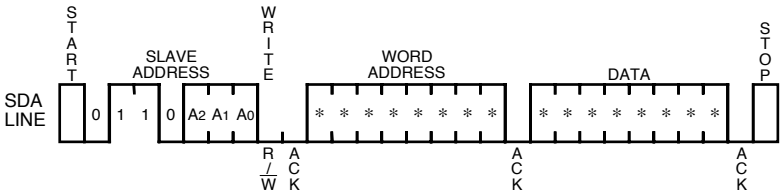
■ Random read cycle



■ Sequential read cycle



■ Write protect cycle



Note : BR9877AFV is a double-cell.