

BR93LC56-W/F-W/RF-W/FJ-W/RFJ-W/FV-W BR93LC66-W/F-W/RF-W/FJ-W/RFJ-W/FV-W

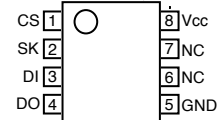
<Under development>

Features

- 2k bit serial EEPROM organized as $128 \times 16\text{bit}$ (BR93LC56)
4k bit serial EEPROM organized as $256 \times 16\text{bit}$ (BR93LC66)
- Low voltage operation
Read : 2.0~5.5V
Write : 2.7~5.5V
- Low current consumption
Active : 2mA MAX ($V_{cc}=3V$)
Standby : 3 μ A MAX ($V_{cc}=3V$)
- Clock frequency : 1MHz MAX ($V_{cc}=5V$)
250kHz MAX ($V_{cc}=3V$)
- Write cycle time : 25ms MAX ($V_{cc}=3V$)
10ms MAX ($V_{cc}=5V$)
- Address auto-increment function during write operation
- Automatic erase-before-write function during write operation
- Prevent inadvertent writing
Defaults to power up with write-disabled state
Software instructions for write-enable/disable
Write inhibit at low V_{cc}
- READY/ $\overline{\text{BUSY}}$ Status indicator function (DO pin)
- Schmitt trigger circuit & noise filter are built into SK, DI pin.
- TTL compatible input/output
- 100,000 write cycle typical
- 10 years data retention
- Operating temperature range : -40~85°C

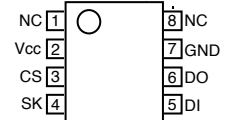
Pin Configurations

BR93LC56-W/
RF-W/RFJ-W
BR93LC66-W/
RF-W/RFJ-W



DIP8/SOP8/SOP-J8

BR93LC56F-W/
FJ-W/FV-W
BR93LC66F-W/
FJ-W/FV-W

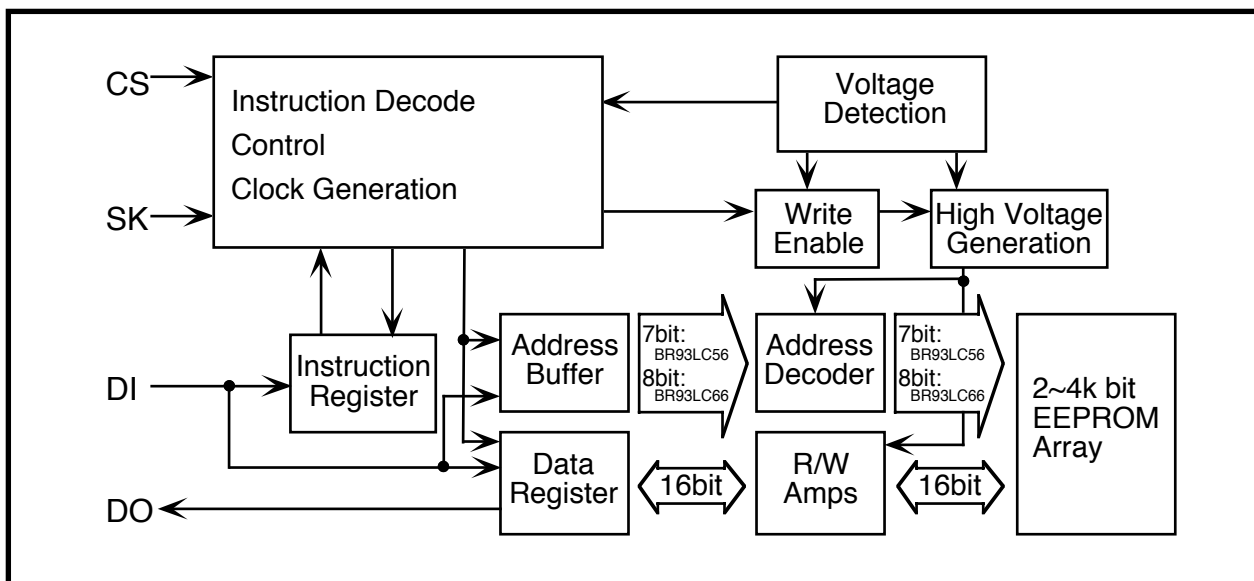


SOP8/SOP-J8/SSOP-B8

Pin Functions

Pin Names	Functions
CS	Chip Select
SK	Serial Data Clock
DI	Serial Data Input
DO	Serial Data Output
GND	Ground
Vcc	Power Supply

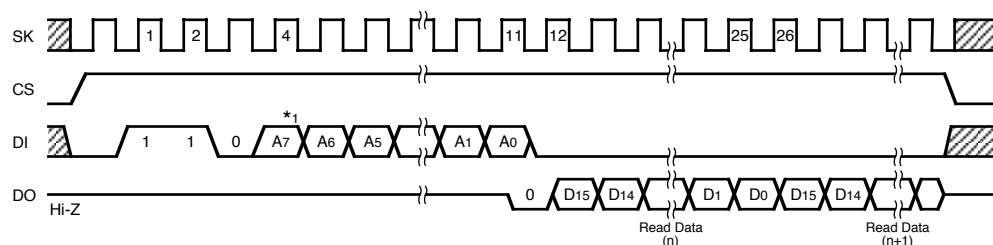
Block Diagram



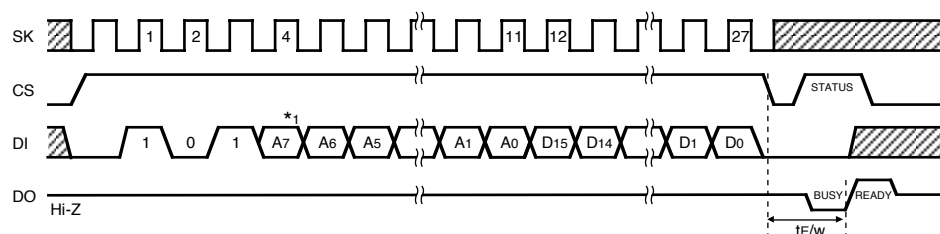
Serial 3 Wire

Timing chart

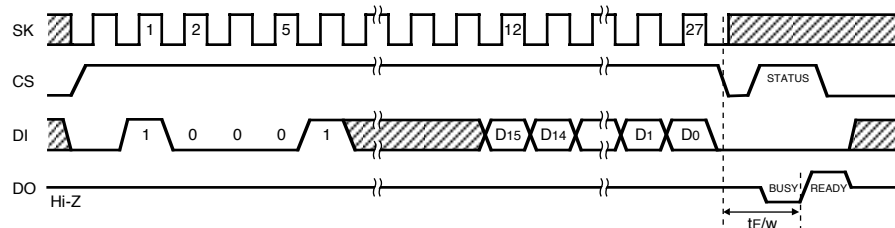
Read cycle



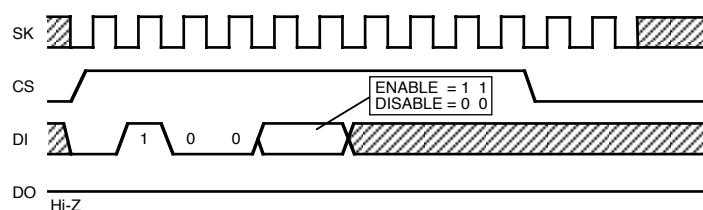
Write cycle



Address writing cycle



Writing enable / disabled cycle



*1: A7=0 (BR93LC56)

Note : Single-cell types (BR93LC56/F/RF/FJ/RFJ/FV, BR93LC66/F/RF/FJ/RFJ/FV).
"-W" means double-cell type.)