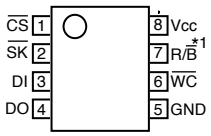
BR9010-W/F-W/FV-W <Under development>
BR9020-W/F-W/FV-W <Under development>
BR9040-W/F-W/FV-W <Under development>

Features

- 1k bit serial EEPROM organized as 64 × 16bit (BR9010)
  2k bit serial EEPROM organized as 128 × 6bit (BR9020)
  4k bit serial EEPROM organized as 256 × 16bit (BR9040)
- Low operating voltage
  Read : 2.0~5.5V
  Write : 2.7~5.5V
- Low current consumption
  Active : 1.5mA MAX (Vcc=3V), 2mA MAX (Vcc=5V)
  Standby : 2μA MAX (Vcc=3V), 3μA MAX (Vcc=5V)
- Clock frequency : 1MHz MAX(Vcc=3V, 5V)
- Write cycle time : 15ms MAX (Vcc=3V), 10ms MAX (Vcc=5V)
  BR9040 ; 10ms MAX (Vcc=3V, 5V)
- Automatic erase-before-write function during write operation
- Prevent inadvertent writing
  Defaults to power up with write-disabled state
  Software instructions for write-enable/disable
  Write inhibit at low Vcc
  $\overline{WC}$ pin to write protection
- READY/$\overline{BUSY}$ Status indicator function (R/$\overline{B}$ pin, DO pin)
- Schmitt trigger circuit & noise filter are built into $\overline{CS}$, $\overline{SK}$, DI pin
- 100,000 write cycle typical
- 10 years data retention
- Operating temperature range : -40~85˚C

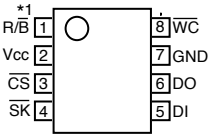
Pin Configurations

BR9010-W
BR9020-W
BR9040-W



DIP8

BR9010F-W/FV-W
BR9020F-W/FV-W
BR9040F-W/FV-W



SOP8/SSOP-B8

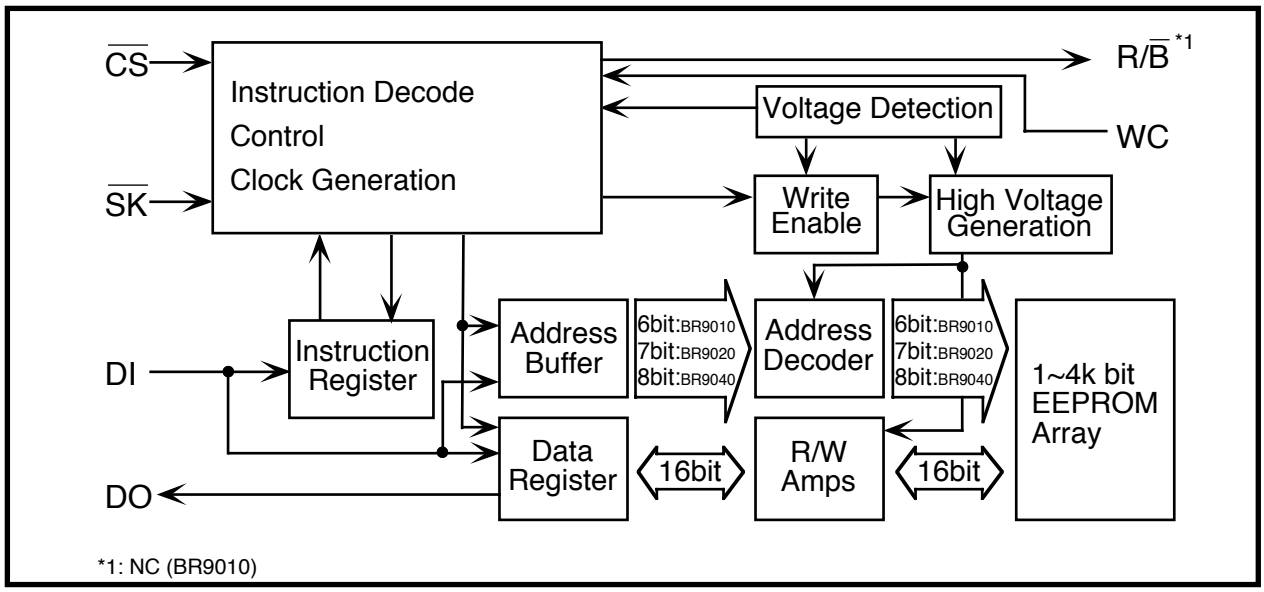
*1: NC (BR9010)

Pin Functions

Pin Names	Functions
$\overline{CS}$	Chip Select
$\overline{SK}$	Serial Data Clock
DI	Serial Data Input
DO	Serial Data Output
GND	Ground
$\overline{WC}$	Write Control Input
R/$\overline{B}$	READY/$\overline{BUSY}$ Status Signal Output
Vcc	Power Supply

*1: NC (BR9010)

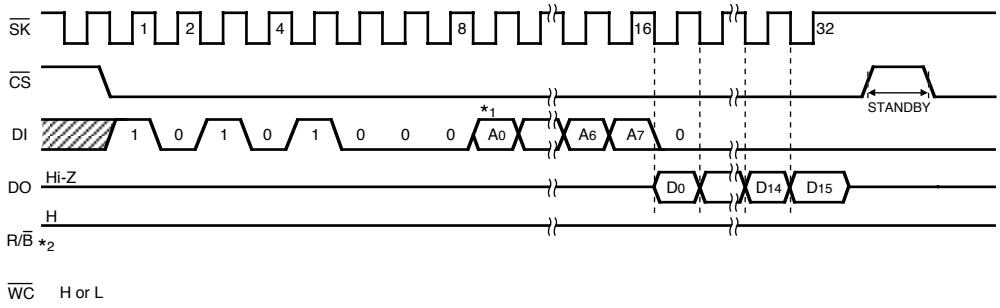
Block Diagram



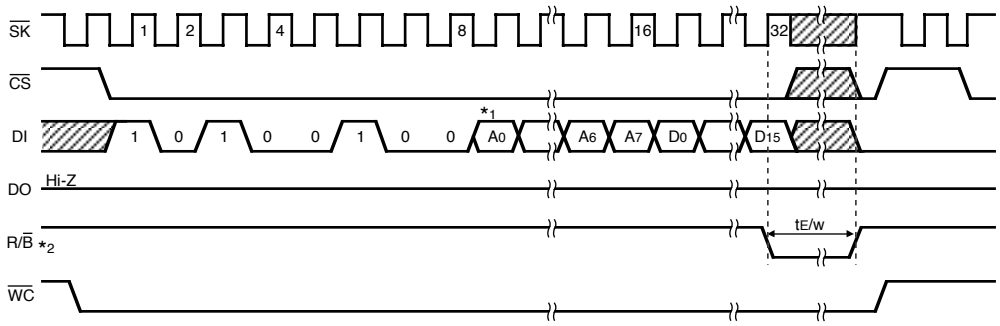
*1: NC (BR9010)

Timing chart

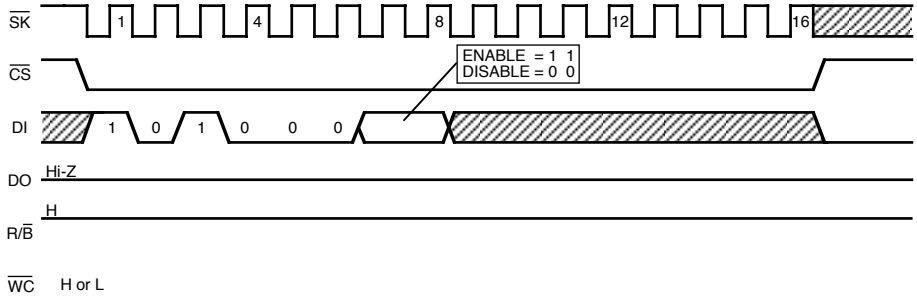
■ Read cycle



■ Write cycle



■ Writing enable / desabled cycle



*1 : BR9010:A6, A7=0
: BR9020:A7=0
*2 : NC (BR9010)

Note : Single-cell types (BR9010/F/FV, BR9020/F).
"-W" means double-cell type.