



# SAW Components

Data Sheet B4871

Data Sheet

A large, stylized, 3D-rendered graphic of the word "EPCOS" in a light gray, sans-serif font. The letters are slightly tilted and appear to be floating or emerging from a dark, textured background that resembles a globe or a complex circuit board pattern.



## SAW Components

B4871

## Low Loss Filter for Mobile Communication

135,54 MHz

### Data Sheet



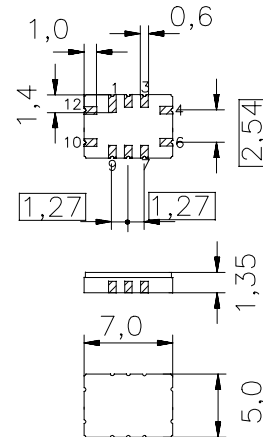
#### Features

- Low-loss IF filter for mobile telephone
- Channel selection in AMPS / D-AMPS systems
- Filter surface passivated
- Balanced or unbalanced operation possible
- Package for **Surface Mounted Technology (SMT)**

#### Terminals

- Ni, gold plated

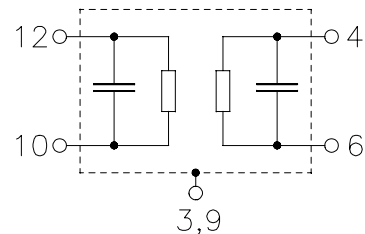
SMD ceramic package **QCC12B**



Dimensions in mm, approx. weight 0,2 g

#### Pin configuration

12	Input
6	Output
10	Balanced input or input ground
4	Balanced output or output ground
3,9	Case ground
1,2,7,8	Not connected



Type	Ordering code	Marking and Package according to	Packing according to
B4871	B39141-B4871-Z910	C61157-A7-A52	F61074-V8038-Z000

Electrostatic Sensitive Device (ESD)

#### Maximum ratings

Operable temperature range	$T$	- 30/+ 85	°C
Storage temperature range	$T_{stg}$	- 40/+ 85	°C
DC voltage	$V_{DC}$	13	V
Source power	$P_s$	10	dBm



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#### Characteristics

Operating temperature range:	$T = -30^{\circ}\text{C} \dots 85^{\circ}\text{C}$
Terminating source impedance:	$Z_S = 850\ \Omega \parallel -0,8\ \text{pF}$
Terminating load impedance:	$Z_L = 850\ \Omega \parallel -0,8\ \text{pF}$

		min.	typ.	max.	
<b>Nominal frequency</b>	$f_N$	—	135,54	—	MHz
<b>3 dB Bandwidth</b>		—	58	—	kHz
<b>Minimum insertion attenuation</b> (including losses in the matching network)	$\alpha_{\min}$	—	4,2	5,3	dB
<b>Amplitude ripple (p-p)</b>	$\Delta\alpha$				
$f_N - 10,0\ \text{kHz} \dots f_N + 10,0\ \text{kHz}$		—	0,2	1,5	dB
$f_N - 13,0\ \text{kHz} \dots f_N + 13,0\ \text{kHz}$		—	0,5	3,0	dB
<b>Group delay ripple (p-p)</b>	$\Delta\tau$				
$f_N - 13,0\ \text{kHz} \dots f_N + 13,0\ \text{kHz}$		—	2,0	10,0	$\mu\text{s}$
<b>Relative attenuation (relative to <math>\alpha_{\min}</math>)</b>	$\alpha_{\text{rel}}$				
$f_N \pm 60,0\ \text{kHz} \dots f_N \pm 120,0\ \text{kHz}$		14	24	—	dB
$f_N \pm 120,0\ \text{kHz} \dots f_N \pm 240,0\ \text{kHz}$		40	50	—	dB
$f_N \pm 240,0\ \text{kHz} \dots f_N \pm 1000,0\ \text{kHz}$		50	56	—	dB
$f_N \pm 330,0\ \text{kHz}$		55	65	—	dB
$f_N \pm 660,0\ \text{kHz}$		55	70	—	dB
<b>Impedance within the passband</b>					
Input: $Z_{\text{IN}} = R_{\text{IN}} \parallel C_{\text{IN}}$		—	850 $\parallel$ 0,8	—	$\Omega \parallel \text{pF}$
Output: $Z_{\text{OUT}} = R_{\text{OUT}} \parallel C_{\text{OUT}}$		—	850 $\parallel$ 0,8	—	$\Omega \parallel \text{pF}$
<b>Temperature coefficient of frequency <sup>1)</sup></b>	$TC_f$	—	-0,034	—	ppm/K <sup>2</sup>
<b>Turnover temperature</b>	$T_0$	—	25	—	$^{\circ}\text{C}$

<sup>1)</sup> Temperature dependance of  $f_c$ :  $f_c(T) = f_c(T_0)(1 + TC_f(T - T_0)^2)$



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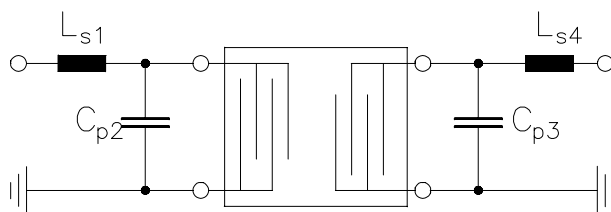
135,54 MHz

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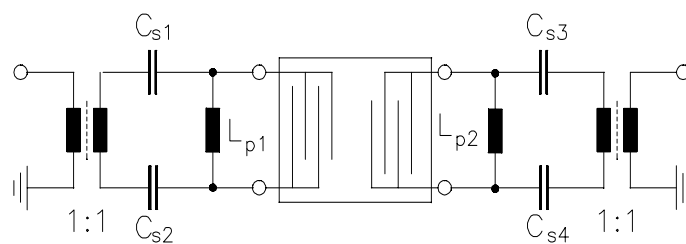
**Test matching networks to 50  $\Omega$**  (element values depend on pcb layout)

a) Unbalanced - unbalanced matching network



Input: Pin 12  
Output: Pin 6  
 $L_{s1} = 220 \text{ nH}$   
 $C_{p2} = 3,3 \text{ pF}$   
 $C_{p3} = 3,3 \text{ pF}$   
 $L_{s4} = 220 \text{ nH}$

b) Balanced - balanced matching network



Input: Pins 12 and 10  
Output: Pins 6 and 4  
 $C_{s1} = 15 \text{ pF}$   
 $C_{s2} = 12 \text{ pF}$   
 $L_{p1} = 150 \text{ nH}$   
 $L_{p2} = 150 \text{ nH}$   
 $C_{s3} = 15 \text{ pF}$   
 $C_{s4} = 12 \text{ pF}$

### Note :

The balanced - balanced network is realized using TOKO 1:1 baluns B5FL. The insertion attenuation of each balun is 0,6 dB at  $f_N$ . The loss of the baluns is not included in the specified filter insertion attenuation. S-Parameters of the TOKO 1:1 balun B5FL are available on request.

The level of ultimate suppression may be limited by electromagnetic feedthrough depending on the layout of the pcb and the arrangement of the matching components.

The above mentioned characteristics can be realized either in balanced or in unbalanced mode of operation.

For more details see our application note *PCB Layout for Highly Selective IF Filters*.



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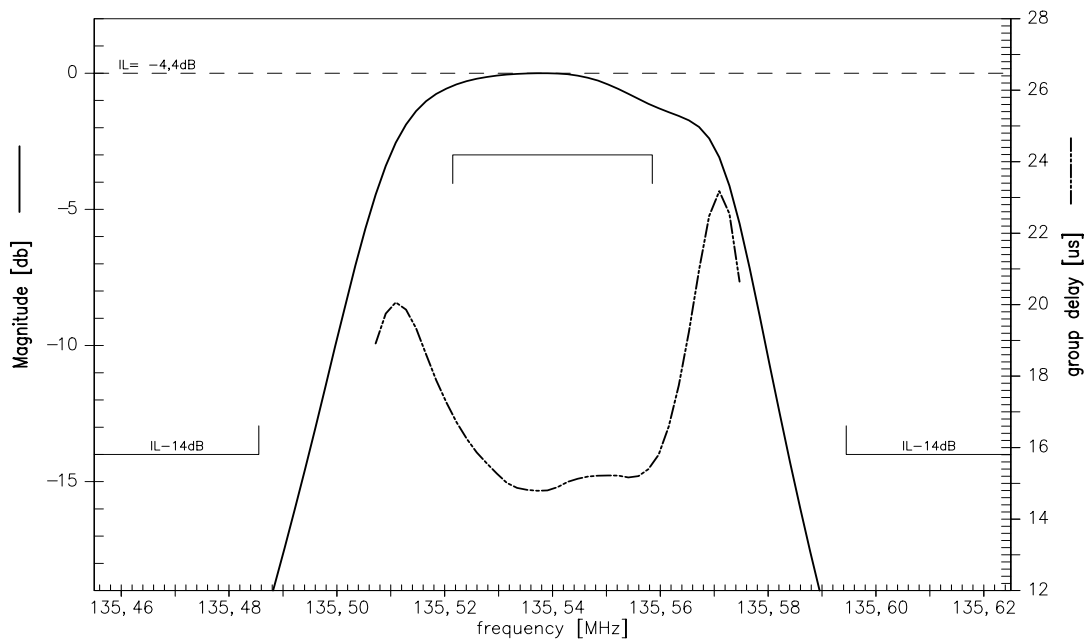
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135,54MHz

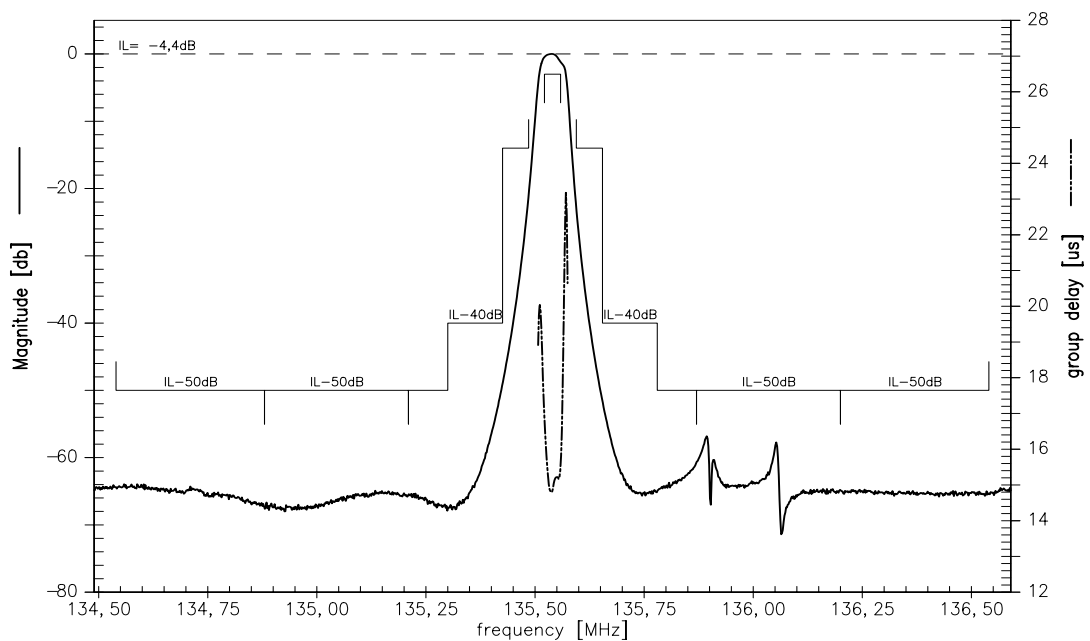
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**Normalized transfer function passband** (measured single ended - single ended)



**Normalized transfer function wideband** (measured single ended - single ended)





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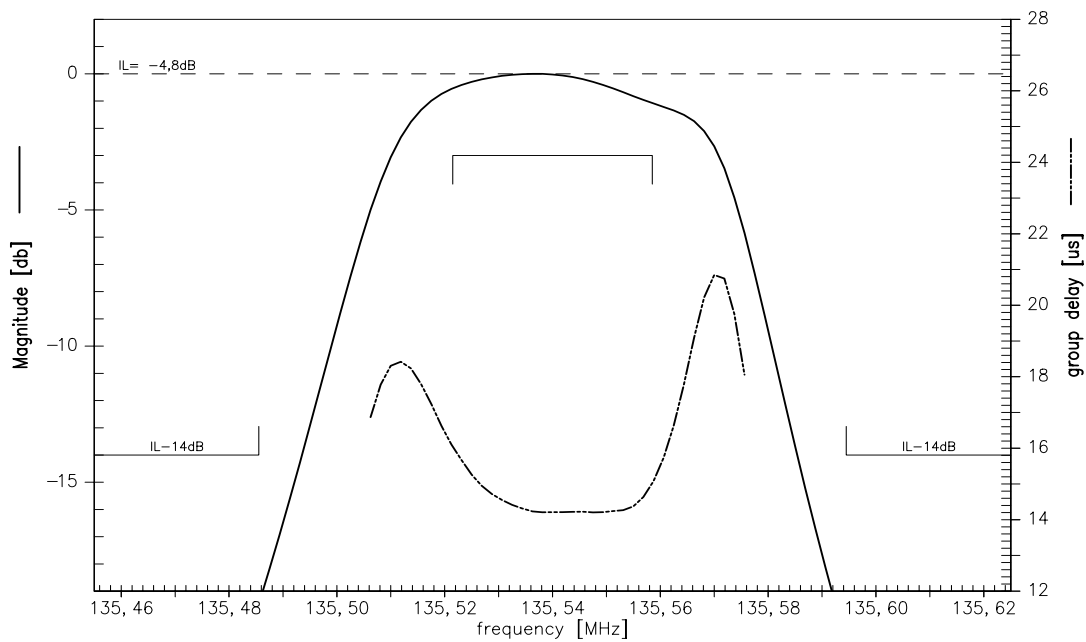
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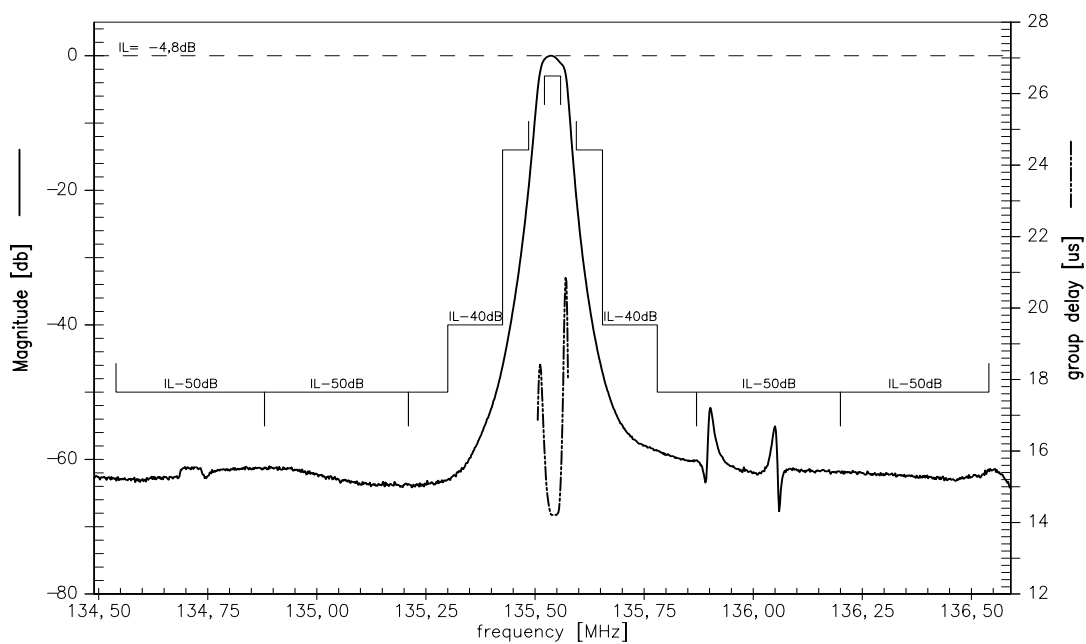
Data Sheet



**Normalized transfer function passband (measured balanced - balanced)**



**Normalized transfer function wideband (measured balanced - balanced)**





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<b>Low Loss Filter for Mobile Communication</b>	<b>135,54MHz</b>
<b>Data Sheet</b>	<b>SMD</b>

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