

# **Switching Diode**

# BAS16HT1

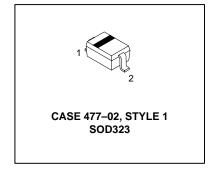
**ON Semiconductor Preferred Device** 

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	٧ <sub>R</sub>	75	Vdc
Peak Forward Current	ΙF	200	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc

# THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board,*  TA = 25°C  Derate above 25°C	PD	200 1.57	mW mW/°C
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	635	°C/W
Junction and Storage Temperature	TJ, T <sub>stg</sub>	150	°C



1 O 2 CATHODE ANODE

#### **DEVICE MARKING**

BAS16HT1 = A6

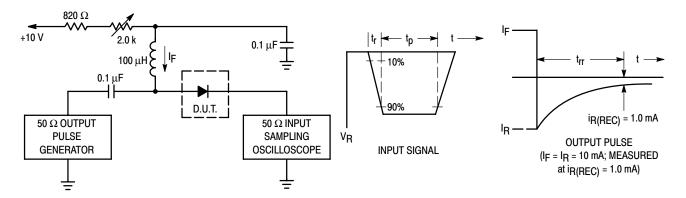
# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	<u>.</u>			
Reverse Voltage Leakage Current ( $V_R = 75 \text{ Vdc}$ ) ( $V_R = 75 \text{ Vdc}$ , $T_J = 150 ^{\circ}\text{C}$ ) ( $V_R = 25 \text{ Vdc}$ , $T_J = 150 ^{\circ}\text{C}$ )	I <sub>R</sub>	_ _ _	1.0 50 30	μAdc
Reverse Breakdown Voltage (I <sub>BR</sub> = 100 μAdc)	V(BR)	75	_	Vdc
Forward Voltage (IF = 1.0 mAdc) (IF = 10 mAdc) (IF = 50 mAdc) (IF = 150 mAdc)	VF	_ _ _ _	715 855 1000 1250	mV
Diode Capacitance (V <sub>R</sub> = 0, f = 1.0 MHz)	C <sub>D</sub>	_	2.0	pF
Forward Recovery Voltage (IF = 10 mAdc, $t_{\Gamma}$ = 20 ns)	VFR	_	1.75	Vdc
Reverse Recovery Time (IF = IR = 10 mAdc, RL = 50 $\Omega$ )	t <sub>rr</sub>	_	6.0	ns
Stored Charge (IF = 10 mAdc to $V_R$ = 5.0 Vdc, $R_L$ = 500 $\Omega$ )	QS	_	45	pC

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

<sup>\*</sup>FR-4 Minimum Pad

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Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so I<sub>R(peak)</sub> is equal to 10 mA.
- 3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

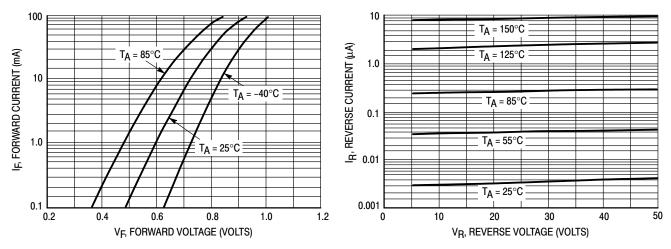


Figure 2. Forward Voltage

Figure 3. Leakage Current

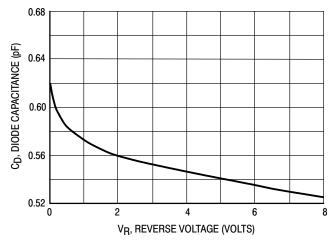
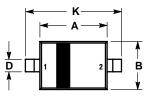


Figure 4. Capacitance

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## **PACKAGE DIMENSIONS**

### SOD-323 CASE 477-02 **ISSUE B**





- NOTES:

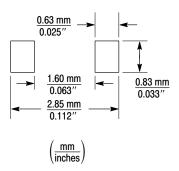
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: MILLIMETERS.

  3. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	1.60	1.80	0.063	0.071	
В	1.15	1.35	0.045	0.053	
C	0.80	1.00	0.031	0.039	
D	0.25	0.40	0.010	0.016	
Е	0.15 REF		0.006 REF		
Н	0.00	0.10	0.000	0.004	
J	0.089	0.177	0.0035	0.0070	
К	2.30	2.70	0.091	0.106	

STYLE 1: PIN 1. CATHODE 2. ANODE



SOD-323 Soldering Footprint

#### BAS16HT1

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