

# **High Voltage Transistor** PNP Silicon

## **BF493S**

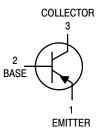
### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	-350	Vdc
Collector-Base Voltage	VCBO	-350	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-6.0	Vdc
Collector Current — Continuous	IC	-500	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0	Watts mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C



## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/W



## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (1) (I <sub>C</sub> = -1.0 mAdc, I <sub>B</sub> = 0)	V(BR)CEO	-350	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = –100 μAdc, I <sub>E</sub> = 0)	V(BR)CBO	-350	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = –100 μAdc, I <sub>C</sub> = 0)	V(BR)EBO	-6.0	_	Vdc
Collector Cutoff Current (V <sub>CE</sub> = -250 Vdc)	ICES	_	-10	nAdc
Emitter Cutoff Current (VEB = -6.0 Vdc, IC = 0)	IEBO	_	0.1	μAdc
Collector Cutoff Current $(V_{CB} = -250 \text{ Vdc}, I_E = 0, T_A = 25^{\circ}\text{C})$ $(V_{CB} = -250 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	I <sub>CBO</sub>	_	-0.005 -1.0	μAdc

<sup>1.</sup> Pulse Test: Pulse Width  $\leq 300 \,\mu s$ ; Duty Cycle  $\leq 2.0\%$ .

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## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit	
ON CHARACTERISTICS					
DC Current Gain (I <sub>C</sub> = $-1.0$ mAdc, V <sub>CE</sub> = $-10$ Vdc) (I <sub>C</sub> = $-10$ mAdc, V <sub>CE</sub> = $-10$ Vdc)	hFE	25 40	_	_	
Collector–Emitter Saturation Voltage (I <sub>C</sub> = -20 mAdc, I <sub>B</sub> = -2.0 mAdc)	VCE(sat)	_	-2.0	Vdc	
Base–Emitter On Voltage $(I_C = -20 \text{ mA}, I_B = -2.0 \text{ mA})$	VBE(sat)	_	-2.0	Vdc	
DYNAMIC CHARACTERISTICS	•	•			
Current–Gain — Bandwidth Product (I <sub>C</sub> = -10 mAdc, V <sub>CE</sub> = -20 Vdc, f = 20 MHz)		50	_	MHz	
Common–Emitter Feedback Capacitance (V <sub>CB</sub> = -100 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>re</sub>	_	1.6	pF	

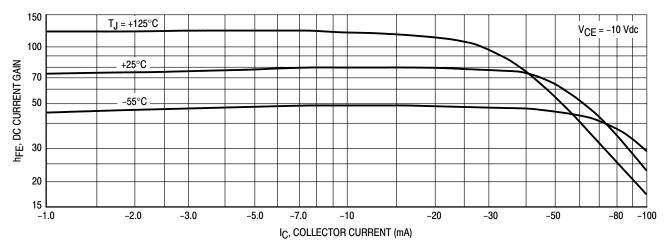


Figure 1. DC Current Gain

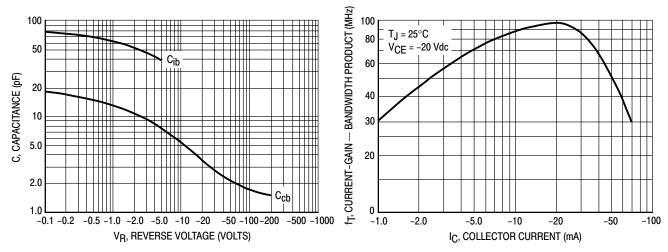
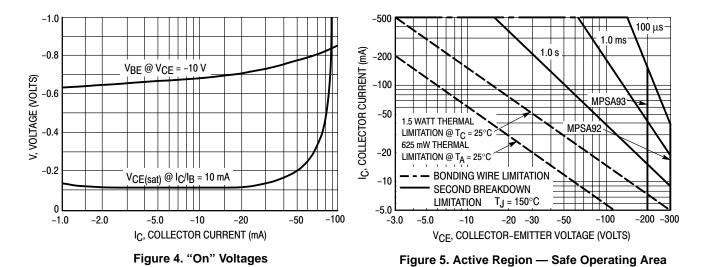


Figure 3. Current-Gain — Bandwidth Product

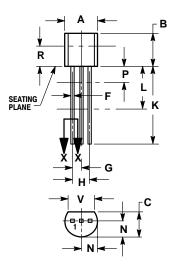
Figure 2. Capacitances

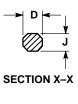


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## PACKAGE DIMENSIONS

CASE 029-04 (TO-226AA) ISSUE AD





#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
  Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
  IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L.
   DIMENSION D AND J APPLY BETWEEN L AND K
   MINIMUM. LEAD DIMENSION IS UNCONTROLLED
   IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 1:

PIN 1. EMITTER 2. BASE 3. COLLECTOR

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