

BSFF68GXX

DBS Tuner

FEATURES

- Receiving frequency range 950 MHz to 2,150 MHz
- Input signal level -65 dBm to -25 dBm
- F-type female input structure
- Nominal input impedance 75 Ω
- First IF frequency 479.5 MHz
- Second local frequency 479.5 MHz
- Electronic tuning, PLL synthesizer channel selection system (IX2186PA or similar IC, clock 4.0 MHz)
- Step frequency 125 kHz
- IF band width 36 MHz (-3 dB) TYP.
- Bit rate 60 Mbps MAX.
- Roll-off factor 35% (root-raised cosine)
- LINKIC L64724A
- FEC inner decoder
 - Viterbi soft decoder, constraint length $M = 7$
 - Punctured codes 1/2, 2/3, 3/4, 5/6, 7/8
 - Automatic or manual rate and phase recognition deinterleaver
 - Word synchro extraction
 - Convolutional deinterleaver
- FEC outer decoder
 - Reed-solomon decoder, for 16 parity bytes block lengths
 - 204 bytes energy dispersal descrambler
- Operating voltage
 - LNB voltage B1, 25 V, 300 mA MAX.
 - Supply voltage B2, B3, B4, 5.0 V ± 0.25 V DC
 - Supply voltage V_{DD} , 3.3 V ± 0.165 V DC
 - Tuning voltage TUN, 30.0 V ± 1.5 V DC
- Operating temperature 0°C to 60°C
- Storage temperature -20°C to 85°C
- Operating humidity, less than 85%
- Storage humidity, less than 95%

APPLICATIONS:

- Set Top Box
- DBS

DESCRIPTION

The BSFF68GXX is a DBS tuner intended for use in digital broadcasting satellites. This tuner incorporates a "LINK" section that is composed of DVB standard QPSK demodulation circuit and FEC (Forward Error Correction) circuit.

This tuner has an 8-bit transport output.

PIN DESCRIPTION

PIN NO.	NAME	PIN NO.	NAME
1	RF Input	18	SDA
2		19	LCLK
3	NC (GND)	20	STR Out
4	GND	21	D/P
5	B1	22	Error
6	GND	23	NRES
7	B2	24	V_{DD}
8	O Out	25	D0
9	GND	26	D1
10	I Out	27	D2
11	AGC Out	28	D3
12	B3	29	D4
13	GND	30	D5
14	B4	31	D6
15	TUN	32	D7
16	P3	33	CLK Out
17	SCL		

BLOCK DIAGRAM

