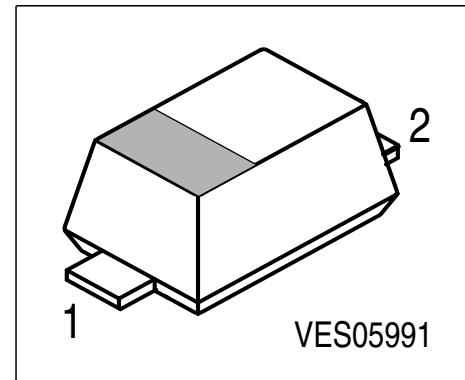


## Silicon Tuning Diode

- For VHF 2-Band-hyperband-TV-tuners
- Very high capacitance ratio
- Low series inductance
- Low series resistance
- Extremely small plastic SMD package
- Excellent uniformity and matching due to "in-line" matching assembly procedure



| Type                   | Marking | Pin Configuration |       | Package |
|------------------------|---------|-------------------|-------|---------|
| BB 689 unmached        | E       | 1=C               | 2=A   | SCD-80  |
| BB 689 in-line matched | E       | 1 = C             | 2 = A | SCD-80  |

### Maximum Ratings

| Parameter                                  | Symbol    | Value       | Unit               |
|--|-----------|-------------|--------------------|
| Diode reverse voltage                      | $V_R$     | 30          | V                  |
| Peak reverse voltage ( $R \geq 5k\Omega$ ) | $V_{RM}$  | 35          |                    |
| Forward current                            | $I_F$     | 20          | mA                 |
| Operating temperature range                | $T_{op}$  | -55 ... 150 | $^{\circ}\text{C}$ |
| Storage temperature                        | $T_{sta}$ | -55 ... 150 |                    |

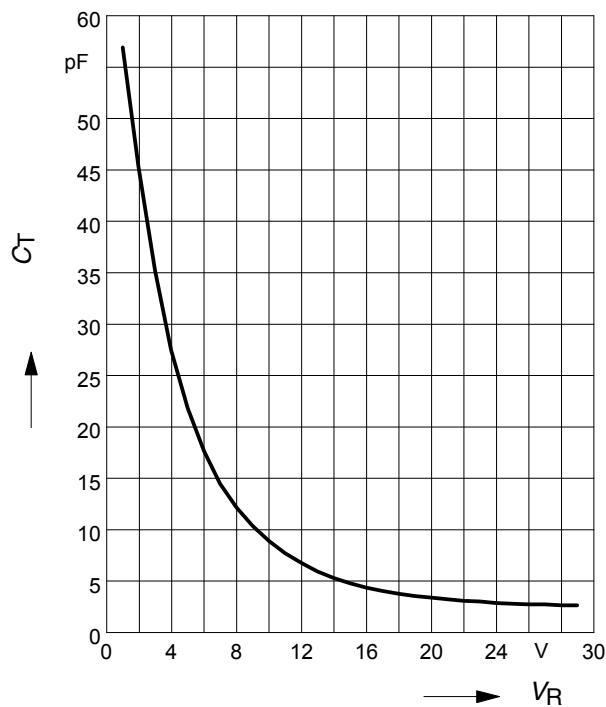
**Electrical Characteristics** at  $T_A = 25^\circ\text{C}$ , unless otherwise specified.

| Parameter  | Symbol           | Values |      |      | Unit     |
|--|------------------|--------|------|------|----------|
|  |                  | min.   | typ. | max. |          |
| <b>DC characteristics</b>  |                  |        |      |      |          |
| Reverse current<br>$V_R = 30 \text{ V}$  | $I_R$            | -      | -    | 10   | nA       |
| Reverse current<br>$V_R = 30 \text{ V}, T_A = 85^\circ\text{C}$                                  | $I_R$            | -      | -    | 200  |          |
| <b>AC characteristics</b>  |                  |        |      |      |          |
| Diode capacitance<br>$V_R = 1 \text{ V}, f = 1 \text{ MHz}$                                      | $C_T$            | 51     | 56.5 | 61.5 | pF       |
| $V_R = 2 \text{ V}, f = 1 \text{ MHz}$   |                  | 39.6   | 43.4 | 47.2 |          |
| $V_R = 25 \text{ V}, f = 1 \text{ MHz}$  |                  | 2.6    | 2.8  | 3    |          |
| $V_R = 28 \text{ V}, f = 1 \text{ MHz}$  |                  | 2.5    | 2.7  | 2.9  |          |
| Capacitance ratio<br>$V_R = 2 \text{ V}, V_R = 25 \text{ V}, f = 1 \text{ MHz}$                  | $C_{T2}/C_{T25}$ | 14.5   | 15.5 | 17   | -        |
| Capacitance ratio<br>$V_R = 1 \text{ V}, V_R = 28 \text{ V}, f = 1 \text{ MHz}$                  | $C_{T1}/C_{T28}$ | 18     | 20.9 | 23.2 |          |
| Capacitance matching <sup>1)</sup><br>$V_R = 1 \text{ V}, V_R = 28 \text{ V}, f = 1 \text{ MHz}$ | $\Delta C_T/C_T$ | -      | -    | 2    | %        |
| Series resistance<br>$V_R = 8 \text{ V}, f = 470 \text{ MHz}$                                    | $r_s$            | -      | 0.85 | -    | $\Omega$ |
| Series inductance chip to ground   | $L_s$            | -      | 0.6  | -    | nH       |

1) In-line matching. For details please refer to Application Note 047

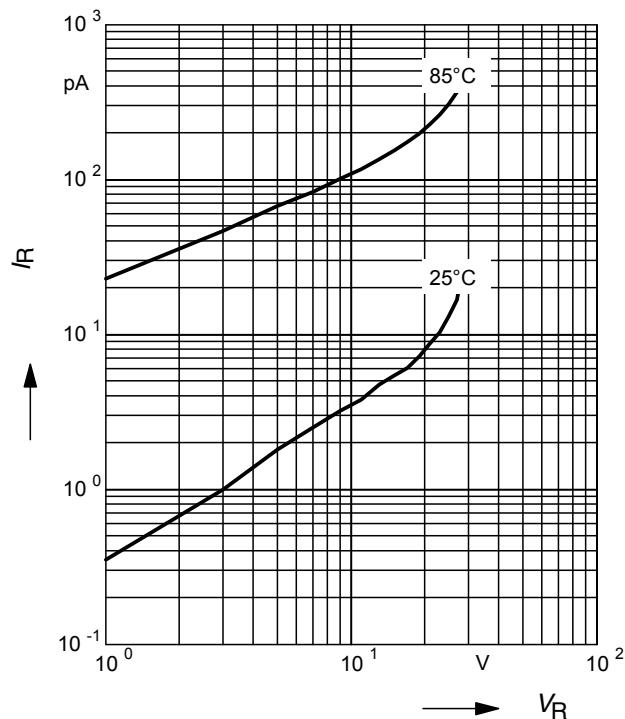
**Diode capacitance**  $C_T = f(V_R)$

$f = 1\text{MHz}$



**Reverse current**  $I_R = f(V_R)$

$T_A$  = Parameter



**Temperature coefficient of the diode capacitance**  $T_{Cc} = f(V_R)$

