...when timing is critical

B9852

High Performance Pentium®III Clock Buffer

Preliminary

Product Features

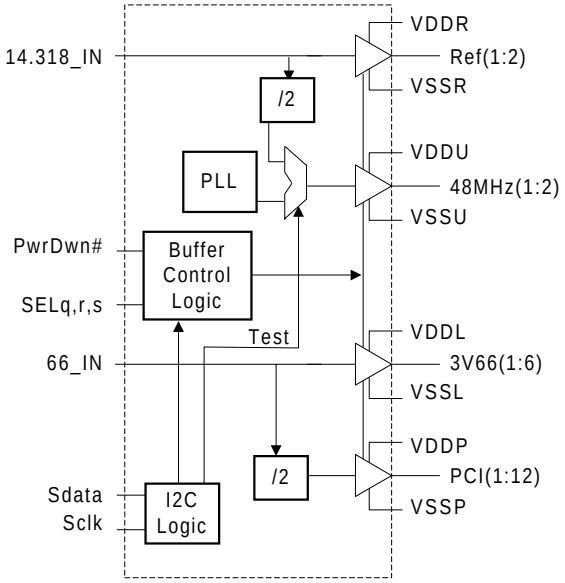
- Six 3V66 clocks
- Twelve 3V, 33 MHz PCI clocks
- Two 48 MHz clocks
- Two 14.318 MHz reference clocks
- Select logic, Hi-Z, and output disable modes
- Power Savings Clock OFF Mode
- 66 MHz reference input for PCI and 3V66 clocks
- 14.318 reference input for REF and 48M clocks
- 56 Pin SSOP and TSSOP Package

Frequency Selection Table

SELq	SELr	SELs	Description
0	0	0	All outputs on
0	0	1	PCI(1:8) off in low state
0	1	0	PCI(1:6) off in low state
0	1	1	All PCI off
1	0	0	All outputs Hi-Z
1	0	1	3V66_1 off in low state
1	1	0	Ref(1:2) off in low state
1	1	1	48MHz1 off in low state

NOTE: all SEL changes cause glitch free output clock transitions to occur.

Block Diagram



Pin Configuration