

April 1984 Revised February 2000

# DM74ALS151 1 of 8 Line Data Selector/Multiplexer

# **General Description**

This Data Selector/Multiplexer contains full on-chip decoding to select one-of-eight data sources as a result of a unique three-bit binary code at the Select inputs. Two complementary outputs provide both inverting and non-inverting buffer operation. A Strobe input is provided which, when at the high level, disables all data inputs and forces the Y output to the LOW state and the W output to the HIGH state. The Select input buffers incorporate internal overlap features to ensure that select input changes do not cause invalid output transients.

#### **Features**

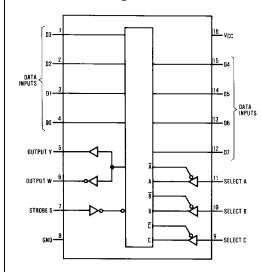
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- $\blacksquare$  Switching performance is guaranteed over full temperature and  $V_{CC}$  supply range
- Pin and functional compatible with LS family counterpart
- Improved output transient handling capability

# **Ordering Code:**

Order Number	Package Number	Package Description
DM74ALS151M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74ALS151N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

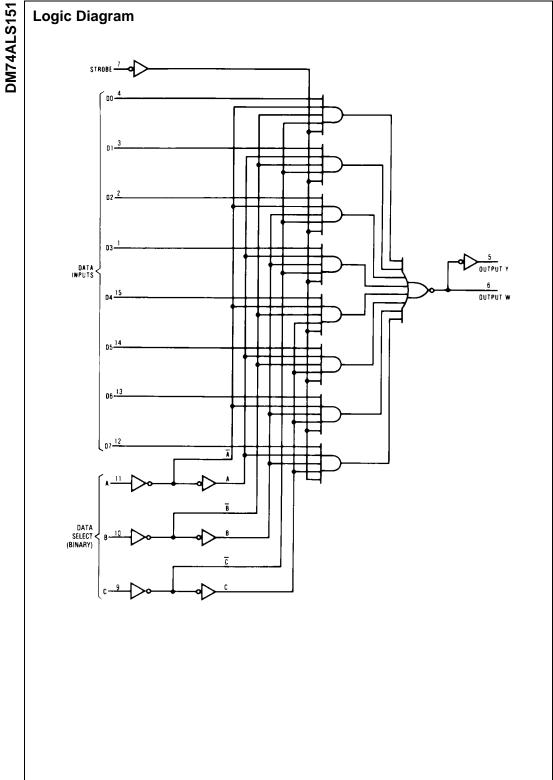
# **Connection Diagram**



# **Function Table**

Inputs				Outputs	
Select		Strobe	Y	w	
С	В	Α	s	I	VV
Χ	Χ	Χ	Н	L	Н
L	L	L	L	D0	D0
L	L	Н	L	D1	D1
L	Н	L	L	D2	D2
L	Н	Н	L	D3	D3
Н	L	L	L	D4	D4
Н	L	Н	L	D5	D5
Н	Н	L	L	D6	D6
Н	Н	Н	L	D7	D7

- H = HIGH Level L = LOW Level
- X = Don't Care D0 thru D7 = the level of the respective D input



# **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V 7V Input Voltage

Operating Free Air Temperature Range  $0^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$ 

Storage Temperature Range -65°C to +150°C

Typical  $\theta_{JA}$ 

N Package 78.0°C/W M Package 107.0°C/W Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

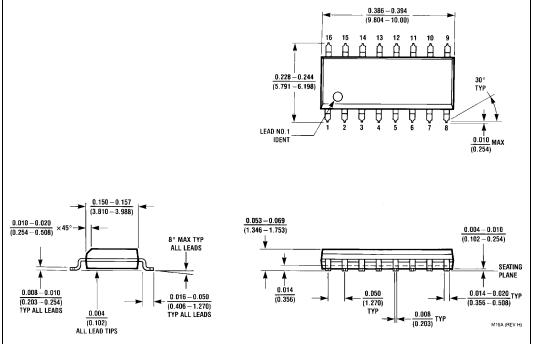
Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>OH</sub>	HIGH Level Output Current			-2.6	mA
I <sub>OL</sub>	LOW Level Output Current			24	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

# **Electrical Characteristics**

recommended operating free-air temperature range. All typical values are measured at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ 

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V <sub>IK</sub>	Input Clamp Voltage	$V_{CC} = 4.5V$ , $I_{IN} = -18$ mA	$V_{CC} = 4.5V$ , $I_{IN} = -18$ mA			-1.5	V
V <sub>OH</sub>	HIGH Level	$V_{CC} = 4.5V$ , $I_{OH} = Max$ $I_{OH} = -400 \ \mu A$ , $V_{CC} = 4.5V \ to 5.5V$ $V_{CC} = 4.5V \ to 5.5V$		2.4	3.2		V
	Output Voltage			V <sub>CC</sub> - 2			V
V <sub>OL</sub>	LOW Level Output Voltage	V <sub>CC</sub> = 4.5V	$I_{OL} = 24 \text{ mA}$		0.35	0.5	V
I <sub>I</sub>	Input Current at Maximum Input Voltage	V <sub>CC</sub> = 5.5V, V <sub>IN</sub> = 7V	-			0.1	mA
I <sub>IH</sub>	HIGH Level Input Current	V <sub>CC</sub> = 5.5V, V <sub>IN</sub> = 2.7V				20	μΑ
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = 5.5V, V_{IN} = 0.4V$				-0.1	mA
I <sub>O</sub>	Output Drive Current	V <sub>CC</sub> = 5.5V, V <sub>OUT</sub> = 2.25V		-30		-112	mA
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = 5.5V All Inputs = 4.5V			7.5	12	mA

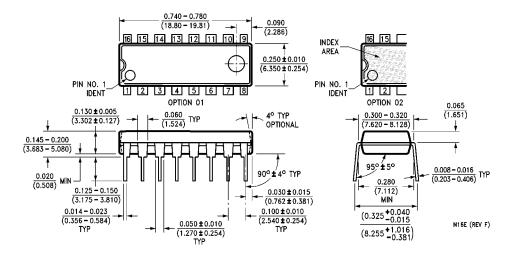
#### **Switching Characteristics** over recommended operating free air temperature range Conditions Symbol Parameter From То Min Units Propagation Delay Time $V_{CC} = 4.5V \text{ to } 5.5V$ Υ 4 Select 18 ns LOW-to-HIGH Level Output $C_L = 50 \text{ pF}$ $R_L = 500\Omega$ Propagation Delay Time Υ 8 Select ns HIGH-to-LOW Level Output Propagation Delay Time $t_{\mathsf{PLH}}$ Select 7 ns LOW-to-HIGH Level Output Propagation Delay Time t<sub>PHL</sub> 7 W Select ns HIGH-to-LOW Level Output Propagation Delay Time $t_{\mathsf{PLH}}$ Data 3 10 ns LOW-to-HIGH Level Output Propagation Delay Time $t_{PHL}$ 5 Data Υ 15 ns HIGH-to-LOW Level Output Propagation Delay Time $t_{\mathsf{PLH}}$ 3 Data W 15 ns LOW-to-HIGH Level Output Propagation Delay Time $t_{PHL}$ HIGH-to-LOW Level Output Propagation Delay Time $t_{PLH}$ 4 18 Strobe ns LOW-to-HIGH Level Output Propagation Delay Time t<sub>PHL</sub> Strobe 4 HIGH-to-LOW Level Output $t_{PLH}$ Propagation Delay Time Strobe W 5 19 ns LOW-to-HIGH Level Output Propagation Delay Time $t_{\text{PHL}}$ W 5 Strobe ns HIGH-to-LOW Level Output



Physical Dimensions inches (millimeters) unless otherwise noted

16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M16A

# Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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