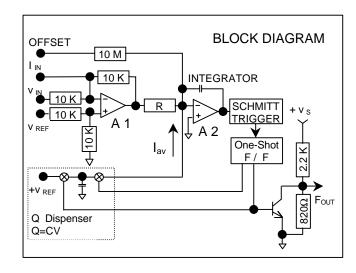


# 5 MHz Voltage to Frequency Converters

### Models 8610, 8612





#### Features:

- Guaranteed Minimum / Maximum Specifications
- Wide Dynamic Range
  - > 5,000,000:1 > 134 dB
- Excellent Linearity
  - 0.01% FS
  - ±0.03% of input
- Excellent Stability
  - 10 mV/°C offset
  - 15 ppm / °C gain
- Buffered Frequency Output
  10 TTL loads
- Self-contained Subsystem
  - 2"x 2"x 0.4" module
- Low Power
  - < 1.26 W

# Applications:

- Analytical Instrumentation
- Medical Instrumentation
- Telemetry
- Data Recording
- Weighing Systems

### **Description**

The **8600 Series** are high performance, high precision 5 MHz full scale Voltage-to Frequency Converters intended for applications which require high resolution and a six decade dynamic range. The differential input of these units accepts both positive or negative 10  $\mu V$  to 10 V full scale analog signals, with a 5% over-range capability. The input signal, with common-mode signals attenuated by 60 dB minimum, is converted to an output proportional to the full scale frequency, within 0.01% linearity, utilizing the long-proven charge balance technique. A buffered TTL-compatible frequency output with a 10 TTL-load fanout is provided that will drive up to 50 pF capacitive loads.

Stability of the **8600 Series** over temperature is excellent, with a 10  $\mu V$  / °C typical, 50  $\mu V$  / °C maximum offset and 25 ppm / °C maximum (**8612**) gain tempco. Warm-up time to 0.02% accuracy is less than two (2) minutes. In applications that require slightly different specifications such as different full scale output frequency, or where fixed offset or different full scale voltages would be convenient, **custom frequencies** and/or **custom trimming** can be easily accommodated. Other variations such as ratio-metric operation, FET input op-amp, or extended temperature range can also be accommodated. Please contact the factory to discuss your specific requirements.

The **8600 Series** are packaged in a 2.00"x 2.00" x 0.40" modular package. Power dissipation is less than 1.26 W maximum, and operation to rated performance is over the  $0^{\circ}$  C to  $+70^{\circ}$  C temperature range.

# 8600 Series Specifications

(Unless otherwise noted, specifications are at 25°C and are subject to change without notification)

#### Analog Input

Input Range  $\pm~10~\mu V$  to  $\pm~10~V$ Current Range ~+~1~nA to ~+~1~mAOverrange 5% minimum Configuration Differential

Common-Mode Voltage Range ± 10 V minimum Common-Mode Rejection Ratio 60 dB minimum 66 dB typical (See Note 1)

Offset Voltage ± 3 mV typical ± 10 mV maximum adjustable to zero

 $\label{eq:local_local} \begin{array}{ll} \textbf{Impedance} \; (+\mathbf{V_{in}}) \; \; 10 \; K\Omega, \pm 1\% \\ \textbf{Impedance} \; (\textbf{Differential}) \; \; 40 \; K\Omega, \pm 1\% \\ \end{array}$ 

Overvoltage Protection ( $l_{in}$  Terminal)  $\pm V_S$  without damage Overvoltage Protection ( $V_{ref}$  Terminal)  $\pm 2 V_S$  without damage

#### Transfer Characteristics

Full Scale Frequency Output ( $F_{out}$ ) 5 MHz + 5% over-range Transfer Characteristics 5 MHz ( $V_{in}/10$  V) Full Scale Factor 1 mA  $\pm$  0.1%, or 10 V trimmable to 5 MHz Non-Linearity  $\pm$  0.01% FS,  $\pm$  0.03% of input maximum

not specified under overrange conditions

Full Scale Step Response (to 0.01%) 2 cycles of new frequency plus 2  $\mu s$  Overload Recovery 12 cycles of new frequency

### Power Requirements

 $(+V_s)$  + 15V,  $\pm$  5% 55 mA maximum  $(-V_s)$  - 15V,  $\pm$  5% 25 mA maximum **Power Dissipation** 1.26 W maximum

Note 1: CMRR specification given assumes zero (0) ohms for GAIN ADJUST potentiometer. With GAIN ADJUST potentiometer at 200  $\Omega$ , CMRR is 34 dB.

#### Stability

Gain - Tempco

**8610** 60 ppm FS / °C typical - 100ppm FS / °C maximum **8612** 15 ppm FS / °C typical - 25ppm FS / °C maximum

Gain - PS Sensitivity 200 ppm / 1% change in supply voltage

Gain - Drift Per Day ± 100 ppm FS maximum Gain - Drift Per Month ± 200 ppm FS maximum Offset - Tempco ± 10 µV typical - ± 50 µV maximum

Offset - PS Sensitivity 20 μV / V change in supply voltage

Offset - Drift Per Day  $\pm 10 \,\mu\text{V}$  typical Offset - Drift Per Month  $\pm 20 \,\mu\text{V}$  typical Warm-up Time  $\leq 2$  minutes to 0.02% accuracy

#### Output

**Pulse Polarity** Positive **Pulse Width** 100ns ± 40ns **Logic Levels** 

Logic Levels

**Logic "1" (High)** + 4.0 V ± 0.5 V **Logic "0" (Low)** < 0.4 V @ 16 mA sink

**Load** ≤ 50 pF for rated performance

Fanout 10 TTL loads

Short Circuit Protection Indefinite to ground without damage

#### Environmental And Mechanical

Operating Temperature (to Rated Performance)  $0^{\circ}$  C to  $+70^{\circ}$  C Operating Temperature

(to 50% Derated TC, Linearity, and Fanout) - 25° C to + 85° C

**Storage Temperature** - 55° C to + 125° C **Humidity** 0 - 85%, non-condensing up to 40° C

**Dimensions** 2.00"x 2.00" x 0.40"

(50.8 x 50.8 x 10.16 mm)

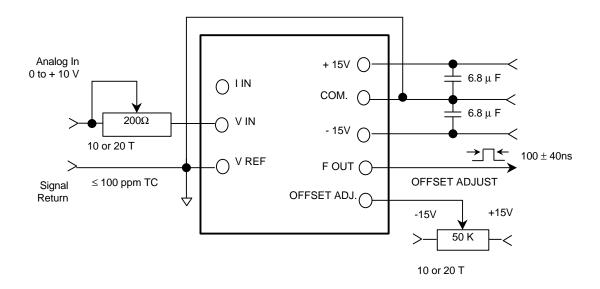


Fig. 1. Normal 8600 Series Input Configuration

# Voltage to Frequency Converters

#### Using The 8600 Series of V/F Converters

#### **General Considerations**

Figure 1 depicts a typical circuit configuration for the **8600 Series.** The layout should be clean, with output pulses routed as far away from the input analog signals as possible. For maximum performance, bypass capacitors, as shown in Figure 1, should be mounted right at the appropriate pins of the **8600 Series**. For positive input signals, use the connections as shown. For negative input voltages,  $V_{in}$  should be grounded and the negative-going voltage should be connected to the  $V_{ref}$  input.

#### Grounding

The Analog and Digital grounds are internally separate in the **8600 Series**. The use of a ground plane is not necessary for proper operation; however, a ground plane is recommended with any analog signal conditioning circuitry that may be used in front of the V/F, especially if this circuitry involves high gains. Any amplifiers used in front of the **8600 Series** should be de-coupled to eliminate potential problems with the high frequency output of the V/F.

### Input considerations Single-ended Inputs

The  $V_{in}$  pin accepts a 0 V to +10 V analog input, and has an impedance of 10 K $\Omega$ . Figure 2 provides a recommended configuration for expanded or contracted input ranges.

#### **Differential Inputs**

The input can be configured as a differential input as shown in Figure 3. The differential input impedance is 40 K $\Omega$ . The maximum common mode voltage is  $\pm$  10 V.

#### **Offset and Gain Trimming**

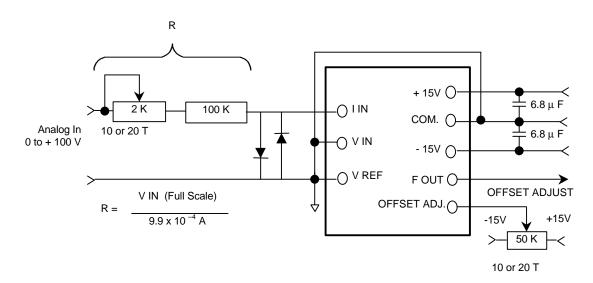
The OFFSET adjustment potentiometer should be a 50 K $\Omega$ , 10-turn unit. With this pot in the circuit, initial offsets of up to  $\pm$  10 mV may be trimmed to zero. The GAIN adjustment potentiometer should be a 200 $\Omega$ , 10-turn unit with a recommended temperature coefficient of 100 ppm or better. With this pot in the circuit, initial gain errors of up to  $\pm$  2% may be trimmed to zero.

#### **Offset Calibration**

Offset calibration should be performed prior to gain calibration. With a + 1 mV analog signal at the input of the **8600 Series**, adjust the OFFSET potentiometer until a frequency of 500 Hz is observed on the output pin.

#### **Gain Calibration**

With a full scale analog input voltage of + 10.00 V, adjust the GAIN potentiometer until a full scale frequency of 5.000 MHz is observed on the output pin.



Note: This configuration is also useful for adding or subtracting currents, off-setting the input for a bipolar signal, or presetting a minimum frequency output.

Fig. 2. Expanded Input Range

## **Useful Configurations (cont.)**

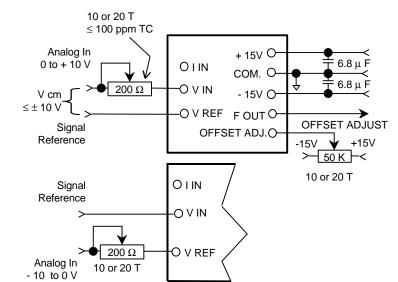
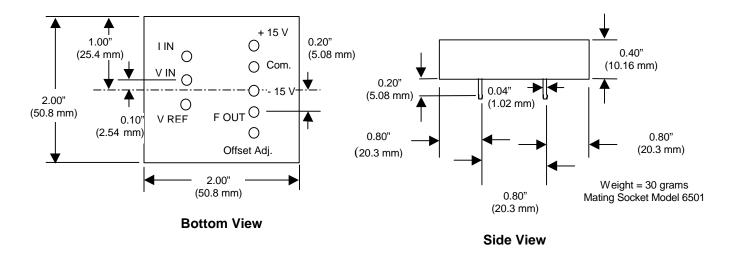


Fig. 3. Differential Inputs

# **Mechanical Dimensions and Pin Key**



# **Ordering Information**

Voltage to Frequency	Model	Input	Input Frequency Range
Converters	8610	Differential	100 ppm FS / °C maximum Gain Drift
	8612	Differential	25 ppm FS / °C maximum Gain Drift

Accessories	Model	Description
	6501	Socket

