

8m122399

3 Dimming Options (valid when the "C" jumper is removed)

Pulse-Width Modulated Controlled

The diagram shows a pulse-width modulated (PWM) signal source connected to pin 3. The signal is a square wave with a duty cycle of 5% to 100% and a period of approximately 5 ms. The input voltage is +Vin. A by-pass capacitor (1) is connected between pins 1 and 2, with Vin applied to pin 1. The tube is connected to a load resistor.

Resistive Controlled

The diagram shows a resistive control circuit. A 100 Kohms resistor is connected between +Vin and pin 3. A by-pass capacitor (1) is connected between pins 1 and 2, with Vin applied to pin 1. The tube is connected to a load resistor.

Voltage Controlled

The diagram shows a voltage control circuit. A voltage source Vmin* to +Vin is connected to pin 3. A by-pass capacitor (1) is connected between pins 1 and 2, with Vin applied to pin 1. The tube is connected to a load resistor.

*** should be selected to be compatible with LCD and display driver**

*** value should be low enough to maintain minimum tube current at minimum brightness**

*** Vmin should be high enough to maintain minimum tube current**

Note 1 Input by-pass capacitor (25 uf - 100uf) may be required to reduce reflected ripple.

Endicott Research Group, Inc.

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Absolute Maximum Ratings

Rating	Symbol	Value	Units
Input Voltage Range	Vin	-0.3 to +13.2	Vdc
Operating Temperature	To	-0 to +70	°C
Storage Temperature	Tstg	-40 to +85	°C

Recommended Operating Conditions

Rating	Symbol	Value	Units
Input Voltage	Vin	+6 to 13.2	Vdc

Electrical Characteristics

Unless otherwise noted Vin = 12.00 Volts dc and Ta = 25 °C

Characteristic	Symbol	Min	Typ	Max	Units
Input Current	Iin	-	.22	.26	Adc
Operating Frequency	Fo	28	33	38	KHz
Minimum Output Voltage	Vout (min)	1500	-	-	Vrms
Efficiency	η	-	74	-	%
Output Current	Iout	-	3	-	marms
Output Voltage (When powering a Load simulating the referenced display)	Vout	-	650	-	Vrms
Pin3 Input Current Requirement	-	-	5.5	-	madc

After tube has been allowed to warm-up for 5 minutes
External Disable Circuit shown on page 3.

Specifications subject to change without notice.

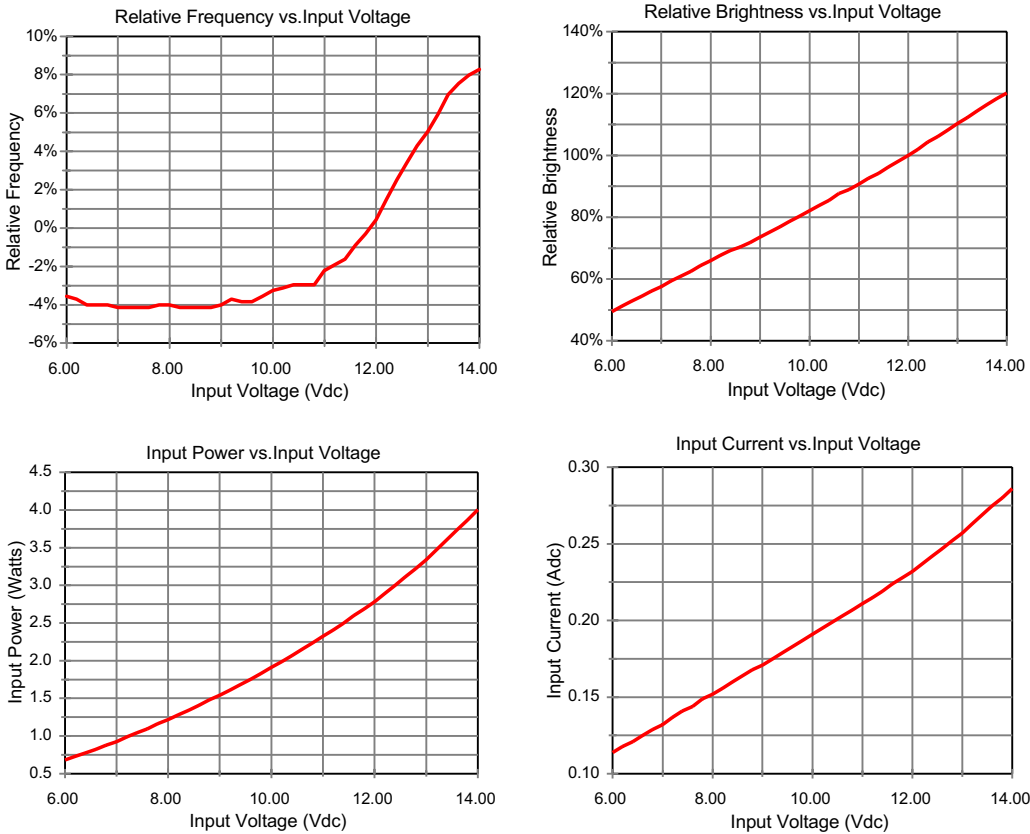


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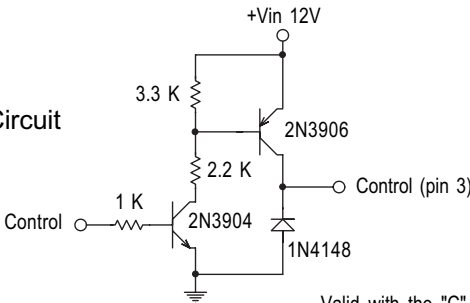
Made in USA

Typical Performance Curves

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Disable Circuit



Valid with the "C" jumper removed



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