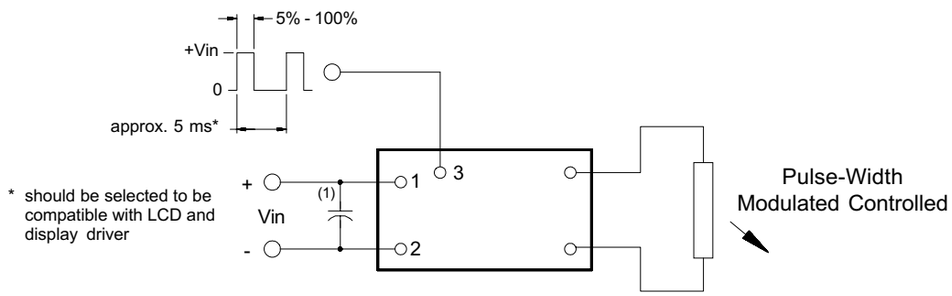
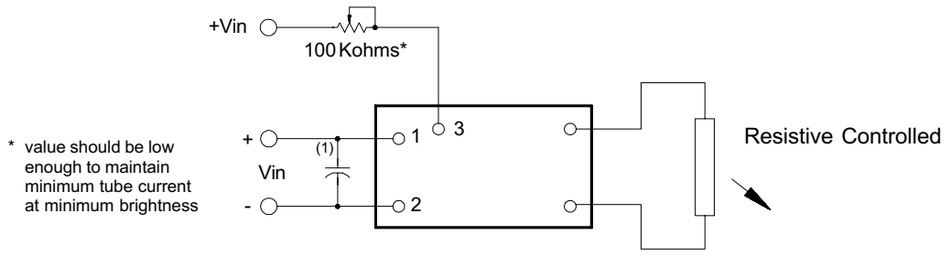
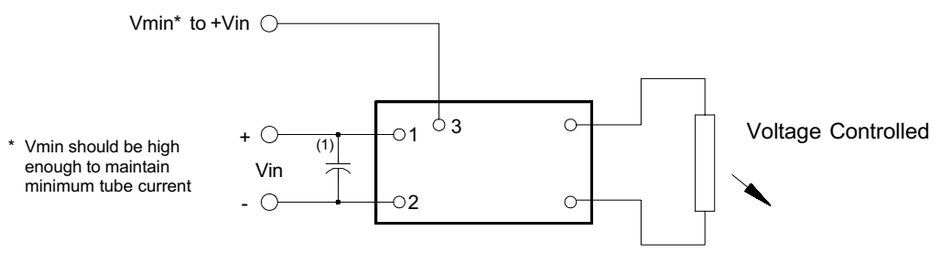
8m122362

3 Dimming Options







Note 1 Input by-pass capacitor (25 uf - 100uf) may be required to reduce reflected ripple.

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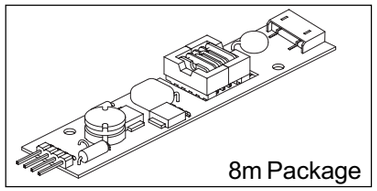
8m122362

Specifications and Applications Information

7/10/00 Preliminary

8m Class
DC to AC Inverter

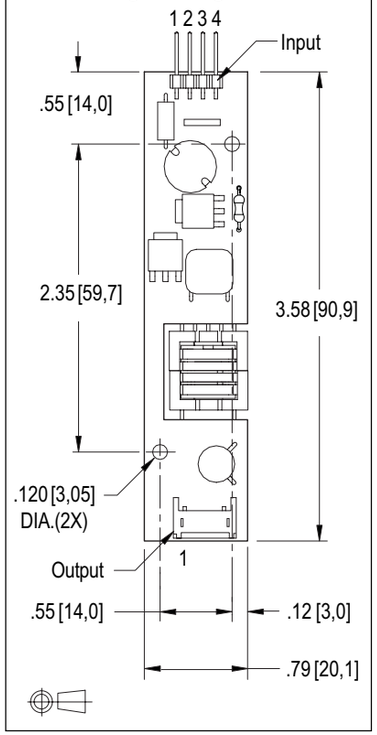

8m Package

The ERG 8m122362 (8m Class) low profile dc to ac inverter is specifically designed to power the Kyocera KCB065HV1AC-G40 LCD display module to a moderate brightness level from a +12 volt dc source.

This low profile inverter features:

- ✓ Less Than 8 mm in Height
- ✓ LCD Module Specific
- ✓ Display Compatible Output Connector
- ✓ Firm Specifications
- ✓ Application Information
- ✓ Designed, Manufactured and Supported in the USA
- ✓ Custom Input and Output Voltages
- ✓ Flexible System Interface
- ✓ Notebook Display Head Compatible

Package Configuration



Pin Descriptions

Input Connector	Output Connector
4 pins are 0.315" [8,00] Long, 0.025" [0,63] Square and are on 0.100" [2,54] Centers.	JST SM02(8.0)B-BHS-1-TB
Con1-1 Vin(+)	Con2-1 ACout
Con1-2 GND	Con2-2 NC
Con1-3 Control	Con2-3 NC
Con1-4 NC	Con2-4 ACout

Absolute Maximum Ratings

Rating	Symbol	Value	Units
Input Voltage Range	Vin	-0.3 to +13.2	Vdc
Operating Temperature	To	-0 to +70	°C
Storage Temperature	Tstg	-40 to +85	°C

Recommended Operating Conditions

Rating	Symbol	Value	Units
Input Voltage	Vin	+6 to 13.2	Vdc

Electrical Characteristics

Unless otherwise noted Vin = 12.00 Volts dc and Ta = 25 °C

Characteristic	Symbol	Min	Typ	Max	Units
Input Current	Iin	-	.125	.150	Adc
Operating Frequency	Fo	27	32	37	KHz
Minimum Output Voltage	Vout (min)	1000	-	-	Vrms
Efficiency	η	-	72	-	%
Output Current	Iout	-	2.6	-	marms
Output Voltage (When powering a load simulating the referenced display)	Vout	-	415	-	Vrms
Pin3 Input Current Requirement	-	-	3	-	madc

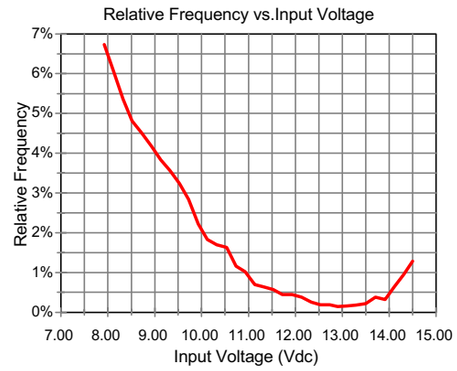
After tube has been allowed to warm-up for 5 minutes
External Disable Circuit shown on page 3.

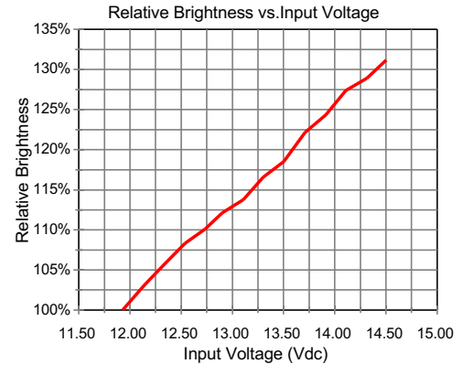
Specifications subject to change without notice.

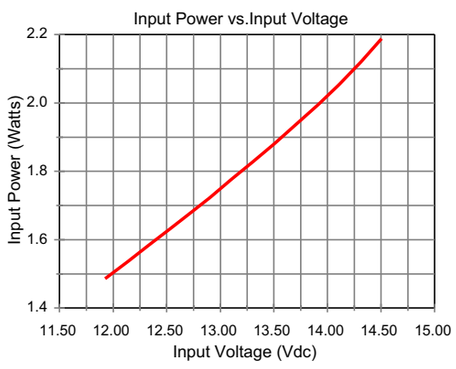
Endicott Research Group, Inc.

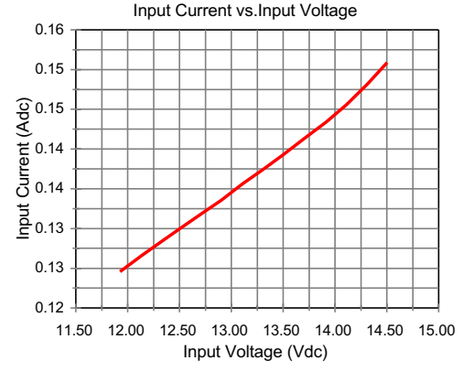
Made in USA

Typical Performance Curves

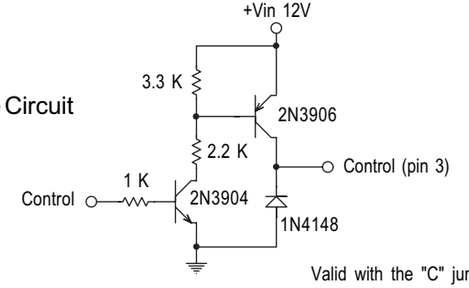








Disable Circuit



Valid with the "C" jumper removed

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