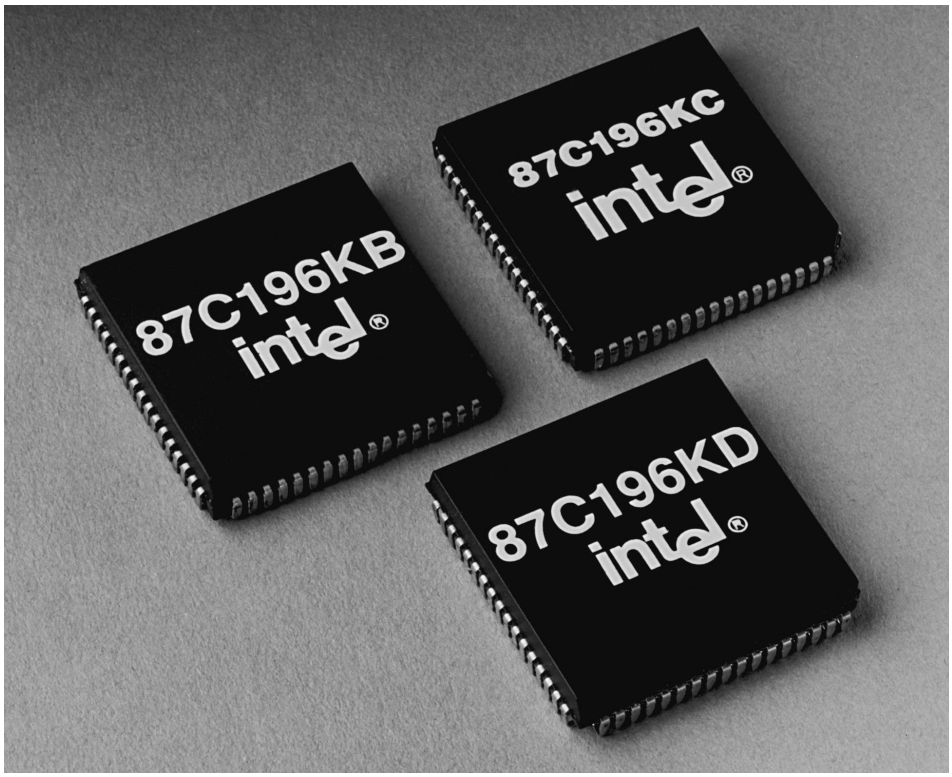
MCS® 96 Microcontroller

- 16-Bit CPU (Operating Up to 50 MHz)
- Register-to-Register Architecture
- On-Chip Memory, Both ROM and RAM
- On-Chip Peripherals
  - Timers/Counters, A/D, HSI/O, EPA Interrupt Controller, Serial Ports, Waveform Generator
  - Chip Select Unit On Board
- CMOS Design and Power Down Modes
- Multiplexed/Demultiplexed Bus
- No Accumulator Bottlenecks
- Simpler More Efficient Code
- Efficient Power Consumption
- Interfaces to Low Cost Memories



Intel's MCS® 96 microcontroller product family fits a variety of embedded applications. The high performance, register-to-register architecture is ideal for complex, real-time control applications. Intel designed its broad portfolio of 8XC196 microcontroller products to meet various levels of peripheral, memory size, addressability and performance requirements.

The 8XC196 family shared a common, register-based architecture core that eliminates the accumulator bottleneck and enables fast context switching. Although the 8XC196 is a 16-bit architecture, all devices have bit, byte, word and 32-bit operations.

The strong future of this architecture is expected to include code-compatible devices with significantly higher performance and greater memory integration. Intel's commitment to providing a complete tools solution and continued enhancements in MCS 96 microcontrollers creates a strong foundation in which to build.

HSIO Family
Intel designed the HSIO family for applications that required high-speed input and output and closed-loop event control. These devices can lock events in the high-speed output unit (CAM), which allows you to repeat events with no software overhead. The family consists of the 8XC196KB, 8XC196KC and 8XC196KD.

The CHMOS version is code- and peripheral-compatible with NMOS devices. HSIO allows interrupt servicing in the background with minimal CPU overhead and reduces external components for temperature control, strain gauge and motion detection. It's an attractive solution for applications that need accurate timing of multiple events.

PRODUCT FAMILY:
8xC196KB, 8xC196KC, 8xC196KD, 8xC196KR, 8xC196MC, 8xC196MD, 8xC196MH, 8xC196NP, 8xC196NT, 8xC196NU, 8xC198

DEVELOPMENT PLATFORMS:
8xC196KD Project Builder
Eval Board

ENVIRONMENT:
DOS/Windows, DOS

AVAILABILITY:
Now

CONTACT:
Local Intel Sales Office
WWW: http://www.intel.com