

LM431SA/LM431SB/LM431SC

Programmable Shunt Regulator

Features

- Programmable Output Voltage to 36 Volts
- Low Dynamic Output Impedance 0.20 Typical
- Sink Current Capability of 1.0 to 100mA
- Equivalent Full-range Temperature Coefficient of 50ppm/°C Typical
- Temperature Compensated for Operation Over Full Rated Operating Temperature Range
- Low Output Noise Voltage
- Fast Turn-on Response

Description

The LM431SA/LM431SB/LM431SC are three terminal output adjustable regulators with thermal stability over operating temperature range. The output voltage can be set any value between V_{REF} (approximately 2.5 volts) and 36 volts with two external resistors. These devices have a typical dynamic output impedance of 0.2Ω . Active output circuit provides a sharp turn-on characteristic, making these devices excellent replacement for Zener Diodes in many applications.

SOT-89



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1. Ref 2. Anode 3. Cathode

SOT-23F

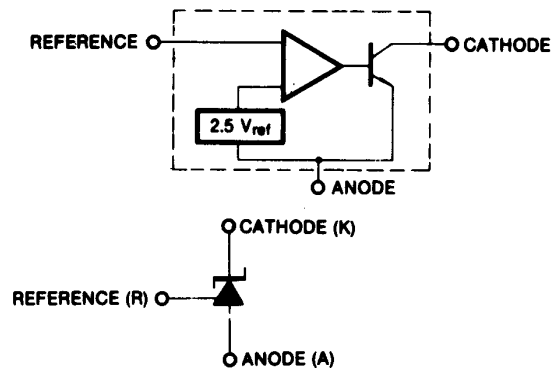
3



1 2

1. Cathode 2. Ref 3. Anode

Internal Block Diagram



Absolute Maximum Ratings

(Operating temperature range applies unless otherwise specified.)

Parameter	Symbol	Value	Unit
Cathode Voltage	V _{KA}	37	V
Cathode current Range (Continuous)	I _{KA}	-100 ~ +150	mA
Reference Input Current Range	I _{REF}	0.05 ~ +10	mA
Thermal Resistance Junction-Air (Note1,2) MF Suffix Package ML Suffix Package	R _{θJA}	350 220	°C/W
Power Dissipation (Note3,4) MF Suffix Package ML Suffix Package	P _D	350 560	mW
Operating Temperature Range	T _{OPR}	-25 ~ +85	°C
Storage Temperature Range	T _{STG}	-65 ~ +150	°C

Note:

- Thermal resistance test board
Size: 76.2mm * 114.3mm * 1.6mm (1S0P)
JEDEC Standard: JESD51-3, JESD51-7
- Assume no ambient airflow.
- T_{JMAX} = 150 °C, Ratings apply to ambient temperature at 25 °C
- Power dissipation calculation: $P_D = (T_J - T_A)/R_{\theta JA}$

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Cathode Voltage	V _{KA}	V _{REF}	-	36	V
Cathode Current	I _{KA}	1.0	-	100	mA

Electrical Characteristics

(T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Conditions		LM431SA			LM431SB			LM431SC			Unit
				Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
Reference Input Voltage	V _{REF}	V _{KA} =V _{REF} , I _{KA} =10mA		2.450	2.500	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V
Deviation of Reference Input Voltage Over-Temperature	$\Delta V_{REF}/\Delta T$	V _{KA} =V _{REF} , I _{KA} =10mA T _{MIN} ≤T _A ≤T _{MAX}		-	4.5	17	-	4.5	17	-	4.5	17	mV
Ratio of Change in Reference Input Voltage	$\Delta V_{REF}/\Delta V_{KA}$	I _{KA} =10mA	ΔV _{KA} =10V-V _{REF}	-	-1.0	-2.7	-	-1.0	-2.7	-	-1.0	-2.7	mV/V
to the Change in Cathode Voltage			ΔV _{KA} =36V-10V	-	-0.5	-2.0	-	-0.5	-2.0	-	-0.5	-2.0	
Reference Input Current	I _{REF}	I _{KA} =10mA, R ₁ =10KΩ, R ₂ =∞		-	1.5	4	-	1.5	4	-	1.5	4	μA
Deviation of Reference Input Current Over Full Temperature Range	ΔI _{REF} /ΔT	I _{KA} =10mA, R ₁ =10KΩ, R ₂ =∞ T _A =Full Range		-	0.4	1.2	-	0.4	1.2	-	0.4	1.2	μA
Minimum Cathode Current for Regulation	I _{KA(MIN)}	V _{KA} =V _{REF}		-	0.45	1.0	-	0.45	1.0	-	0.45	1.0	mA
Off - Stage Cathode Current	I _{KA(OFF)}	V _{KA} =36V, V _{REF} =0		-	0.05	1.0	-	0.05	1.0	-	0.05	1.0	μA
Dynamic Impedance	Z _{KA}	V _{KA} =V _{REF} , I _{KA} =1 to 100mA f ≥1.0KHz		-	0.15	0.5	-	0.15	0.5	-	0.15	0.5	Ω

Note1

T_{MIN}= -25 °C, T_{MAX}= +85°C

Test Circuits

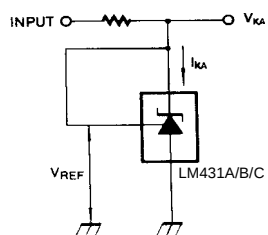


Figure 1. Test Circuit for $V_{KA} = V_{REF}$

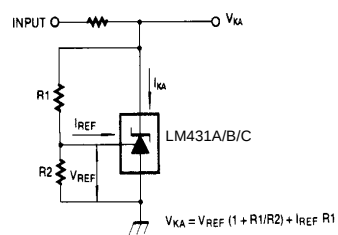


Figure 2. Test Circuit for $V_{KA} \geq V_{REF}$

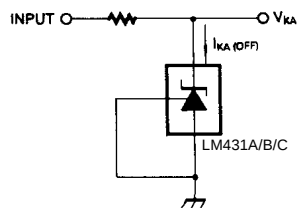


Figure 3. Test Circuit for $I_{KA(OFF)}$

Typical Performance Characteristics

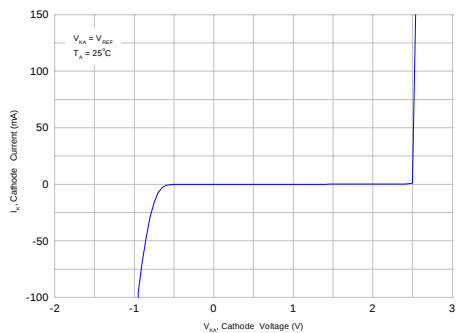


Figure 4. Cathode Current vs. Cathode Voltage

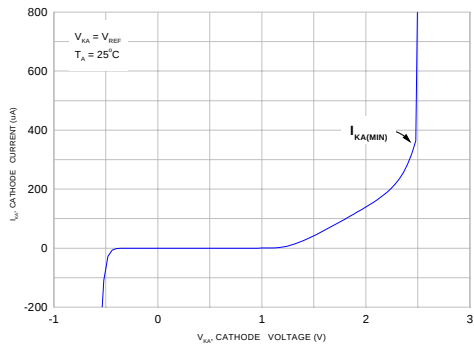


Figure 5. Cathode Current vs. Cathode Voltage

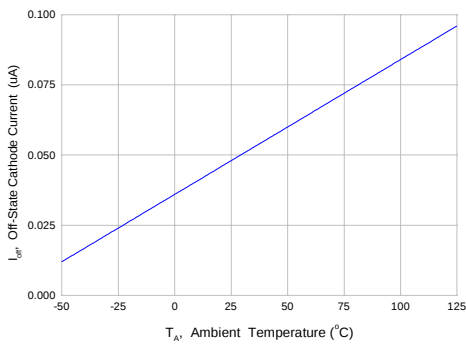


Figure 6. OFF-State Cathode Current vs. Ambient Temperature

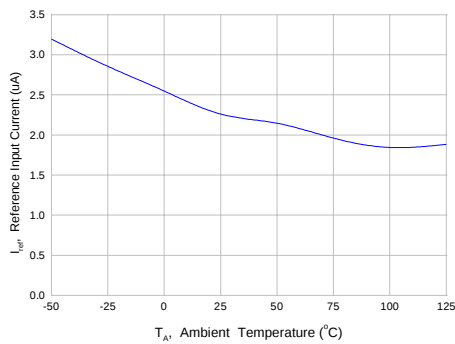


Figure 7. Reference Input Current vs. Ambient Temperature

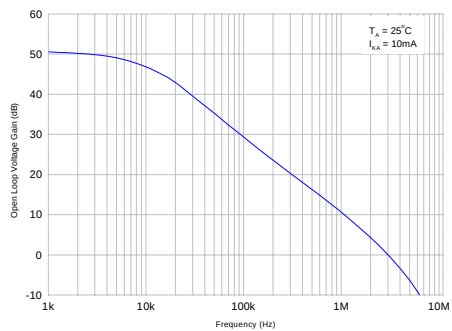


Figure 8. Small Signal Voltage Amplification vs. Frequency

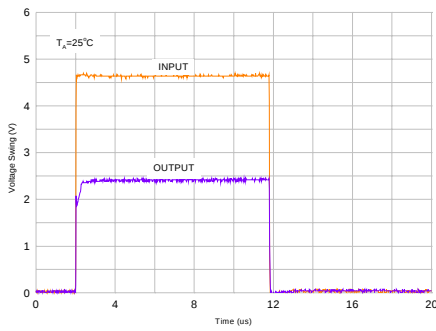


Figure 9. Pulse Response

Typical Performance Characteristics (Continued)

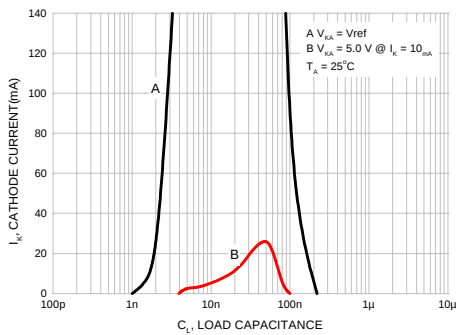


Figure 10. Stability Boundary Conditions

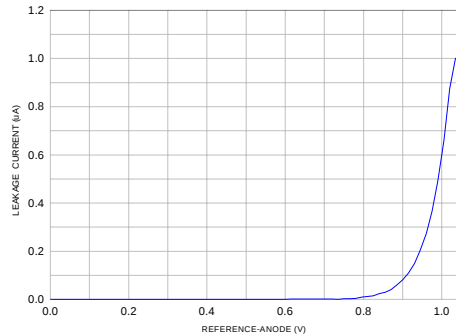


Figure 11. Reference-Anode Diode Curve

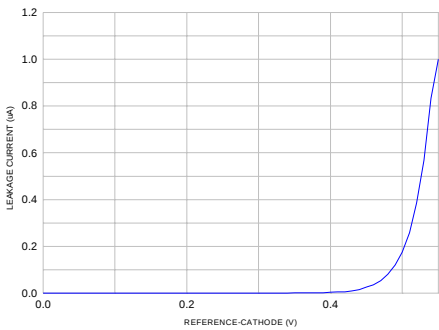


Figure 12. Reference-Cathode Diode Curve

Typical Application

$$V_O = \left(1 + \frac{R_1}{R_2}\right) V_{\text{ref}}$$

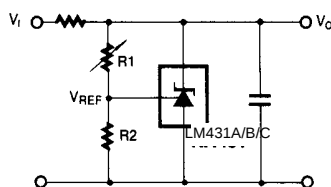


Figure 13. Shunt Regulator

$$V_O = V_{\text{ref}} \left(1 + \frac{R_1}{R_2}\right)$$

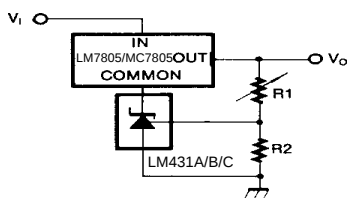


Figure 14. Output Control for Three-Terminal Fixed Regulator

$$V_O = \left(1 + \frac{R_1}{R_2}\right) V_{\text{ref}}$$

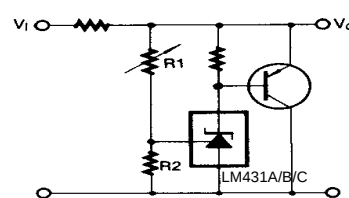


Figure 15. High Current Shunt Regulator

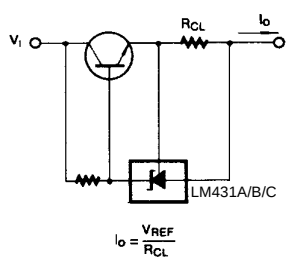


Figure 16. Current Limit or Current Source

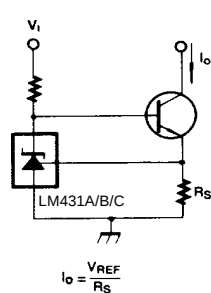


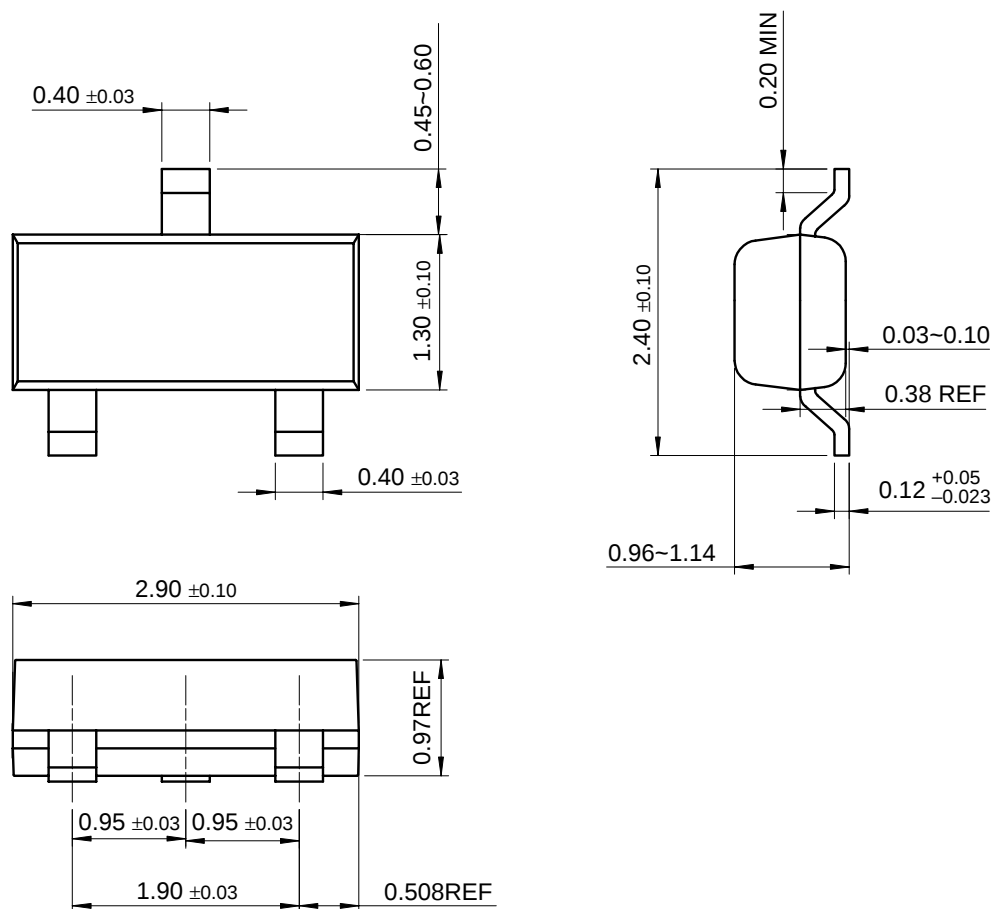
Figure 17. Constant-Current Sink

Mechanical Dimensions

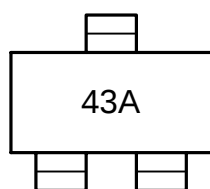
Package

Dimensions in millimeters

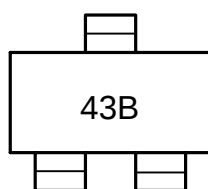
SOT-23F



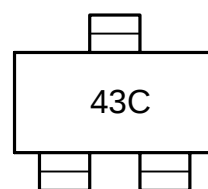
Marking



2% tolerance



1% tolerance



0.5% tolerance

Ordering Information

Product Number	Output Voltage Tolerance	Package	Operating Temperature
LM431SCCML	0.5%	SOT-89	-25 ~ +85 °C
LM431SCCMF		SOT-23F	
LM431SBCML	1%	SOT-89	
LM431SBCMF		SOT-23F	
LM431SACML	2%	SOT-89	
LM431SACMF		SOT-23F	

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