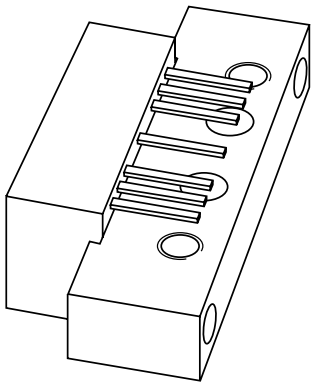
DATA SHEET



BGY67A
200 MHz, 24 dB gain reverse amplifier

Product specification
Supersedes data of 1997 Apr 09

2001 Oct 18

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200 MHz, 24 dB gain reverse amplifier BGY67A

FEATURES

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- TiPtAu metallized crystals ensure optimal reliability.

DESCRIPTION

Hybrid amplifier module for CATV systems operating over a frequency range of 5 to 200 MHz at a voltage supply of +24 V (DC). The device is intended as a reverse amplifier for use in two way systems.

PINNING - SOT115J

PIN	DESCRIPTION
1	input
2	common
3	common
5	$+V_B$
7	common
8	common
9	output

PIN CONFIGURATION

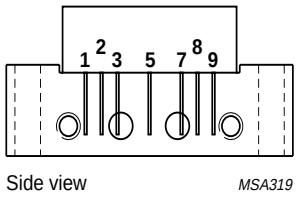


Fig.1 Simplified outline.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G_p	power gain	f = 10 MHz	23.5	–	24.5	dB
I_{tot}	total current consumption (DC)	V_B = +24 V	–	215	230	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V_i	RF input voltage	–	65	dBmV
T_{stg}	storage temperature	–40	+100	°C
T_{mb}	mounting base operating temperature	–20	+90	°C

CHARACTERISTICS

Table 1 Bandwidth 5 to 200 MHz; T_{mb} = 30 °C; $Z_S = Z_L$ = 75 Ω

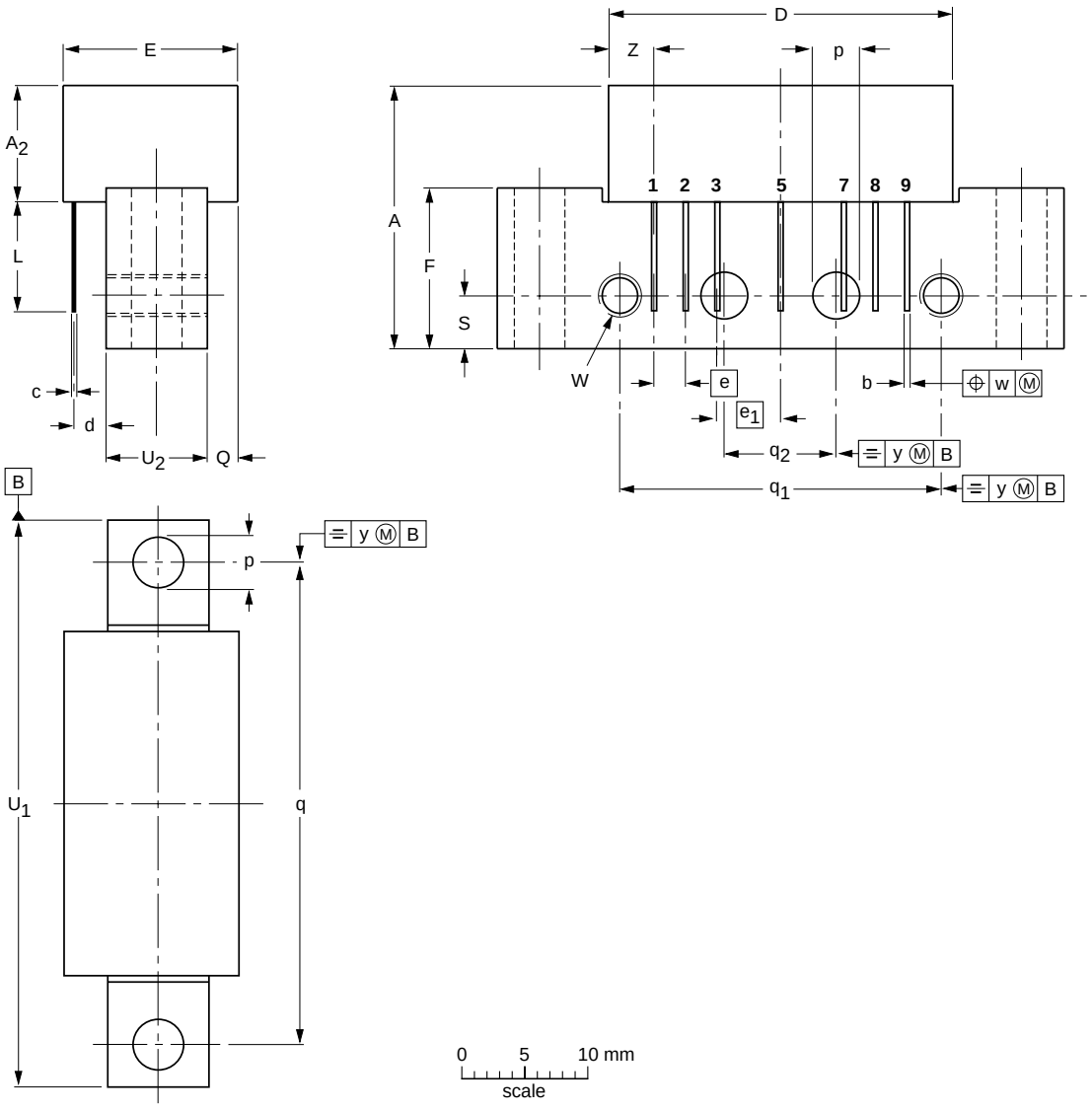
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G_p	power gain	f = 10 MHz	23.5	–	24.5	dB
SL	slope cable equivalent	f = 5 to 200 MHz	–0.2	–	+0.5	dB
FL	flatness of frequency response	f = 5 to 200 MHz	–	–	±0.2	dB
S_{11}	input return losses	f = 5 to 200 MHz	20	–	–	dB
S_{22}	output return losses	f = 5 to 200 MHz	20	–	–	dB
CTB	composite triple beat	22 channels flat; V_o = 50 dBmV; measured at 175.25 MHz	–	–	–67	dB
X_{mod}	cross modulation	22 channels flat; V_o = 50 dBmV; measured at 55.25 MHz	–	–	–59	dB
d_2	second order distortion	V_o = 50 dBmV; note 1	–	–	–67	dB
V_o	output voltage	d_{im} = –60 dB; note 2	67	–	–	dBmV
		d_{im} = –60 dB; note 3	64	–	–	dBmV
F	noise figure	f = 200 MHz	–	–	5.5	dB
I_{tot}	total current consumption	DC value; V_B = +24 V; note 4	–	215	230	mA

Notes

1. f_p = 83.25 MHz; V_p = 50 dBmV;
   f_q = 109.25 MHz; V_q = 50 dBmV;
   measured at $f_p + f_q$ = 192.5 MHz.
2. Measured according to DIN45004B;
   f_p = 35.25 MHz; $V_o = V_p$;
   f_q = 42.25 MHz; $V_q = V_o$ –6 dB;
   f_r = 44.25 MHz; $V_r = V_o$ –6 dB;
   measured at $f_p + f_q - f_r$ = 33.25 MHz.
3. Measured according to DIN45004B;
   f_p = 187.25 MHz; $V_o = V_p$;
   f_q = 194.25 MHz; $V_q = V_o$ –6 dB;
   f_r = 196.25 MHz; $V_r = V_o$ –6 dB;
   measured at $f_p + f_q - f_r$ = 185.25 MHz.
4. The module normally operates at V_B = +24 V, but is able to withstand supply transients up to +30 V.

PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads **SOT115J**



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A_2 max.	b	c	D max.	d max.	E max.	e	e_1	F	L min.	p	Q max.	q	q_1	q_2	S	U_1 max.	U_2	W	w	y	Z max.
mm	20.8	9.1	0.51 0.38	0.25	27.2	2.54	13.75	2.54	5.08	12.7	8.8	4.15 3.85	2.4	38.1	25.4	10.2	4.2	44.75	8	6-32 UNC	0.25	0.1	3.8

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT115J						99-02-06

DATA SHEET STATUS

DATA SHEET STATUS[1]	PRODUCT STATUS[2]	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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NOTES

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