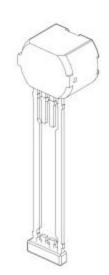
## ATS645LSH

### Preliminary – Subject to Change

PRELIMINARY DATASHEET (subject to change without notice)



Pin 1: Supply
Pin 2: No connection
Pin 3: Test pin
Pin 4: Ground

#### ABSOLUTE MAXIMUM RATINGS

Supply Voltage,
V <sub>CC</sub>
Reverse Supply Voltage,
V <sub>R</sub>
Operating Temperature Range,
T <sub>A</sub> —40 °C to 150 °C*
Storage Temperature,
T <sub>S</sub>
Package Power Rating,
ϑ <sub>JA</sub>
Maximum Junction Temperature,
T <sub>Jmax</sub>

\* Operation at increased supply voltages with external circuitry is described in the Applications Information.

# True Zero Speed Miniature Gear Tooth Sensor

The ATS645LSH is an optimized Hall effect sensing integrated circuit and magnet combination that provides a user-friendly solution for true zero-speed digital gear-tooth sensing in two-wire applications. The sensor consists of a single-shot molded plastic package that includes a samarium cobalt magnet, a pole piece, and a Hall effect IC that has been optimized to the magnetic circuit. This small package can be easily assembled and used in conjunction with a wide variety of gear shapes and sizes.

The integrated circuit incorporates a dual element Hall effect sensor and signal processing that switches in response to differential magnetic signals created by ferrous gear teeth. The circuitry contains a sophisticated digital circuit to eliminate magnet and system offsets and to achieve true zero speed operation (ref U.S. Patent 5,917,320). A-D and D-A converters are used to adjust the device gain at power up and to allow air gap independent switching.

The regulated current output is configured for two wire applications and the sensor is ideally suited for obtaining speed and duty cycle in ABS and transmission application gear-tooth-based configurations.

#### **FEATURES**

- Fully optimized differential digital gear tooth sensor
- Single chip sensing IC for high reliability
- Internal current regulator for 2 wire operation
- Small mechanical size (8 mm dia x 5.5 mm length)
- · Air gap independent switch points
- Digital output representing gear profile
- Precise duty cycle signal with temperature
- Large operating air gaps
- Automatic Gain Control (AGC)
- Automatic Offset Adjustment circuit
- True zero speed operation
- Under-voltage lockout
- Wide operating voltage range
- Defined power-on state



#### **CHARACTERISTICS**

Valid over operating temperature range and Supply Voltage within specification unless otherwise noted.

Characteristics	Symbol	Test Conditions		Lir	mits	
Citatacteristics	Symbol	rest Conditions	Min.	Тур.	Max.	Units
ELECTRICAL CHARACTERIST	ICS					
Supply Voltage	V <sub>CC</sub>	Operating, T <sub>J</sub> < 165°C	4.0		24	٧
Under Voltage Lockout	V <sub>CC(UV)</sub>	$V_{CC} \ 0 \rightarrow 5 \ V$	-	-	<vcc<sub>Min</vcc<sub>	٧
<sup>1</sup> Supply Zener Clamp Voltage	Vz	I <sub>ZT</sub> = 1 mA	28	32	-	٧
Max Zener Pulse Current	lz	t=20mS Pulse Mode	-	-	50	mA
Supply Zener Resistance	R <sub>Z</sub>		-	50	-	Ω
Supply Current	Icc	Low Current State: Icc <sub>Low</sub>	4.0	6	8.0	mA
ATS645LSH – I1		High Current State: Icc <sub>High</sub>	12.0	14.0	16.0	mA
Supply Current	1	Low Current State: Icc <sub>Low</sub>	5.9	7	8.4	mA
ATS645LSH – I2	I <sub>CC</sub>	High Current State: Icc <sub>High</sub>	11.8	14.0	16.8	mA
POWER-ON STATE CHARACT	ERISTICS					
Power-On State	S <sub>PO</sub>	$V_{CC} \ 0 \rightarrow 5 \ V$	-	Icc <sub>High</sub>	-	-
<sup>2</sup> Power-On Time	t <sub>on</sub>	Gear speed < 100 rpm	-	1	2	ms
OUTPUT STAGE						
Output Current Slew Rate	I <sub>R</sub>	$lcc_{High} \rightarrow lcc_{Low}, lcc_{Low} \rightarrow lcc_{High},$ $R_S = 100 \ \Omega, \ C_S = 10 \ pF, \ 10 \ to \ 90\%$		10		mA/μs

<sup>&</sup>lt;sup>2</sup> Power On Time is the time required to complete internal offset adjust. It does not include automatic gain control, which requires three tooth valley transitions to complete and is therefore RPM dependent.



<sup>&</sup>lt;sup>1</sup> The zener is tested using a pulse method and is designed for transient protection, continuous operation may destroy the device.

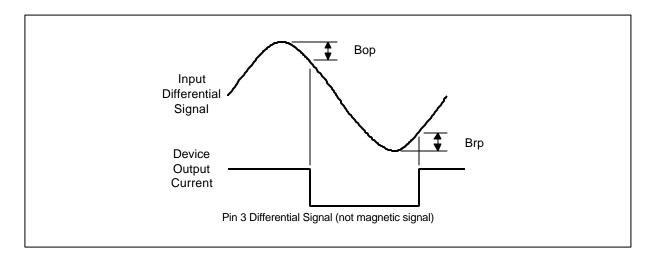
Operating Characteristics: Valid with Reference Target unless otherwise specified

Characteristics	Symbol	Test Conditions	Limits			
Citatacteristics	Syllibol	rest Conditions		Тур.	Max.	Units
SWITCH POINT CHARACTERISTICS						
Rotation Speed	S <sub>max</sub>	Using Reference Target over Operating Air Gap Range	0		8K	RPM
Analog Signal Bandwidth	f-3db	-3dB Point	20	40		KHz
<sup>1</sup> Operate Point	Вор	Icc <sub>High</sub> → Icc <sub>Low</sub> Positive Peak referenced, AG < AG <sub>max</sub>	-	100	150	mV
<sup>1</sup> Release Point	Brp	Icc <sub>Low</sub> → Icc <sub>High</sub> Negative Peak referenced, AG < AG <sub>max</sub>	-	100	150	mV

Calibration				
Initial Calibration	Cı	Number of Rising Mechanical Edges for Accurate Edge Detection	3	Edges

DAC Characteristics				
Allowable User Induced Differential Offset	Output switching only; may not meet data sheet specifications	-60	60	G

#### **SWITCH POINT DETAIL**



<sup>&</sup>lt;sup>1</sup> Bop and Brp should be adjustable with metal mask changes, limits max air gap but improves vibration immunity.



#### Operating Characteristics:

Valid only if magnetic offset is within the Dynamic Offset Compensation DAC Range as specified above

Characteristics	Symbol	Test Conditions	Limits				
	Symbol	Symbol Test Conditions	Min.	Тур.	Max.	Units	
OPERATING CHARACTERISTICS: Using Reference Target and Valid Over Operating Temperature Range							
<sup>1</sup> Operational Air Gap Range	Op <sub>AG</sub>	Duty cycle within specification	0.5		2.75	mm	
Switching Air Gap Range	Op <sub>MaxAG</sub>	Output Switching: Duty cycle Not in Specification	3	-	-	mm	
Duty Cycle Variation	DC	Wobble < 0.5mm Typical value at 1.5mm air gap Valid over operating air gap range	37	53	57	%	
<sup>2</sup> Operating Signal Range	Sig	Duty cycle within Specification Wobble < 0.5mm	30	-	1000	G	
<sup>3</sup> Minimum Operating Signal	Sig <sub>Min</sub>	Output Switching: Duty Cycle Not in Specification	20	-	-	G	

<sup>1</sup> Operating air gap is dependent on the available magnetic field. The available field is target geometry and materiel dependent and should be independently characterized. The field available from the reference target is given in the reference gear parameter section of the datasheet

<sup>&</sup>lt;sup>3</sup> Duty cycle is not guaranteed to be in specification. Reference the Duty Cycle vs. Air Gap Over Temperature graph in the typical operating characteristics section of this document.

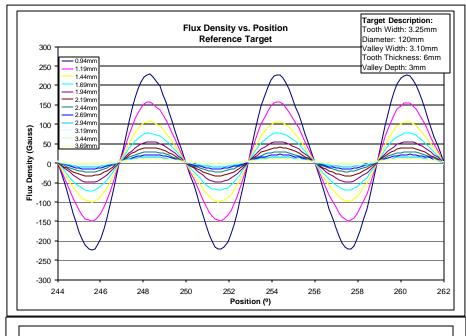


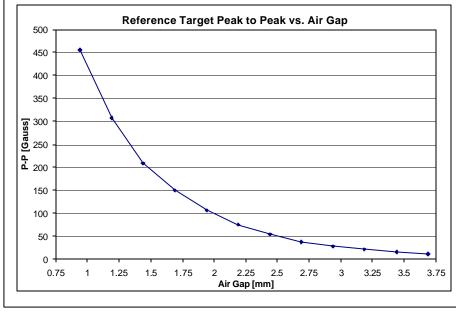
Revision 1.2; MHN; 07.Aug.02

<sup>&</sup>lt;sup>2</sup> In order to remain in specification the magnetic signal must be larger than the minimum value specified, this includes the effect of target wobble.

#### Reference Gear Parameters

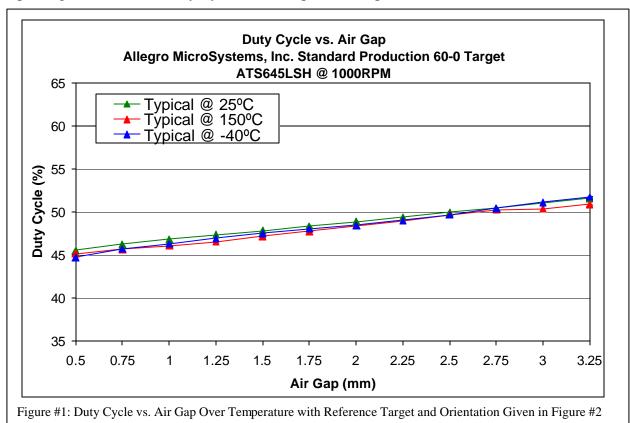
REFERENCE GEAR DIMENSIONS (60-0)						
Diameter	G <sub>d</sub>		-	120	-	mm
Thickness	Gt		-	6	-	mm
Tooth Width	T <sub>w</sub>		-	3	-	mm
Valley Width	V <sub>w</sub>		-	3	-	mm
Valley Depth	V <sub>d</sub>		-	3	-	mm
Materiel		Low Carbon Steel				

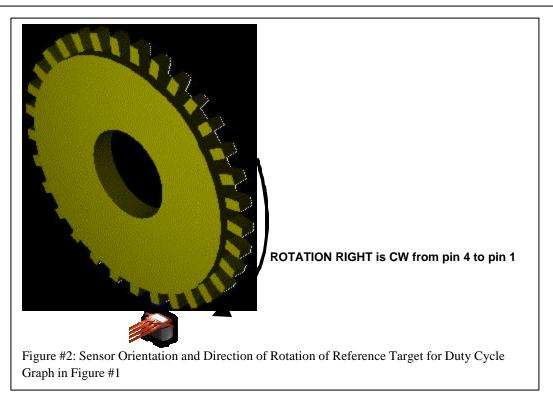






Typical Operating Characteristics: Duty Cycle vs. Air Gap Over Temperature





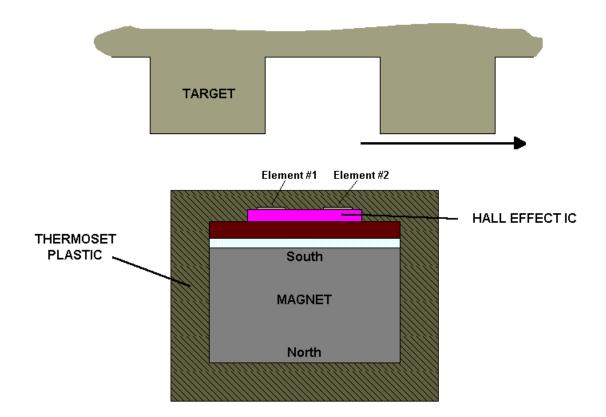
#### SENSOR DESCRIPTION

#### **Assembly Description:**

The ATS645LSH true zero speed gear tooth sensor is a Hall IC/magnet configuration that is fully optimized to provide digital detection of gear tooth edges. This sensor is integrally molded into a plastic body that has been optimized for size, ease of assembly, and manufacturability. High operating temperature materials are used in all aspects of construction.

#### **Sensing Technology:**

The gear tooth sensor sub-assembly contains a single-chip differential Hall effect sensor IC, a Samarium Cobalt magnet, and a flat ferrous pole piece. The Hall IC consists of 2 Hall elements spaced 1.5 mm apart that measure the magnetic gradient created by the passing of a ferrous object. The two elements measure the magnetic gradient and convert it to an analog voltage that is then processed to provide a digital output signal.





#### **Transient Performance per ISO 7637-1**

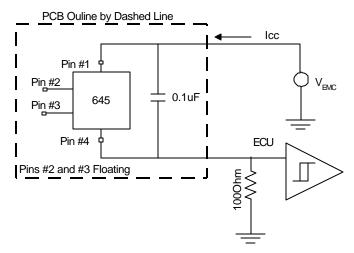
at  $T_A = 23 \pm 5$ °C

(Tested at Allegro test facility – for engineering reference only)

		Performance Class at Test Level				
Pulse No.	Test	I	II	III	IV	
1	Inductive turn off (negative)	-	-	-	С	
2	Inductive turn off (positive)	-	-	-	Α	
3a	Capacitive/inductive coupling (neg)	-	-	-	Α	
3b	Capacitive/inductive coupling (pos)	-	-	-	Α	
4	Reverse battery	-	-	-	Α	
5	Load dumb	А	С	С	E	
6	Ignition coil disconnect	-	-	С	E	
7	Field decay (negative)	-	-	-	С	

 $U_A = 13.5V$  for pulses 1, 2, 3a, 3b, 5, 6, 7 and 12V for pulse 4.

#### Test Circuit #1



**Transient & EMC test circuit** 

Power supply voltage transients, or device output short circuits, may be caused by faulty connectors, crimped wiring harnesses, or service errors. The prevent catastrophic failure, internal protection against over-voltage, reverse voltage, and output overloads have been incorporated to meet the automotive 12 volt system protection requirements of ISO DP7637/1. A series-blocking diode or current-limiting resistor is required in order to survive pulse number six.

#### **Performance Class Definitions**

(for transient performance and EMC performance)

- A All functions of a device perform as designed during and after the exposure.
- B All functions of a device perform as designed during exposure; however, one or more of them may go beyond the specified limit tolerance. All functions return automatically to within normal limits after exposure is removed. Memory must remain Class A.
- C One or more functions of the device do not perform as designed during exposure but return automatically to normal operation after exposure is removed.
- D One or more functions of the device do not perform as designed during exposure and do not return to normal operation until exposure is removed and the device is reset by simple "operator" action.
- E One or more functions of the device do not perform as designed during and after the exposure and cannot be returned to proper operation without repairing or replacing the device.



#### **ELECTROMAGNETIC COMPATIBILITY (EMC) PERFORMANCE**

at  $T_A = 23 \pm 5$ °C

(Tested at Allegro test facility – for engineering reference only)

#### **TEM Cell Performance per ISO 11452-3**

Test severity level I = 50 V/m II = 100 V/m III = 150 V/m IV = 200 V/m

Frequency Band	Performance Class at Test Level					
(MHz)	I	II	III	IV		
F1 (0.01 to 10)	_	_	_	А		
F2 (0 to 30)	_	_	_	А		
F3 (30 to 80)	_	_	_	А		
F4 (80 to 200)	_	A	В	В		

#### **BCI per ISO 11452-4**

Test severity level I = 25 mA II = 50 mA III = 75 mA IV = 100 mA

Frequency Band		Performance Class at Test Level				
(MHz)	I	II	III	IV		
F1 (1 to 10)	_	_	_	Α		
F2 (10 to 30)	_	_	_	Α		
F3 (30 to 80)	_	_	_	Α		
F4 (80 to 200)	_	_	_	Α		
F5 (200 to 400)	_	_	_	Α		

Direct RF per ISO 11452-7

Test severity level I = 100 mW II = 200 mW III = 300 mW IV = 400 mW V = 500 mW

Frequency Band (MHz)	ı	Performance 1a at Test Level IV	V
F1 (1 to 10)		7 0 4 7	
F2 (10 to 30)			
F3 (30 to 80)		- $        -$	
F4 (80 to 200)			
F5 (200 to 400)			
			•



#### **MECHANICAL INFORMATION**

Component	Material	Function	Value
Sensor Package Material	Thermoset Epoxy	Max. Temperature	170°C <sup>1</sup>
Leads	Copper, 0.016" dia, 0.050" spacing		
Lead Coating	Solder, Tin / Lead 90/10 <sup>2</sup>		

Temperature excursions of up to 225°C for 2 minutes or less are permitted.

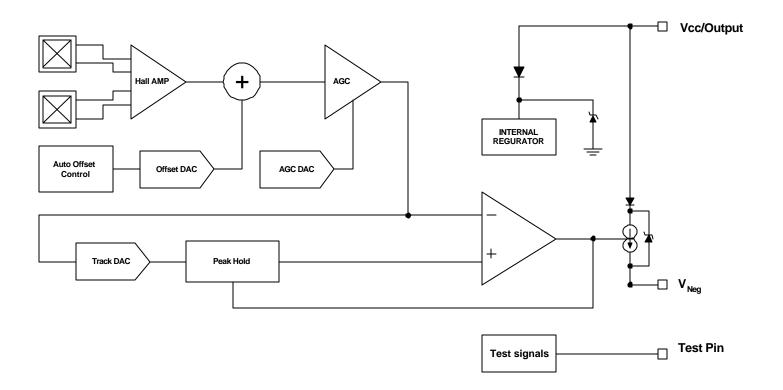
#### **DEVICE QUALIFICATION PROGRAM**

Test Name	Test Conditions	Test Length	# of Lots	Sample / lot	Comments
High Temperature	AEC-Q100 #2 (JA108)	408 hrs	1	77	
Operating Life	Ta = 150°C, Tj = 165°C				
High Temperature Bake	AEC-Q100 #3 (JA103) Ta = 170°C	1000 hrs	1	77	
Temperature Humidity Bias	AEC-Q100 #5 (JA101) 85°C/85%RH	1008 hrs	1	77	
Autoclave	AEC-Q100 #6 (JA102)	96 hrs	1	77	
Temperature Cycling	AEC-Q100 #7(JA104)	500 cycles	1	//	-65°C to +160°C, 30 sec. transition, 30 minute dwell
ESD	AEC-Q100 #20 (AEC-Q100- 002, AEC-Q100-003)	-	1		Passes ±6kV all pin combinations: Human Body Model
Early Life Failure Rate	AEC-Q100 #25 (AEC-Q100- 008)	150°C/24 hrs	1	800	

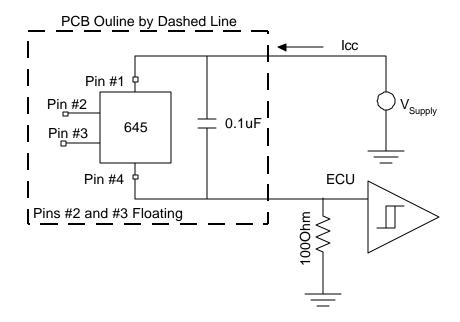


<sup>&</sup>lt;sup>2</sup> Industry accepted soldering techniques are acceptable for this sub-assembly as long as the indicated maximum temperatures for each component are not exceeded.

#### Functional block diagram



### **Typical Application Circuit**





#### **SENSOR PACKAGE**

### LSH PACKAGE REFERENCE DIMENSIONS ONLY

