NEW PRODUCT BRIEF

73M2902

TDK Semiconductor's ADVANCED SINGLE-CHIP MODEM WITH ERROR CORRECTION

Description

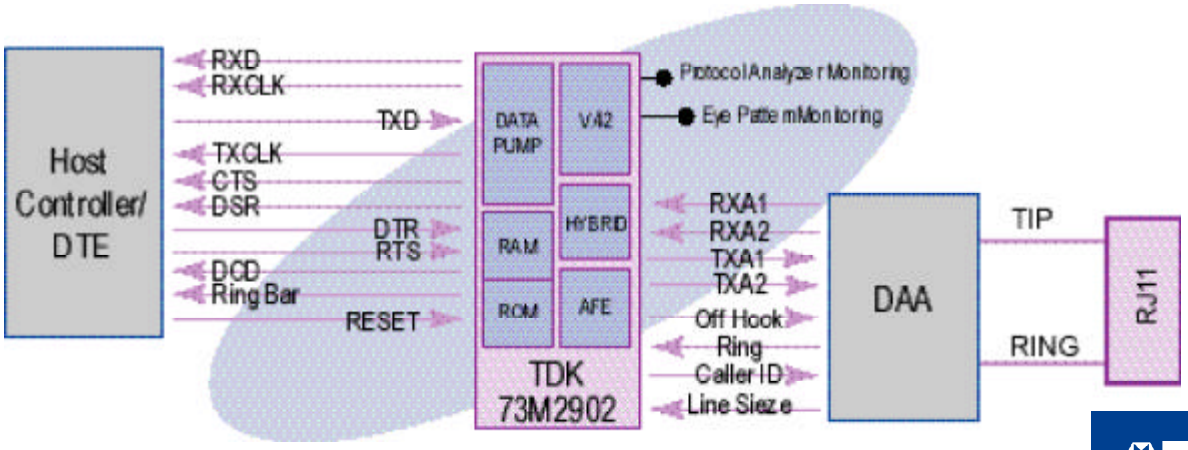
The TDK 73M2902 is a single chip modem capable of V.22bis, V.22, V.23 PAVI, and V.21 operation with V.42 (LAPM) Error Correction on-chip. The 73M2902 consists of a DSP core, analog front end (AFE), on-chip hybrid driver, and all the ROM and RAM necessary to operate a V.22bis/V.42 error correcting modem.

The 73M2902 supports both asynchronous and synchronous serial DTE interfaces (UART/USART). The 73M2902 is driven by "AT" commands from the data terminal equipment (DTE). An eye pattern generator circuit is available for monitoring line quality.

The TDK 73M2902 was specifically designed to easily embed into applications, such as set top boxes, without the need for external error correction software; and the feature set is tailored to meet the needs of both US and European digital broadcast service providers. User friendly features such as country specific (and CTR 21) call progress tone detection, line-in-use and parallel-pick-up detection, and caller-ID make the 73M2902 simple to use.

Features

- True single chip error correcting modem
- On chip analog front end (AFE)
- Data modem standards:
  V.22bis (2400bps), V.22 (1200bps),
  V.23 PAVI (1200/75bps), V.21 (300bps),
  and V.42(LAPM) Error correction
- "AT" command set
- Country specific and CTR 21 call progress support
- Caller ID
- On chip 2-4 wire hybrid and off hook relay drivers
- Supports auto dial/answer, tone and pulse dialing
- Serial Interfaces: Asynchronous (300-9600bps) and Synchronous (2400, 1200bps)
- Speaker output
- Programmable call progress detectors
- Receive Signal Level: -10dBm to -40dBm
- Line-in-use and parallel-pick-up detection interfaces
- Operating voltage: 3.3V±10%
- Easily interfaces with silicon DAAs (reference designs available)
- 100 pin QFP package



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