

8M X 32 Bits (32MB) 72-Pin Flash SO-DIMM (3.3V Only)

FEATURES

- Access Time of 100ns and 120ns
- TTL compatible inputs and outputs
- 3.3V only power supply
- JEDEC standard components
- Tin (option "T") or gold (option "G") edge connectors
- Three power on reset options
 - No power on reset: /RST tied to V_{CC} (Option "V")
 - Power on reset: /RST tied to power supervisor circuit (Option "R")
 - System control of power on reset: /RST tied to pin 70 (Option "S")
- Intel Part Number RC28F320J3A flash memory components
- Minimum 3,200,000 total write/erase cycles

PIN CONFIGURATION

Pin Symbols

Pin	Symbol	Pin	Symbol	Pin	Symbol
1	V _{SS}	25	DQ ₂₂	49	DQ ₈
2	DQ ₀	26	DQ ₇	50	DQ ₂₄
3	DQ ₁₆	27	DQ ₂₃	51	DQ ₉
4	DQ ₁	28	A ₇	52	DQ ₂₅
5	DQ ₁₇	29	A ₁₂	53	DQ ₁₀
6	DQ ₂	30	V _{CC}	54	DQ ₂₆
7	DQ ₁₈	31	A ₈	55	DQ ₁₁
8	DQ ₃	32	A ₉	56	DQ ₂₇
9	DQ ₁₉	33	A ₂₂ *	57	DQ ₁₂
10	V _{CC}	34	A ₂₃ *	58	DQ ₂₈
11	A ₁₀	35	A ₁₃	59	V _{CC}
12	A ₀	36	A ₁₄	60	DQ ₂₉
13	A ₁	37	A ₁₅	61	DQ ₁₃
14	A ₂	38	A ₁₆	62	DQ ₃₀
15	A ₃	39	V _{SS}	63	DQ ₁₄
16	A ₄	40	/CE ₀	64	DQ ₃₁
17	A ₅	41	A ₂₁	65	DQ ₁₅
18	A ₆	42	NC	66	A ₁₉
19	A ₁₁	43	/CE ₁	67	PD ₁
20	DQ ₄	44	/WE ₀	68	PD ₂
21	DQ ₂₀	45	/WE ₁	69	PD ₃
22	DQ ₅	46	A ₁₇	70	/RST
23	DQ ₂₁	47	/OE	71	A ₂₀
24	DQ ₆	48	A ₁₈	72	V _{SS}

* Not Used.

GENERAL DESCRIPTION

This SiliconTech product is a 8M x 32 bits flash Small Outline Dual In-line Memory Module (SO-DIMM). This module consists of eight 4M x 8 bits CMOS flash memory in Easy BGA packages mounted on a 72-pin glass epoxy substrate. Decoupling capacitors of 0.1μF are also mounted.

Option "T" provides tin edge connectors and option "G" provides gold edge connectors. Option "R" provides power on reset through a power supervisor circuit; option "S" provides system control of power on reset by connecting the reset signals to Pin 70; and, option "V" provides no power on reset.

The module is intended for mounting into 72-pin SO-DIMM edge connector sockets keyed for 3.3V power supply. The module uses the standard programming algorithms for Intel RC28F320J3A StrataFlash memory components.

Pin Functions

Pin Symbol	Pin Function
A ₀ -A ₂₁	Address Inputs
DQ ₀ -DQ ₃₁	Data In/Out
/CE ₀ , /CE ₁	Chip Enable
/WE ₀ , /WE ₁	Write Enable
/OE	Output Enable
PD ₁ -PD ₃	Presence Detect
/RST	Reset
V _{CC}	Power (+3.3V)
V _{SS}	Ground
NC	Not Connected

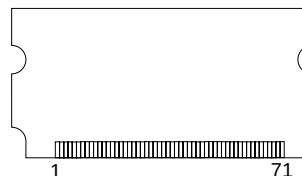
Presence Detect Pins*

Pin Name	Signal
PD ₁	TBD
PD ₂	TBD
PD ₃	TBD

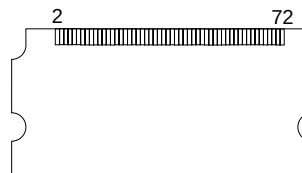
* Pin Connection Changing Available.

Pin Locations

Odd numbered pins
from pin 1 to pin 71
on front

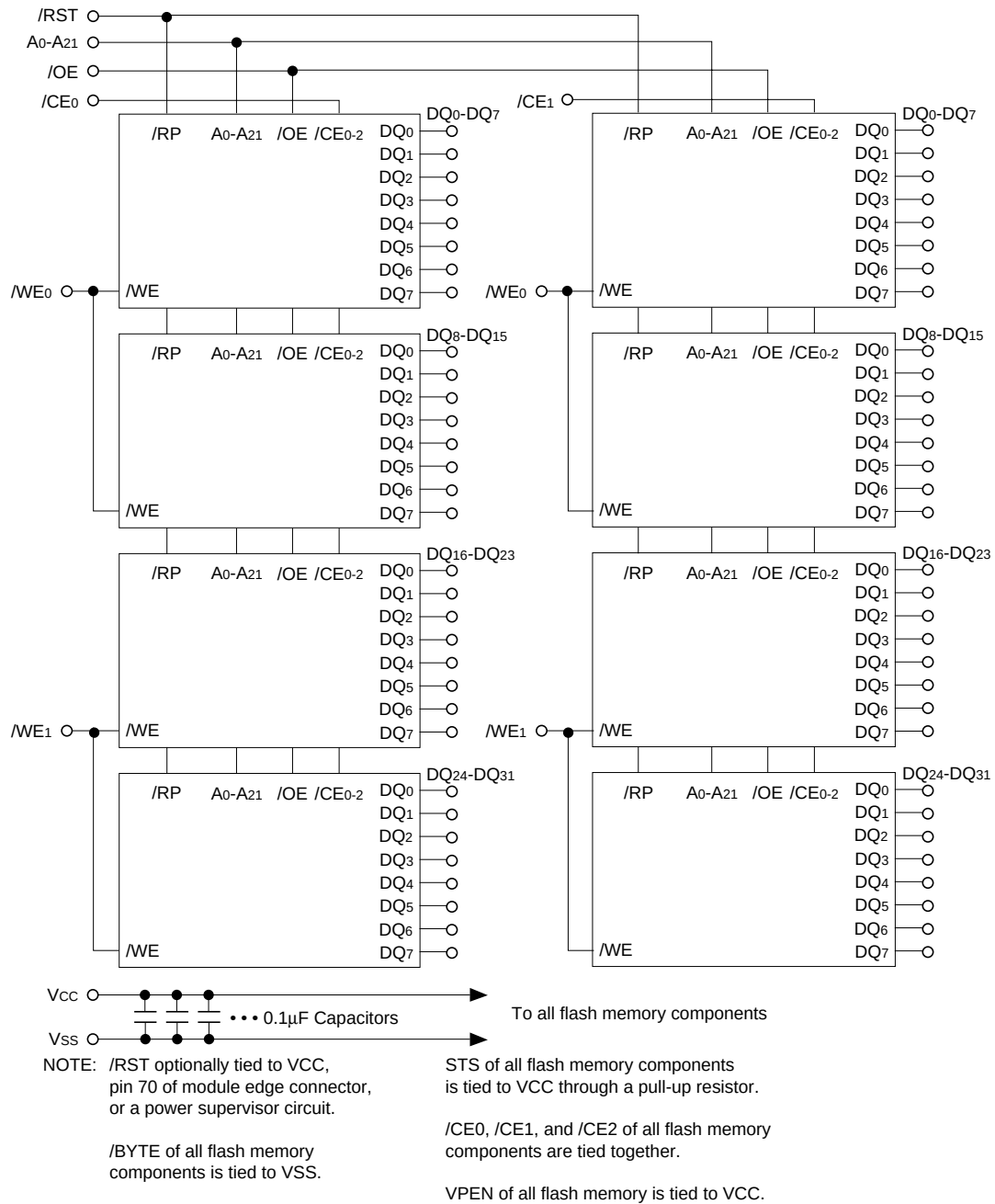


Even numbered pins
from pin 2 to pin 72
on back

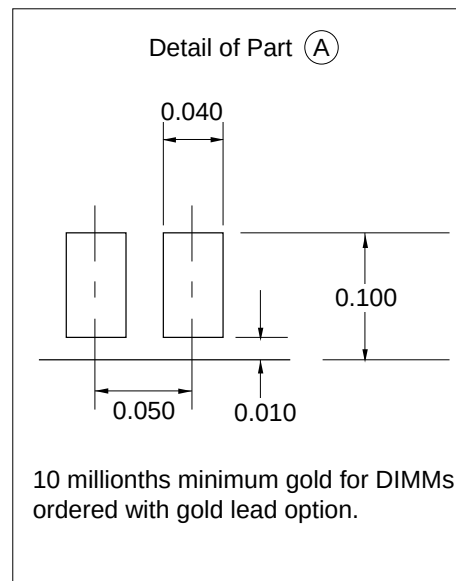
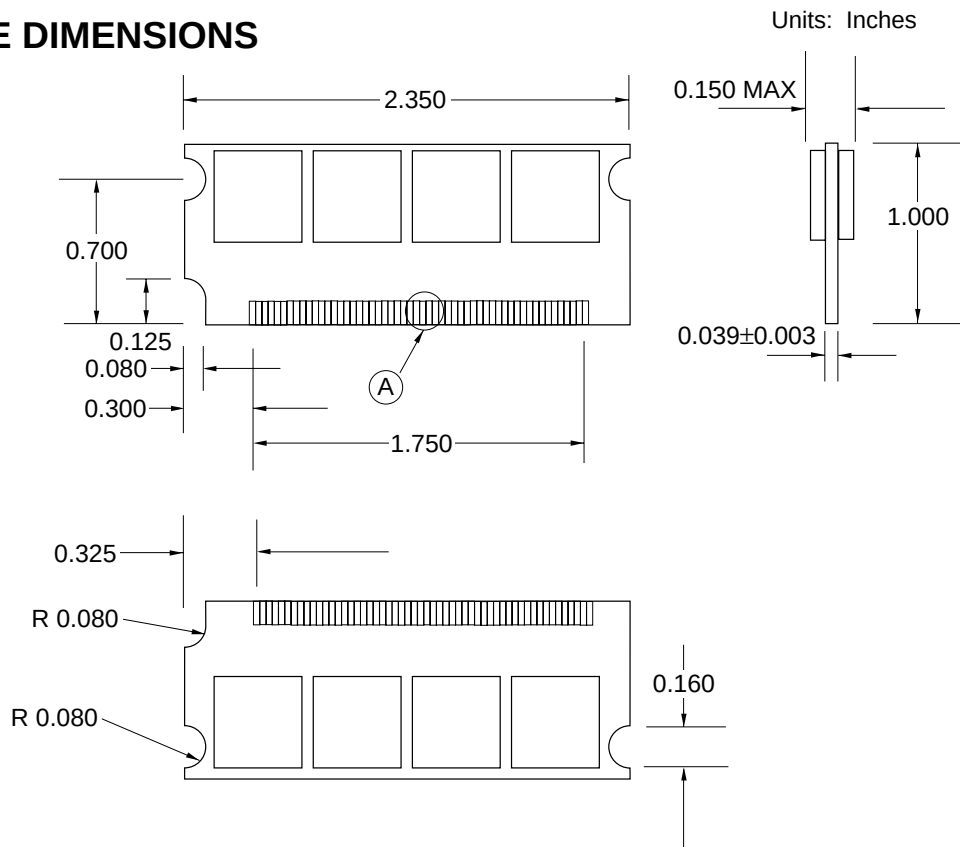


[†] "Advanced" indicates that this product is in design engineering and that it may or may not be moved into production. Parameters may change.

FUNCTIONAL BLOCK DIAGRAM



PACKAGE DIMENSIONS



TBD

TOLERANCES: ±0.005 UNLESS OTHERWISE SPECIFIED