## INTEGRATED CIRCUITS

# DATA SHEET

## 74LVT162245B

3.3V LVT 16-bit transceiver with  $30\Omega$  termination resistors (3-State)

Product specification Supersedes data of 1995 Aug 22 IC23 Data Handbook





## 3.3V 16-bit transceiver with 30 $\Omega$ termination resistors (3-State)

## 74LVT162245B

#### **FEATURES**

- 16-bit bidirectional bus interface
- 3-State buffers
- Output capability: +12mA/-12mA
- TTL input and output switching levels
- Input and output interface capability to systems at 5V supply
- Bus-hold data inputs eliminate the need for external pull-up resistors to hold unused inputs
- Live insertion/extraction permitted
- Outputs include series resistance of 30Ω making external termination resistors unnecessary
- Power-up 3-State
- No bus current loading when output is tied to 5V bus
- Latch-up protection exceeds 500mA per JEDEC Std 17
- ESD protection exceeds 2000V per MIL STD 883 Method 3015 and 200V per Machine Model
- Same part as 74LVT16245B-1

#### **DESCRIPTION**

The 74LVT162245B is a high-performance BiCMOS product designed for  $V_{CC}$  operation at 3.3V.

This device is a 16-bit transceiver featuring non-inverting 3-State bus compatible outputs in both send and receive directions. The control function implementation minimizes external timing requirements. The device features an Output Enable (nOE) input for easy cascading and a Direction (DIR) input for direction control.

The 74LVT162245B is designed with  $30\Omega$  series resistance in both the High and Low states of the output. This design reduces line noise in applications such as memory address drivers, clock drivers, and bus transceivers/transmitters.

The 74LVT162245B is the same as the 74LVT16245B-1. The part number has been changed to reflect industry standards.

### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS T <sub>amb</sub> = 25°C	TYPICAL	UNIT
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay nAx to nBx or nBx to nAx	$C_L = 50pF;$ $V_{CC} = 3.3V$	2.5	ns
C <sub>IN</sub>	Input capacitance DIR, OE	V <sub>I</sub> = 0V or 3.0V	3	pF
C <sub>I/O</sub>	I/O pin capacitance	V <sub>I/O</sub> = 0V or 3.0V	9	pF
I <sub>CCZ</sub>	Total supply current	Outputs disabled; V <sub>CC</sub> = 3.6V	70	μΑ

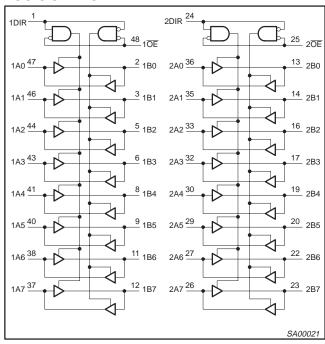
### ORDERING INFORMATION

PACKAGES	TEMPERATURE RANGE	OUTSIDE NORTH AMERICA	NORTH AMERICA	DWG NUMBER
48-Pin Plastic SSOP Type III	-40°C to +85°C	74LVT162245B DL	VT162245B DL	SOT370-1
48-Pin Plastic TSSOP Type II	–40°C to +85°C	74LVT162245B DGG	VT162245B DGG	SOT362-1

## PIN DESCRIPTION

PIN NUMBER	SYMBOL	NAME AND FUNCTION
1, 24	nDIR	Direction control input
47, 46, 44, 43, 41, 40, 38, 37, 36, 35, 33, 32, 30, 29, 27, 26	nA0 – nA7	Data inputs/outputs (A side)
2, 3, 5, 6, 8, 9, 11, 12, 13, 14, 16, 17, 19, 20, 22, 23	nB0 – nB7	Data inputs/outputs (B side)
25, 48	nŌĒ	Output enable input (active-Low)
4, 10, 15, 21, 28, 34, 39, 45	GND	Ground (0V)
7, 18, 31, 42	V <sub>CC</sub>	Positive supply voltage

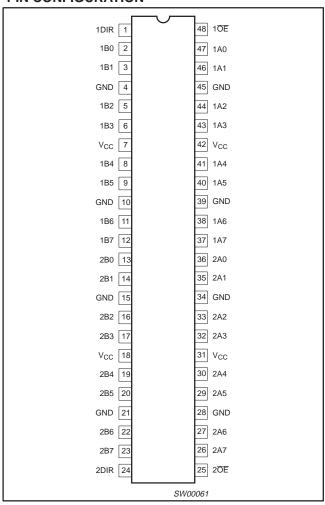
## LOGIC SYMBOL



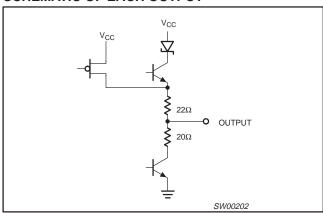
## 3.3V 16-bit transceiver with $30\Omega$ termination resistors (3-State)

## 74LVT162245B

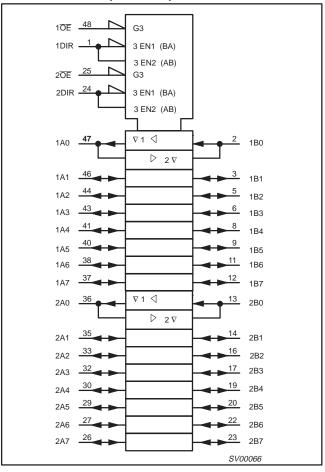
### **PIN CONFIGURATION**



## SCHEMATIC OF EACH OUTPUT



## LOGIC SYMBOL (IEEE/IEC)



## **FUNCTION TABLE**

INP	UTS	INPUTS/OUTPUTS			
nOE	nDIR	nAx	nBx		
L	L	nAx = nBx	Inputs		
L	Н	Inputs	nBx = nAx		
Н	Х	Z	Z		

H = High voltage level

= Low voltage level

X = Don't care Z = High Impedance "off" state

## 3.3V 16-bit transceiver with $30\Omega$ termination resistors (3-State)

## 74LVT162245B

## ABSOLUTE MAXIMUM RATINGS<sup>1,2</sup>

SYMBOL	PARAMETER	CONDITIONS	RATING	UNIT	
V <sub>CC</sub>	DC supply voltage		-0.5 to +4.6	V	
I <sub>IK</sub>	DC input diode current	V <sub>I</sub> < 0	-50	mA	
VI	DC input voltage <sup>3</sup>		-0.5 to +7.0	V	
lok	DC output diode current	V <sub>O</sub> < 0	-50	mA	
V <sub>OUT</sub>	DC output voltage <sup>3</sup>	Output in Off or High state	-0.5 to +7.0	V	
	DC output ourrent	Output in Low state	128	A	
Гоит	DC output current	Output in High state	-64	mA	
T <sub>stg</sub>	Storage temperature range		-65 to +150	°C	

### NOTES:

## **RECOMMENDED OPERATING CONDITIONS**

SYMBOL	PARAMETER	LIM	UNIT	
		MIN	MAX	
V <sub>CC</sub>	DC supply voltage	2.7	3.6	V
V <sub>I</sub>	Input voltage	0	5.5	V
V <sub>IH</sub>	High-level input voltage	2.0		V
V <sub>IL</sub>	Input voltage		0.8	V
I <sub>OH</sub>	High-level output current		-12	mA
I <sub>OL</sub>	Low-level output current		12	mA
Δt/Δν	Input transition rise or fall rate; Outputs enabled		10	ns/V
T <sub>amb</sub>	Operating free-air temperature range	-40	+85	°C

Stresses beyond those listed may cause permanent damage to the device. These are stress ratings only and functional operation of the
device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to
absolute-maximum-rated conditions for extended periods may affect device reliability.

<sup>2.</sup> The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability. The maximum junction temperature of this integrated circuit should not exceed 150°C.

<sup>3.</sup> The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.

## 3.3V 16-bit transceiver with $30\Omega$ termination resistors (3-State)

## 74LVT162245B

#### DC ELECTRICAL CHARACTERISTICS

				LIMITS			
SYMBOL	PARAMETER	TEST CONDITIONS			= –40°C to	+85°C	UNIT
			MIN	TYP <sup>1</sup>	MAX		
V <sub>IK</sub>	Input clamp voltage	V <sub>CC</sub> = 2.7V; I <sub>IK</sub> = -18mA			0.8	-1.2	V
V <sub>OH</sub>	High-level output voltage	$V_{CC} = 3.0V; I_{OH} = -12mA$		2.0	2.5		V
V <sub>OL</sub>	Low-level output voltage	V <sub>CC</sub> = 3.0V; I <sub>OL</sub> = 12mA			0.3	0.8	V
		$V_{CC} = 3.6V$ ; $V_I = V_{CC}$ or GND	Operators I miles		0.1	±1	
	least to the second	V <sub>CC</sub> = 0 or 3.6V; V <sub>I</sub> = 5.5V	Control pins		0.1	10	_
I <sub>I</sub>	Input leakage current	V <sub>CC</sub> = 3.6V; V <sub>I</sub> = V <sub>CC</sub>	1/0 5		0.5	10	μΑ
		$V_{CC} = 3.6V; V_I = 0$ I/O Data pins <sup>4</sup>			0.1	-5	
I <sub>OFF</sub>	Output off current	$V_{CC} = 0V$ ; $V_I$ or $V_O = 0$ to 4.5V		0.1	±100	μΑ	
		V <sub>CC</sub> = 3V; V <sub>I</sub> = 0.8V		75	130		
$I_{HOLD}$	Bus Hold current A or B outputs <sup>6</sup>	V <sub>CC</sub> = 3V; V <sub>I</sub> = 2.0V		-75	-130		μΑ
		$V_{CC} = 0V \text{ to } 3.6V; V_{CC} = 3.6V$		±500			
I <sub>EX</sub>	Current into an output in the High state when V <sub>O</sub> > V <sub>CC</sub>	$V_{O} = 5.5V; V_{CC} = 3.0V$			75	125	μΑ
I <sub>PU/PD</sub>	Power up/down 3-State output current <sup>3</sup>	$V_{CC} \le 1.2V$ ; $V_O = 0.5V$ to $V_{CC}$ ; $V_I = GNI$ OE/OE = Don't care	D or V <sub>CC</sub> ;		40	±100	μА
I <sub>CCH</sub>		$V_{CC} = 3.6V$ ; Outputs High, $V_I = GND$ or $V_{CC}$ , $I_O = 0$			0.07	0.12	
I <sub>CCL</sub>	Quiescent supply current	$V_{CC} = 3.6V$ ; Outputs Low, $V_I = GND$ or $V_{CC}$ , $I_O = 0$			4.2	6	mA
I <sub>CCZ</sub>	1	$V_{CC} = 3.6V$ ; Outputs Disabled; $V_I = GNI$		0.07	0.12	1	
Δl <sub>CC</sub>	Additional supply current per input pin <sup>2</sup>	$V_{CC}$ = 3V to 3.6V; One input at $V_{CC}$ -0.6 Other inputs at $V_{CC}$ or GND	V,		0.1	0.2	mA

## NOTES:

- All typical values are at V<sub>CC</sub> = 3.3V and T<sub>amb</sub> = 25°C.
   This is the increase in supply current for each input at the specified voltage level other than V<sub>CC</sub> or GND
   This parameter is valid for any V<sub>CC</sub> between 0V and 1.2V with a transition time of up to 10msec. From V<sub>CC</sub> = 1.2V to V<sub>CC</sub> = 3.3V ± 0.3V a transition time of 100µsec is permitted. This parameter is valid for T<sub>amb</sub> = 25°C only.

- 4. Unused pins at V<sub>CC</sub> or GND.
  5. I<sub>CCZ</sub> is measured with outputs pulled to V<sub>CC</sub> or GND.
  6. This is the bus hold overdrive current required to force the input to the opposite logic state.

## **AC CHARACTERISTICS**

GND = 0V;  $t_R = t_F = 2.5 ns$ ;  $C_L = 50 pF$ ;  $R_L = 500 \Omega$ ;  $T_{amb} = -40 ^{\circ} C$  to  $+85 ^{\circ} C$ .

				L	IMITS		
SYMBOL	PARAMETER	WAVEFORM	Vc	<sub>C</sub> = 3.3V ±0.	3V	V <sub>CC</sub> = 2.7V	UNIT
			MIN	TYP <sup>1</sup>	MAX	MAX	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay nAx to bBx or bBx to nAx	1	1.0 1.0	2.5 2.2	3.5 3.5	3.9 3.9	ns
t <sub>PZH</sub>	Output enable time to High and Low level	2	1.5 1.5	3.5 3.2	5.3 4.4	6.4 5.0	ns
t <sub>PHZ</sub>	Output disable time from High and Low Level	2	1.5 1.5	3.5 4.3	4.8 6.7	5.1 5.9	ns

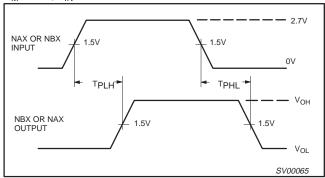
<sup>1.</sup> All typical values are at  $V_{CC}$  = 3.3V and  $T_{amb}$  = 25°C.

## 3.3V 16-bit transceiver with $30\Omega$ termination resistors (3-State)

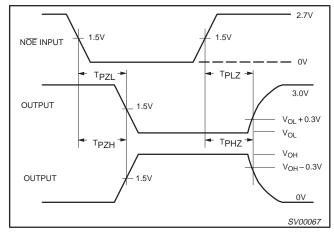
## 74LVT162245B

#### **AC WAVEFORMS**

 $V_M = 1.5V$ ,  $V_{IN} = GND$  to 2.7V

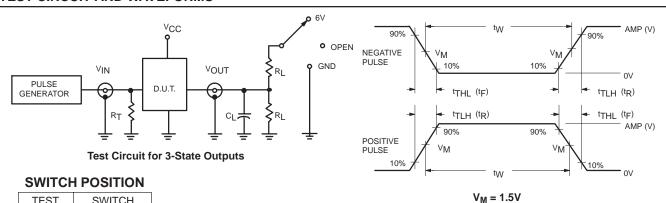


**Waveform 1. Input to Output Propagation Delays** 



Waveform 2. 3-State Output Enable and Disable Times

### **TEST CIRCUIT AND WAVEFORMS**



TEST	SWITCH
t <sub>PHZ</sub> /t <sub>PZH</sub>	GND
t <sub>PLZ</sub> /t <sub>PZL</sub>	6V
t <sub>PLH</sub> /t <sub>PHL</sub>	open

## **DEFINITIONS**

 $R_L$  = Load resistor; see AC CHARACTERISTICS for value.

 $C_L = Load$  capacitance includes jig and probe capacitance; see AC CHARACTERISTICS for value.

 $R_T$  = Termination resistance should be equal to  $Z_{OUT}$  of pulse generators.

FAMILY	INPUT PULSE REQUIREMENTS								
FAMILY	Amplitude	Rep. Rate	t <sub>W</sub>	t <sub>R</sub>	t <sub>F</sub>				
74LVT16	2.7V	≤10MHz	500ns	≤2.5ns	≤2.5ns				

Input Pulse Definition

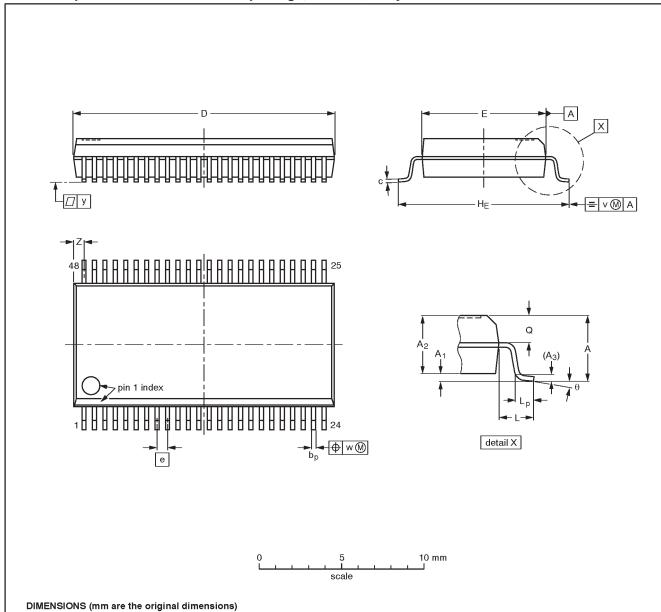
SW00003

# $3.3V\ LVT\ 16$ -bit transceiver with $30\Omega$ termination resistors (3-State)

## 74LVT162245B

## SSOP48: plastic shrink small outline package; 48 leads; body width 7.5 mm

SOT370-1



UNIT	A max.	Α1	A <sub>2</sub>	<b>A</b> <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	HE	L	Lp	Q	v	w	у	Z <sup>(1)</sup>	θ
mm	2.8	0.4 0.2	2.35 2.20	0.25	0.3 0.2	0.22 0.13	16.00 15.75	7.6 7.4	0.635	10.4 10.1	1.4	1.0 0.6	1.2 1.0	0.25	0.18	0.1	0.85 0.40	8° 0°

#### Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

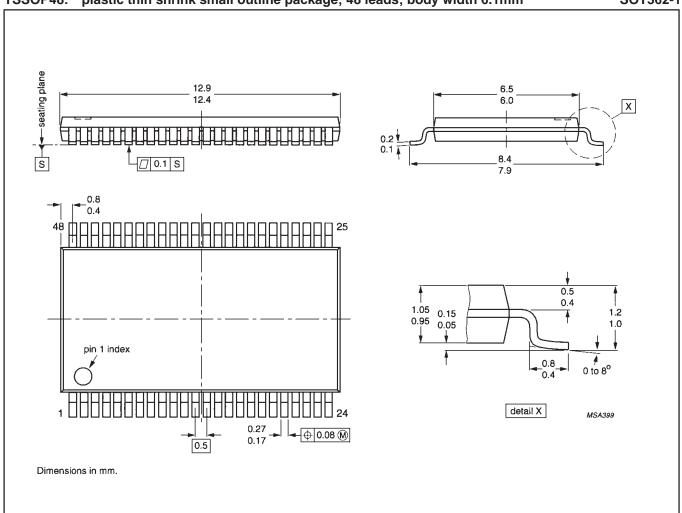
OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT370-1		MO-118AA			<del>93-11-02</del> 95-02-04

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# 3.3V LVT 16-bit transceiver with $30\Omega$ termination resistors (3-State)

## 74LVT162245B

TSSOP48: plastic thin shrink small outline package; 48 leads; body width 6.1mm SOT362-1



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3.3V LVT 16-bit transceiver with  $30\Omega$  termination resistors (3-State)

74LVT162245B

**NOTES** 

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## 3.3V LVT 16-bit transceiver with $30\Omega$ termination resistors (3-State)

74LVT162245B

#### Data sheet status

Data sheet status	Product status	Definition [1]
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make chages at any time without notice in order to improve design and supply the best possible product.
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<sup>[1]</sup> Please consult the most recently issued datasheet before initiating or completing a design.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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