- Flow-Through Architecture Optimizes **PCB Layout**
- Center-Pin V<sub>CC</sub> and GND Configurations Minimize High-Speed Switching Noise
- EPIC™ (Enhanced-Performance Implanted) CMOS) 1-µm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic (N) and Ceramic (J) DIPs

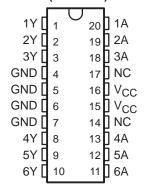
## description

The 'AC11014 contains six independent inverters. They perform the Boolean function  $Y = \overline{A}$ . Because of the Schmitt action, the devices have different input threshold levels for positive-going  $(V_{T+})$  and for negative-going  $(V_{T-})$  signals.

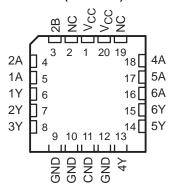
These circuits are temperature compensated. They can be triggered from the slowest of input ramps and still give clean, jitter-free output signals and greater noise margin than conventional inverters.

The 54AC11014 is characterized for operation over the full military temperature range of -55°C to 125°C. The 74AC11014 is characterized for operation from -40°C to 85°C.

#### **54AC11014...JOR W PACKAGE 74AC11014...DW OR N PACKAGE** (TOP VIEW)



#### **54AC11014...FK PACKAGE** (TOP VIEW)



NC - No internal connection

#### **FUNCTION TABLE** (each inverter)

	,
INPUT	OUTPUT
Α	Υ
Н	L
L	Н

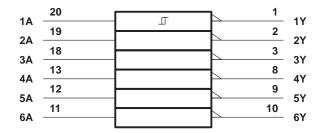


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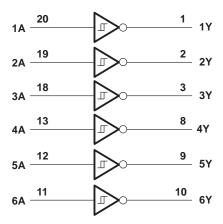
### logic symbol†



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the DW, J, N, and W packages.

### logic diagram (positive logic)



## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V <sub>CC</sub>	0.5 V to 7 V
Input voltage range, V <sub>I</sub> (see Note 1)	$-0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Output voltage range, V <sub>O</sub> (see Note 1)	$-0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ )	±20 mA
Output clamp current, I <sub>OK</sub> (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CC</sub> )	±50 mA
Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ )	±50 mA
Continuous current through V <sub>CC</sub> or GND	±150 mA
Storage temperature range, T <sub>stg</sub>	–65°C to 150°C

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

### recommended operating conditions

			54	4AC1101	4	74AC11014			UNIT	
			MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
VCC	Supply voltage		3	5	5.5	3	5	5.5	V	
	VCC = 3 V	2.1			2.1					
$V_{IH}$	IH High-level input voltage	V <sub>CC</sub> = 4.5 V	3.15			3.15			V	
	$V_{CC} = 5.5 \text{ V}$	3.85			3.85					
V <sub>IL</sub> Low-level input voltage		VCC = 3 V			0.9			0.9		
	V <sub>CC</sub> = 4.5 V			1.35			1.35	V		
		V <sub>CC</sub> = 5.5 V			1.65			1.65		
VI	Input voltage	•	0		Vcc	0		Vcc	V	
VO	Output voltage		0		Vcc	0		Vcc	V	
		VCC = 3 V			-4			-4		
loh	High-level output current	VCC = 4.5 V			-24			-24	mA	
		V <sub>CC</sub> = 5.5 V			-24			-24		
		V <sub>CC</sub> = 3 V			12			12		
$I_{OL}$	Low-level output current	V <sub>CC</sub> = 3 V			24		-	24	mA	
		V <sub>CC</sub> = 5.5 V			24			24		
T <sub>A</sub>	Operating free-air temperature		-55		125	-40		85	°C	



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# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		T,	<sub>Δ</sub> = 25°C	54AC	11014	74AC11014		UNIT	
PARAMETER	TEST CONDITIONS	Vcc	MIN	TYP MAX	MIN	MAX	MIN	MAX	UNIT	
V <sub>T+</sub>		3 V		2.:	2	2.2		2.2		
Positive-going		4.5 V		3.2	2	3.2		3.2	V	
threshold		5.5 V		3.9	)	3.9		3.9		
V <sub>T</sub> _		3 V	0.5		0.5		0.5			
Negative-going		4.5 V	0.9		0.9		0.9		V	
threshold		5.5 V	1.1		1.1		1.1			
ΔVΤ		3 V	0.3	1.3	0.3	1.2	0.3	1.2		
Hysteresis		4.5 V	0.4	1.4	0.4	1.4	0.4	1.4	V	
$(V_{T+} - V_{T-})$		5.5 V	0.5	1.0	0.5	1.6	0.5	1.6		
		3 V	2.9		2.9		2.9			
	ΙΟΗ = – 50 μΑ	4.5 V	4.4		4.4		4.4		V	
		5.5 V	5.4		5.4		5.4			
.,	I <sub>OH</sub> = -4 mA	3 V	2.58		2.4		2.48			
VOH	I <sub>OH</sub> = -24 mA	4.5 V	3.94		3.7		3.8			
		5.5 V	4.94		4.7		4.8			
	I <sub>OH</sub> = - 50 mA <sup>†</sup>	5.5 V			3.85					
	I <sub>OH</sub> = -75 mA <sup>†</sup>	5.5 V					3.85			
	Ι <sub>ΟL</sub> = 50 μΑ	3 V		0.		0.1		0.1		
		4.5 V		0.		0.1		0.1		
		5.5 V		0.		0.1		0.1		
	I <sub>OL</sub> = 12 mA	3 V		0.30	3	0.5		0.44	\ \	
V <sub>OL</sub>		4.5 V		0.30	5	0.5		0.44	V	
	I <sub>OL</sub> = 24 mA	5.5 V		0.30	5	0.5		0.44		
	I <sub>OL</sub> = 50 mA <sup>†</sup>	5.5 V				1.65				
	I <sub>OL</sub> = 75 mA <sup>†</sup>	5.5 V						1.65		
IJ	V <sub>I</sub> = V <sub>CC</sub> or GND	5.5 V		±0.		±1		±1	μΑ	
Icc	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V		4	1	80		40	μΑ	
C <sub>i</sub>	V <sub>I</sub> = V <sub>CC</sub> or GND	5 V		3.5					pF	

<sup>†</sup> Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

# switching characteristics over recommended operating free-air temperature range, $V_{CC}$ = 3.3 V $\pm$ 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	то	T,	4 = 25°C	;	54AC1	1014	74AC1	1014	UNIT
PARAMETER	(INPUT) (OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT	
t <sub>PLH</sub>	А	V	1.2	5.4	9.2	1.2	10.3	1.2	9.8	no
<sup>t</sup> PHL		T	1.7	6	8.5	1.7	9.9	1.7	9.3	ns



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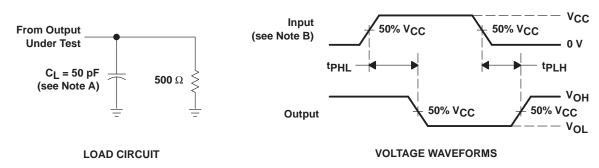
# switching characteristics over recommended operating free-air temperature range, $V_{CC}$ = 5 V $\pm$ 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	то	T,	4 = 25°C	;	54AC1	11014	74AC1	1014	UNIT
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
<sup>t</sup> PLH	А	V	1.1	3.6	6.8	1.1	7.6	1.1	7.1	no
t <sub>PHL</sub>		ī	1.5	4.1	6.7	1.5	7.6	1.5	7.4	ns

# operating characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$

PARAMETER	TEST CONDITIONS	TYP	UNIT
C <sub>pd</sub> Power dissipation capacitance	$C_L = 50 \text{ pF},  f = 1 \text{ MHz}$	27	pF

#### PARAMETER MEASUREMENT INFORMATION



NOTES: A. C<sub>L</sub> includes probe and jig capacitance.

- B. Input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz,  $Z_O = 50 \Omega$ ,  $t_f = 3 \text{ ns}$ ,  $t_f = 3 \text{ ns}$ .
- C. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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