HVC322B

Variable Capacitance Diode for ET tuner

HITACHI

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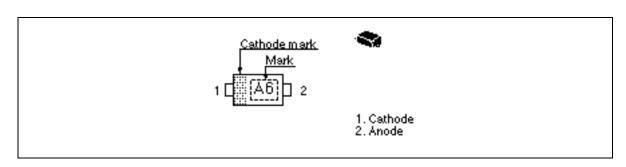
Features

- Low matching error. (ÆC/C =2.0% max)
- High capacitance ratio. (n =6.22min)
- Low series resistance. (rs=0.65½max)
- <u>Ultra small Flat Package (UFP)</u> is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVC322B	A6	UFP

Outline





HVC322B

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Value	Unit	
Peak reverse voltage	V _{RM} *1	35	V	
Reverse voltage	V _R	34	V	
Junction temperature	Tj	125	°C	
Storage temperature	Tstg	-55 to +125	°C	

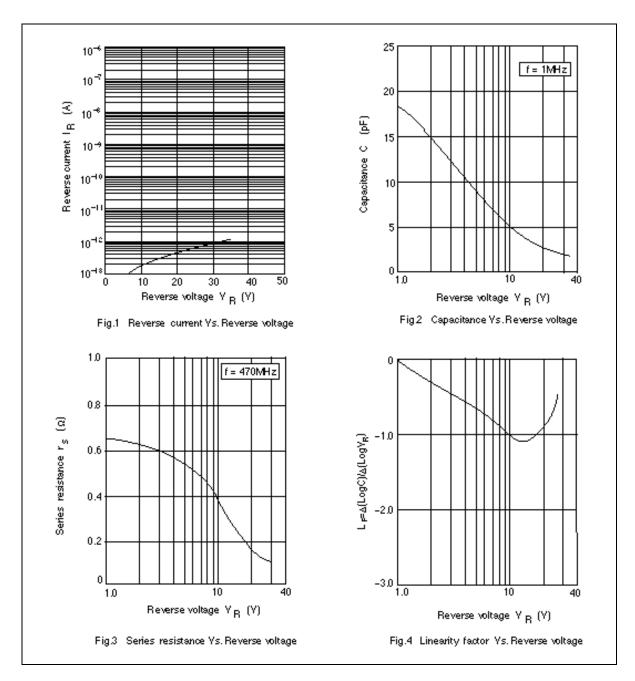
Note 1. RL=4.7K½

Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_		10	nA	V _R = 32V
	I _{R2}	_	_	100	_	V _R = 32V, Ta= 60°C
Capacitance	C ₂	14.22	_	15.473	pF	V _R = 2V, f = 1MHz
	C ₁₀	5.2	_	6.0	_	$V_R = 10V$, $f = 1MHz$
	C ₁₇	2.8	_	3.3	_	$V_R = 17V$, $f = 1MHz$
	C ₂₅	2.132	_	2.290	_	$V_R = 25V$, $f = 1MHz$
Capacitance ratio	n ₁	6.22	_	_	_	C ₂ /C ₂₅
	n ₂	1.70	_	1.96	_	C ₁₀ /C ₁₇
	n ₃	1.04		_	_	C ₂₅ /C ₂₈
Series resistance	r _S	_		0.65	1/2	V _R = 5V, f = 470MHz
Matching error	ÆC/C*1	_	_	2.0	%	V _R = 2 to 25V, f = 1 MHz

Note 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of $\Delta C/C$ continuous in a reel , expect extention to another group. Calculate Matching Error,

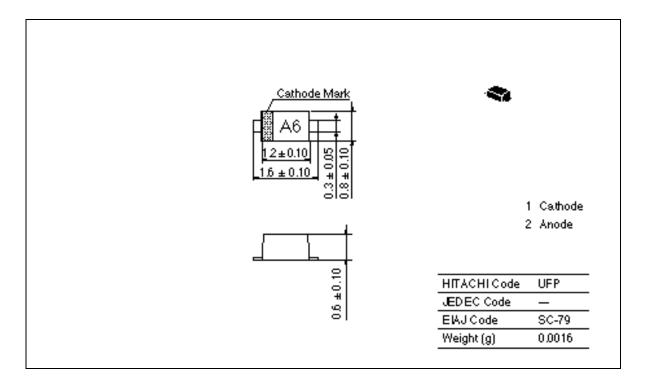
Main Characteristic



HVC322B

Package Dimensions

Unit: mm



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