Typical Applications

Satellite Communications
Military

Features

Surface Mount
High Reliability
Designed to Withstand 50 Krads Total Dose
(Optional to 100 Krads Total Dose)

Frequency

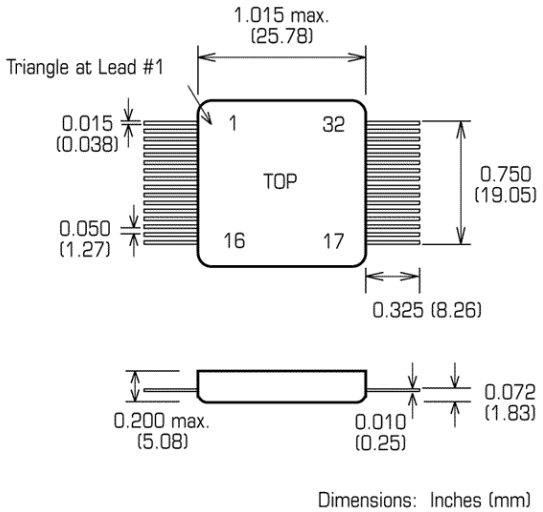
10 MHz – 75 MHz

Parameters

	TTL	CMOS
Output		
Rise and Fall Time:	Meas. 0.8 V & 2.0 V	Meas. 10% to 90%
10 MHz to 20 MHz	15 ns max.	5 ns max.
20 MHz to 75 MHz	5 ns max.	5 ns max.
Duty Cycle:	40/60% @ 1.4 V level	40/60% @ 50% level
Logic "0"	+ 0.5 V max.	+ 0.5 V max.
Logic "1"	+ 2.4 V min.	+ 4.0 V min.

Load:	As defined by MIL-PRF-55310
Temperature Stability:	± 2 to ± 4 ppm max. typical over -40° to +85°C ± 5 to ± 10 ppm max. typical over -55° to +105°C
Aging:	± 1 to ± 2 ppm/year depending on frequency
Calibration (factory):	± 4 ppm @ 25°C (if EFC is not specified)
Electrical Frequency Adjustment: (when specified)[1]	± 5 ppm min.
Screening:	Class B or Class S
Environmental:	Random Vibration: 50 g typical Shock: 1500 g, 0.5 msec. typical
Short Term Stability:	± 1 x 10^{-9} per second typical

Enclosure



Dimensions: Inches (mm)

Typical Phase Noise @ 10 MHz

1Hz Offset	- 55 dBc / Hz
10Hz Offset	-85 dBc / Hz
100Hz Offset	-115 dBc / Hz
1kHz Offset	-135dBc / Hz
10kHz Offset	-145dBc / Hz
100kHz Offset	-145dBc / Hz

PIN CONNECTIONS

4 – Ext. Freq. Adj. (when specified), or N/C
5 – GND/Case
11 – Supply Voltage
12 – RF Output
13 – Supply Voltage

Notes: Leads 11 and 13 are connected internally; either or both may be used.

Optional lead forming available for surface mounting.
[1]External Frequency Adjustment is required for calibration @ +25°C for frequency stability specifications of < ±4 ppm.

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