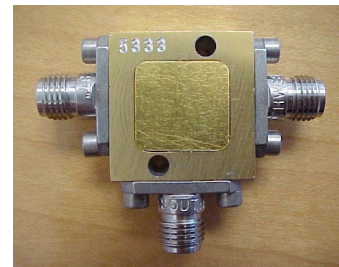
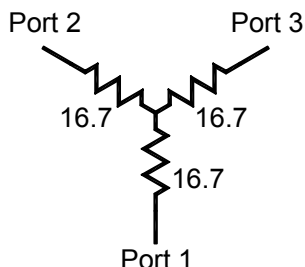


- DC to >25 GHz
- Excellent Return Loss
- Superb Insertion Loss Stability
- Accurate Amplitude and Phase Tracking



The PSPL model 5333 Power Divider is an Ultra-Broadband Resistive Coupler, which provides even division of an input signal at the output ports. The device exhibits excellent through-line insertion loss stability for frequencies from DC to >25 GHz. The 5333 features good impedance match across a wide range of frequencies, with Return Loss typically >15 dB from 0-25 GHz. Resistors are accurate within $\pm 2\%$, providing excellent input matching.

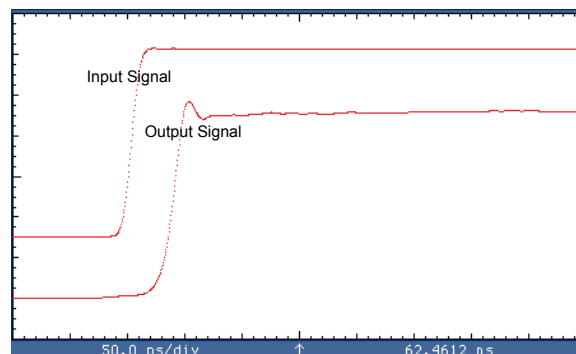
Parameter	Performance
Insertion Loss	6 dB, nominal DC 6.5 $\pm$ 1dB, 0-25 GHz*
Risetime (10% - 90%)	≤ 15 ps
Return Loss	>15 dB, 0-18 GHz* ≥ 10 dB, 18-25 GHz*
Input Impedance @ DC (port 1, 2 or 3)	50 $\pm$ 1 Ω *
Insertion Loss Asymmetry (S31-S21)	≤ 0.5 dB, 0-25 GHz*
Phase Tracking (S31-S21)	≤ 5 deg, 0-25 GHz*
Group Delay	198 (-8,+12) ps, 0-25 GHz, typical
Temperature Range	0-110 $^{\circ}$ C, Storage 0-90 $^{\circ}$ C @ 1.5 W, Operating Case Temperature Linearly derated to 0W @ 110 $^{\circ}$ C
Max Input Power (Avg)	1.5 Watts, CW
Warranty	One Year, See Terms of Sale for details

* Guaranteed Specifications. Measurements made with 50 Ω matched line/load at output ports.

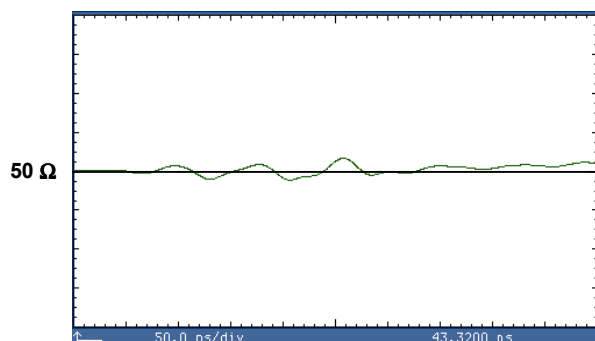
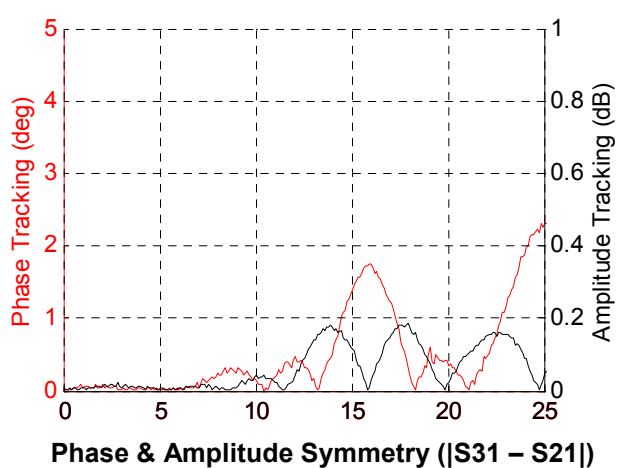
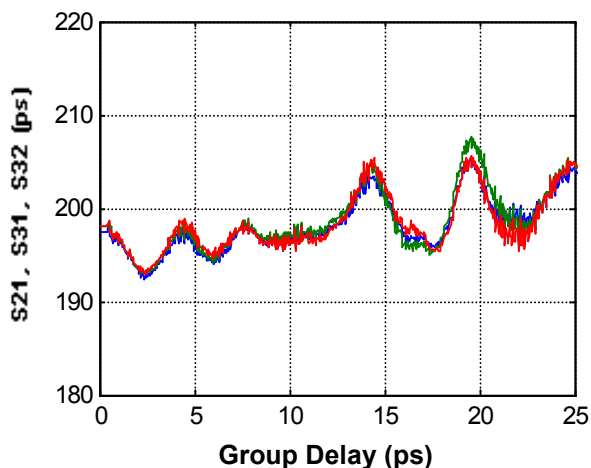
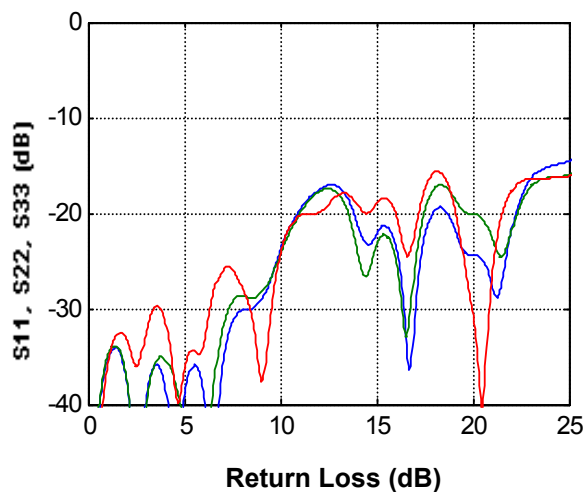
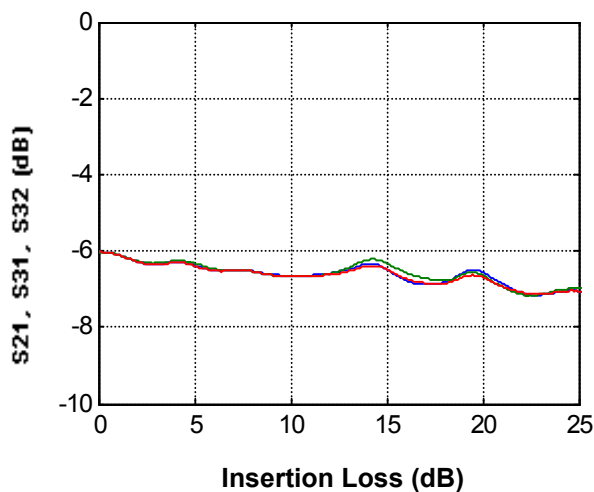
Ordering Information

Model Number	Connector Configuration**
5333-104	SMA Jacks (f)

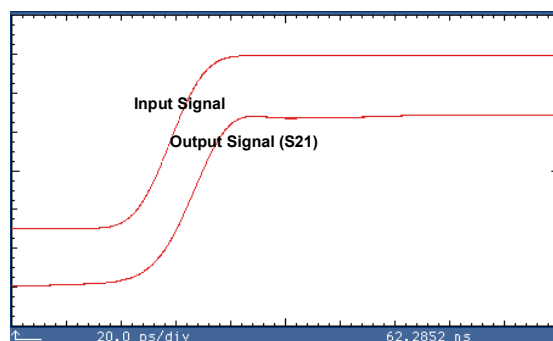
** Other connector combinations are available on request.



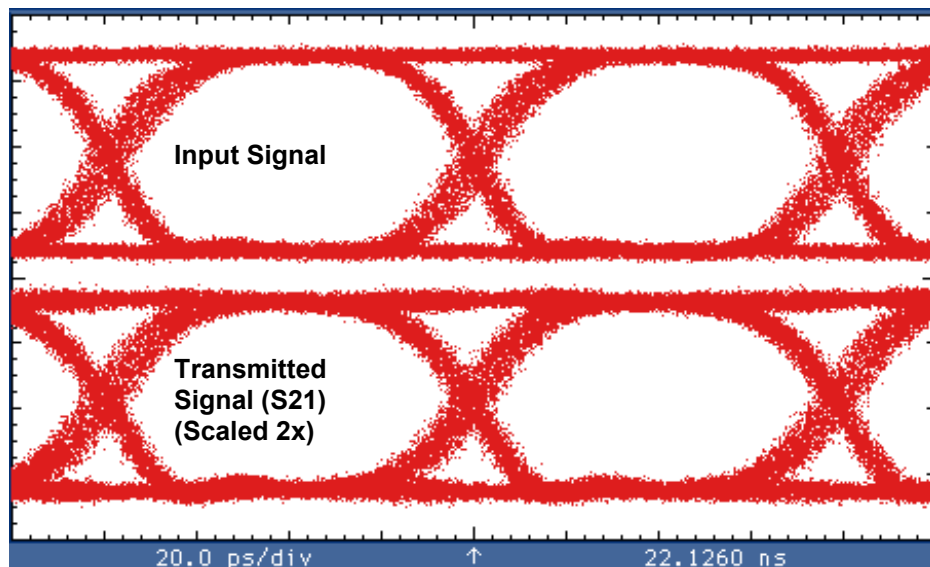
Transmission Response to 15 ps
Risetime Step into Port 1



**S11 Input TDR Response to 25 ps
Risetime Pulse, Scale 5% p/div,
Timebase, 50 ps/div**



**Transmission Response to 25 ps risetime
step into Port 1. Timebase 20 ps/div,
Output Signal Scaled 2x. Measured with
PSPL 4015C, 15 ps Pulse Generator &
HP54750, 50 GHz Oscilloscope. See AN-5C**



**Eye Diagram of 12.5 GBit/s
Pseudo-Random Bitstream**

5333 Mechanical Diagram

