## IQVCXO-172, -173, -174

#### **ISSUE 8: 18 MAY 1998**

#### **Delivery Options**

Please contact our sales office for current leadtimes

#### **Output Compatibility**

HCMOS/LS TTL

#### **Package Outlines**

 14-pin DIL compatible resistance welded enclosure, hermetically sealed with glass to metal seals.

#### Standard Frequency Stabilities

 ±25ppm, ±50ppm, ±100ppm @ V<sub>C</sub>=2.5V (inclusive of supply voltage & output load variations over the operating temperature range)

## **Operating Temperature Range**

■ 0 to 70°C

### Storage Temperature Range

■ -30 to 85°C

#### **Output Frequency Change**

- ±50ppm min (IQVCXO-172)
- ±100ppm min (IQVCXO-173)
- ±200ppm min (IQVCXO-174)

#### **Voltage Control Pin 1**

■ 2.5V ±2.0V

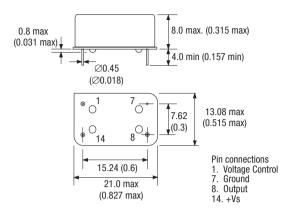
#### Marking

- Model number
- Frequency Stability Code
- Frequency
- Date code (Year/Week)

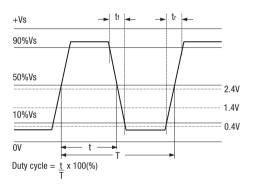
#### **Minimum Order Information Required**

• Frequency + Model Number + Frequency Stability

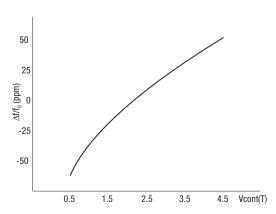
#### Outline in mm (inches)



#### Output Waveform - HCMOS/LS TTL



# Typical Voltage Control Curve @ 25°C & 20.0MHz (IQVCXO-172)

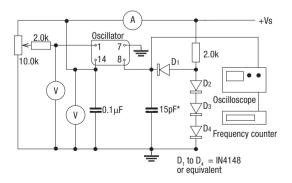


## Electrical Specification - maximum limiting values when measured in HCMOS test circuit

Frequency Range	Frequency Stability	Supply Voltage	Supply Current	Output Frequency Change	Rise Time (t <sub>r</sub> )	Fall T ime (t <sub>f</sub> )	Duty Cycle	Model Number
1.0 to < 24.0MHz	±25ppm	5V±0.25V	20mA	±50ppm min	10ns	10ns	40/60%	IQVCXO-172
1.0 to < 24.0MHz	±25ppm ±50ppm	5V±0.25V	20mA	±100ppm min	10ns	10ns	40/60%	IQVCXO-173
1.0 to < 24.0MHz	±25ppm ±50ppm ±100ppm	5V±0.25V	20mA	±200ppm min	10ns	10ns	40/60%	IQVCXO-174
24.0 to 45.0MHz	±25ppm	5V±0.25V	40mA	±50ppm min	10ns	10ns	40/60%	IQVCXO-172
24.0 to 45.0MHz	±25ppm ±50ppm	5V±0.25V	40mA	±100ppm min	10ns	10ns	40/60%	IQVCXO-173
24.0 to 45.0MHz	±25ppm ±50ppm ±100ppm	5V±0.25V	40mA	±200ppm min	10ns	10ns	40/60%	IQVCXO-174
Ordering Examp	le		22.0MHz	IQVCXO-172 A				

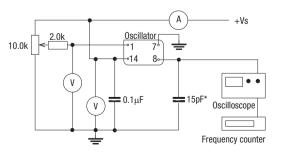
# Model No-Frequency Stability: A = ±25ppm, B = ±50ppm, C = ±100ppm-

## Test Circuit - LS TTL



\*Inclusive of jigging & equipment capacitance

## **Test Circuit - HCMOS**



\*Inclusive of jigging & equipment capacitance

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