

CFPS-130, -131

ISSUE 1; 3 MARCH 1999

Delivery Options

- Please contact our sales office for current leadtimes

Output Compatibility

- HCMOS
- Tri-state (CFPS-131)
- Non tri-state (CFPS-130)

Package Outline

- SMD (surface mount device) plastic encapsulated.
Available over 0 to 70°C

Standard Frequencies

- 1.84320MHz, 2.0MHz, 3.68640MHz, 4.0MHz, 5.0MHz, 8.0MHz, 10.0MHz, 12.0MHz, 14.318180MHz, 16.0MHz, 20.0MHz, 24.0MHz, 25.0MHz, 29.49120MHz, 30.0MHz, 32.0MHz, 33.86880MHz, 36.8640MHz, 40.0MHz, 44.23680MHz, 48.0MHz, 50.0MHz, 66.6660MHz

Standard Frequency Stabilities

- $\pm 50\text{ppm}$, $\pm 100\text{ppm}$ (inclusive of supply voltage variations over the operating temperature range)

Operating Temperature Range

- 0 to 70°C

Storage Temperature Range

- -50 to 125°C

Non-Standard Duty Cycle

- Tighter duty cycles are available on request

Tri-state Operation (CFPS-131)

- Logic '1' to pin 1 enables oscillator output
- Logic '0' to pin 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state, 0.2V max
- No connection to pin 1 enables oscillator output
- When oscillator is enabled, maximum transition time = 100ns

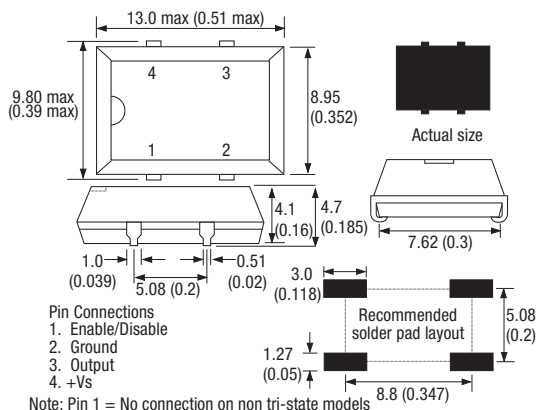
Marking

- Model number (+ Operating Temperature Code; if applicable)
- Frequency Stability Code
- Frequency
- Date code (Year/Week)

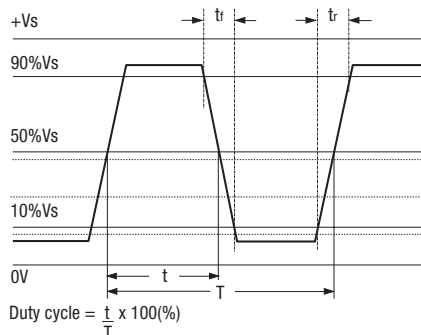
Minimum Order Information Required

- Frequency + Model Number + Operating Temperature Code (if applicable) + Frequency Stability

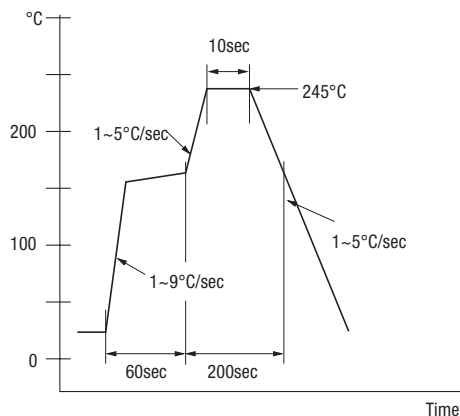
Outline in mm (inches) - (scale 2:1)



Output Waveform - HCMOS



Typical Solder Condition - Infrared Reflow



Electrical Specification – maximum limiting values when measured in HCMOS test circuit

Frequency Range	Frequency Stability	Supply Voltage	Supply Current	Rise Time (t_r)	Fall Time (t_f)	Duty Cycle	Model Number
1.0MHz to <27.0MHz	± 50 ppm, ± 100 ppm	3.3V ± 0.3 V	10mA	5ns	5ns	40/60%	CFPS-130, -131
27.0MHz to <50.0MHz	± 50 ppm, ± 100 ppm	3.3V ± 0.3 V	20mA	5ns	5ns	40/60%	CFPS-130, -131
50.0MHz to 70.0MHz	± 50 ppm, ± 100 ppm	3.3V ± 0.3 V	25mA	5ns	5ns	40/60%	CFPS-130, -131

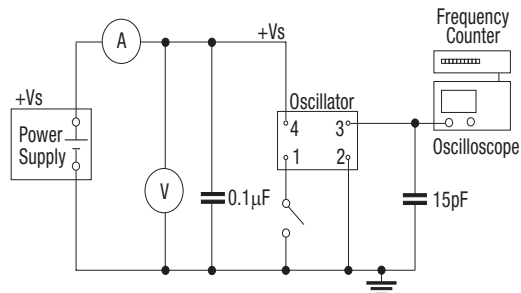
Ordering Example 24.0MHz CFPS-130 C

Frequency _____

Model No _____

Frequency Stability: B = ± 50 ppm; C = ± 100 ppm _____

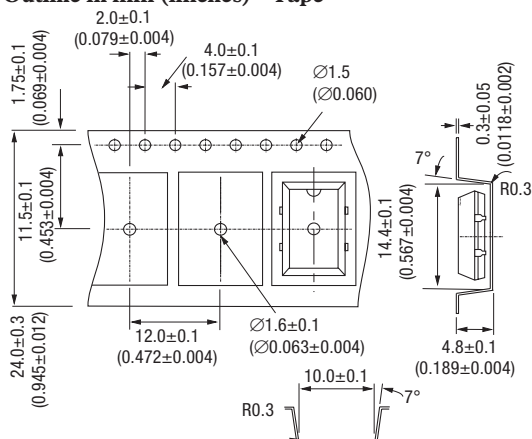
Test Circuit - HCMOS



Load Capacitance (C_L) - Inclusive of jigging & equipment
 $C_L = 15\text{pF}$ (1.0 to 70.0MHz)

Note: Pin 1 = No connection on non tri-state models

Outline in mm (inches) - Tape



Outline in mm (inches) - Reel

