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Silicon N-Channel/P-Channel Power MOS FET Array



ADE-208-1213 (Z) 1st. Edition Mar. 2001

Application

High speed power switching

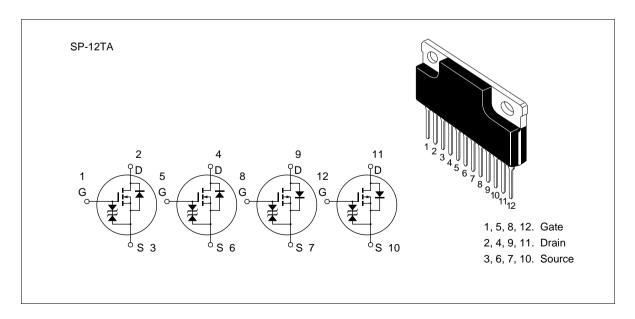
Features

Low on-resistance

$$\begin{split} & \text{N Channel:} \;\; R_{DS(on)} \;\; 0.5 \;\; , \, V_{GS} \;\; = 10 \; V, \, I_D = 2 \; A \\ & \text{P Channel:} \;\; R_{DS(on)} \;\; 0.9 \;\; , \, V_{GS} \;\; = -10 \; V, \, I_D = -2 \; A \end{split}$$

- Low drive current
- High speed switching
- High density mounting
- Suitable for H-bridged motor driver

Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

	Ratings		js –	
Item	Symbol	Nch	Pch	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	200	-200	V
Gate to source voltage	$V_{\sf GSS}$	±20	±20	V
Drain current	I _D	4	-4	A
Drain peak current	I _{D(pulse)} *1	16	-16	A
Body to drain diode reverse drain current	I _{DR}	4	-4	A
Channel dissipation	Pch (Tc = 25°C)*2	32		W
	Pch*2	4.0		W
Channel temperature	Tch	150		°C
Storage temperature	Tstg	-55 to	+150	°C

Notes: 1. PW 10 µs, duty cycle 1%

2. 4 Device Operation

Electrical Characteristics ($Ta = 25^{\circ}C$)

N Channel			
	\sim 1	 	

Symbol	Min	Тур	Max	Unit	Test conditions
$V_{(BR)DSS}$	200	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
l I _{DSS}	_	_	250	μΑ	$V_{DS} = 160 \text{ V}, V_{GS} = 0$
$V_{GS(off)}$	2.0	_	4.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
$R_{\scriptscriptstyle DS(on)}$	_	0.33	0.5		$I_D = 2 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
y _{fs}	1.5	3.0	_	S	$I_D = 2 A$ $V_{DS} = 10 V^{*1}$
Ciss	_	750	_	pF	V _{DS} = 10 V
Coss	_	260	_	pF	$V_{GS} = 0$
Crss	_	40	_	pF	f = 1 MHz
t _{d(on)}	_	19	_	ns	I _D = 2 A
t,	_	26	_	ns	V _{GS} = 10 V
t _{d(off)}	_	45	_	ns	$R_{L} = 15$
t _f	_	24	_	ns	
V_{DF}	_	1.0	_	V	$I_F = 4 \text{ A}, V_{GS} = 0$
t _{rr}		125		ns	$I_F = 4 \text{ A}, V_{GS} = 0,$ diF/dt = 100 A/ μ s
	$\begin{array}{c} V_{(BR)DSS} \\ V_{(BR)GSS} \\ \end{array}$ $\begin{array}{c} I_{GSS} \\ \vdots \ I_{DSS} \\ V_{GS(off)} \\ R_{DS(on)} \\ \end{array}$ $\begin{array}{c} Y_{fs} \\ \end{array}$ $\begin{array}{c} Ciss \\ Coss \\ Crss \\ t_{d(on)} \\ t_r \\ t_{d(off)} \\ t_f \\ V_{DF} \\ \end{array}$	V _{(BR)DSS} 200 V _{(BR)GSS} ±20 I _{GSS} — t I _{DSS} — V _{GS(off)} 2.0 R _{DS(on)} — [y _{fs}] 1.5 Ciss — Coss — Crss — t _{d(on)} — t _r — t _{d(off)} — t _f — V _{DF} —	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

Note: 1. Pulse Test

See characteristic curves of 2SK1957

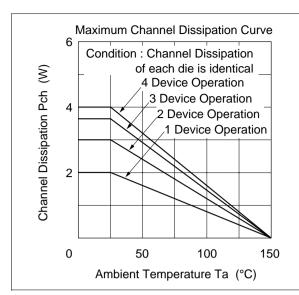
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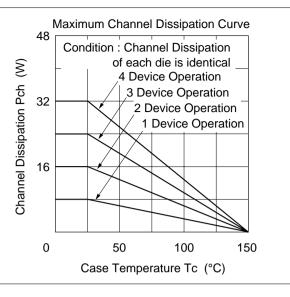
Electrical Characteristics ($Ta = 25^{\circ}C$)

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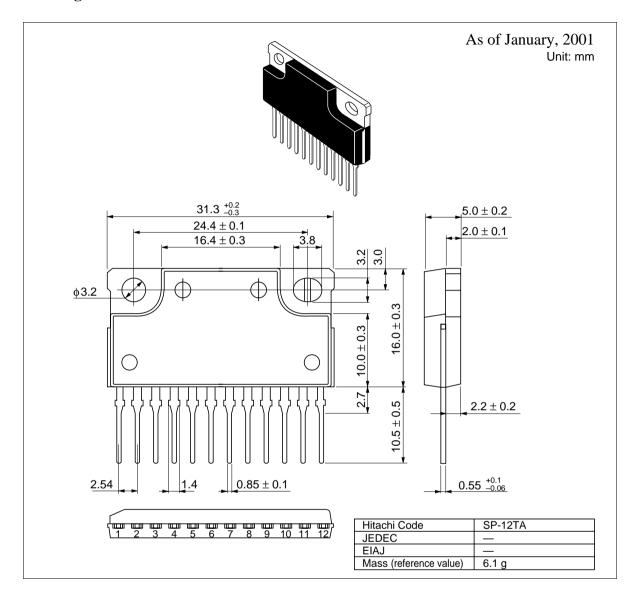
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-200	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	-250	μΑ	$V_{DS} = -160 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-2.0	_	-4.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R _{DS(on)}	_	0.7	0.9		$I_D = -2 \text{ A}, V_{GS} = -10 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	1.5	3.0	_	S	$I_D = -2 A$ $V_{DS} = -10 V^{*1}$
Input capacitance	Ciss	_	920	_	pF	V _{DS} = -10 V
Output capacitance	Coss	_	23 0	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	70	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	17	_	ns	I _D = -2 A
Rise time	t _r	_	40	_	ns	$V_{GS} = -10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	_	85	_	ns	R _L = 15
Fall time	t _f	_	45	_	ns	
Body to drain diode forward voltage	V_{DF}	_	-1.0	_	V	$I_F = -4 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}		170		ns	$I_F = -4 \text{ A}, V_{GS} = 0,$ diF/dt = 100 A/ μ s

Note: 1. Pulse Test





Package Dimensions



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Semiconductor & Integrated Circuits. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL NorthAmerica http://semiconductor.hitachi.com/ http://www.hitachi-eu.com/hel/ecg Europe Asia http://sicapac.hitachi-asia.com Japan http://www.hitachi.co.jp/Sicd/indx.htm

For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose,CA 95134 Tel: <1> (408) 433-1990 Germany

Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Fax: <1>(408) 433-0223 Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead

Berkshire SL6 8YA, United Kingdom Tel: <886>-(2)-2718-3666 Tel: <44> (1628) 585000 Fax: <44> (1628) 585160

Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00, Singapore 049318 Tel: <65>-538-6533/538-8577 Fax: <65>-538-6933/538-3877 URL: http://www.hitachi.com.sg

Hitachi Asia Ltd. (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road, Hung-Kuo Building, Taipei (105), Taiwan

Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP URL: http://www.hitachi.com.tw Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon, Hong Kong

Tel: <852>-(2)-735-9218 Fax: <852>-(2)-730-0281 URL: http://www.hitachi.com.hk

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