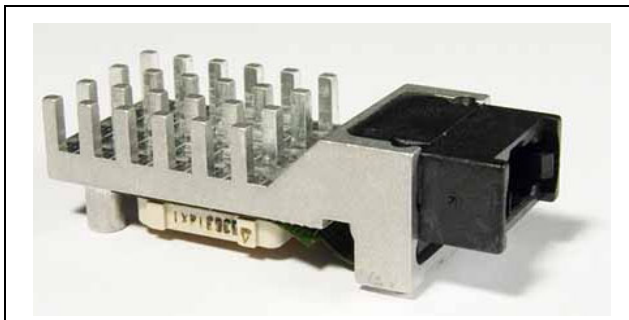


May 2007

**Ordering Information**

ZL60113MLDA Parallel Fiber Transmitter  
ZL60114MLDA Parallel Fiber Receiver

ZL6011xMMDA Parallel Fiber Module with  
EMI gasket

**0°C to +80°C****Features**

- 12 parallel channels, total 37.5 Gbps capacity
- Data rate up to 3.125 Gbps per channel
- 850 nm VCSEL array
- Link reach 200 m with 50/125  $\mu\text{m}$  500 MHz·km fiber at 3.125 Gbps
- Channel BER better than  $10^{-12}$
- Industry standard MPO/MTP<sup>TM</sup> ribbon fiber connector interface
- Pluggable MegArray<sup>®</sup> ball grid array connector
- Optionally available with EMI shield
- Laser class 1M IEC 60825-1:2001 compliant
- Power supply 3.3 V
- Compatible with industry MSA

**Applications**

- High-speed interconnects within and between switches, routers and transport equipment
- Low cost SONET/SDH VSR (Very Short Reach) OC-192/STM64 connections
- InfiniBand<sup>®</sup> connections
- General high-bandwidth density interconnections

**Description**

The ZL60113 and ZL60114 together make a high speed transmitter/receiver pair for parallel fiber applications.

The ZL60113 transmitter module converts parallel electrical input signals via a laser driver and a VCSEL array into parallel optical output signals at a wavelength of 850 nm.

The ZL60114 receiver module converts parallel optical input signals via a PIN photodiode array and a transimpedance and limiting amplifier into electrical output signals.

The modules are pluggable each fitted with an industry-standard MegArray<sup>®</sup> BGA connector. This provides ease of assembly on the host board and enables provisioning of bandwidth on demand.

Reliability assurance is based on Telcordia GR-468-CORE and the parts are compliant to the EU directive 2002/95/EC issued 27 January 2003 [RoHS].

**Exemption 6 & 7**

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## Absolute Maximum Ratings

Not necessarily applied together. Exceeding these values may cause permanent damage. Functional operation under these conditions is not implied.

| Parameter   | Symbol     | Min. | Max.           | Unit |
|---|------------|------|----------------|------|
| Supply voltage                                    | $V_{CC}$   | -0.5 | 3.6            | V    |
| Differential input voltage amplitude <sup>1</sup> | $\Delta V$ |      | 2.4            | V    |
| Voltage on any pin                                | $V_{PIN}$  | -0.3 | $V_{CC} + 0.3$ | V    |
| Relative humidity (non-condensing)                | $M_{OS}$   | 5    | 95             | %    |
| Storage temperature                               | $T_{STG}$  | -40  | 100            | °C   |
| ESD resistance                                    | $V_{ESD}$  |      | ±500           | V    |

1. Differential input voltage amplitude are peak to peak values.

## Recommended Operating Conditions

These parameters apply both to the transmitter and the receiver.

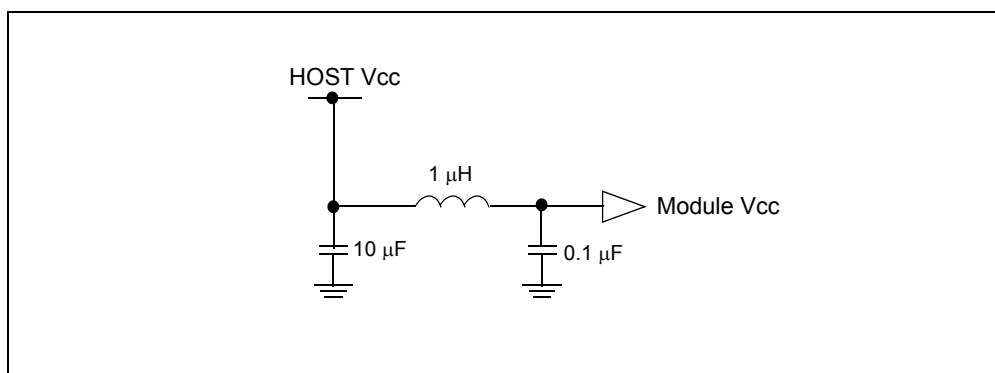
| Parameter                                    | Symbol     | Min.  | Max.  | Unit              |
|--|------------|-------|-------|-------------------|
| Power supply voltage                         | $V_{CC}$   | 3.135 | 3.465 | V                 |
| Operating case temperature                   | $T_{CASE}$ | 0     | 80    | °C                |
| Signaling rate (per channel) <sup>1</sup>    | $f_D$      | 1.0   | 3.125 | Gbps              |
| Link distance <sup>2</sup>                   | LD         | 2     |       | m                 |
| Data I/O DC blocking capacitors <sup>3</sup> | $C_{BLK}$  | 100   |       | nF                |
| Power supply noise <sup>4</sup>              | $V_{NPS}$  |       | 200   | mV <sub>p-p</sub> |

1. Data patterns are to have maximum run lengths and DC balance shifts no worse than that of a Pseudo Random Bit Sequence of length  $2^{23}-1$  (PRBS-23). Information on lower bit rates is available on request.

2. For maximum distance, see Table 6.

3. For AC-coupling, DC blocking capacitors external to the module with a minimum value of 100 nF is recommended.

4. Power supply noise is defined at the supply side of the recommended filter for all  $V_{CC}$  supplies over the frequency range of 500 Hz to 3125 MHz with the recommended power supply filter in place.



**Figure 1 - Recommended Power Supply Filter**

## ZL60113 Transmitter Specifications

All parameters below require operating conditions according to “Recommended Operating Conditions” on page 3.

| Parameter  | Symbol                      | Min.  | Max. | Unit              |
|--|-----------------------------|-------|------|-------------------|
| <i>Optical Parameters</i>  |                             |       |      |                   |
| Launch power (50/125 $\mu\text{m}$ MMF) <sup>1</sup>             | $P_{\text{OUT}}$            |       | -2   | dBm               |
| Extinguished output power  | $P_{\text{OFF}}$            |       | -30  | dBm               |
| Extinction ratio   | ER                          | 6     |      | dB                |
| Optical modulation amplitude <sup>2</sup>                        | OMA                         | 0.190 |      | mW                |
| Center wavelength  | $\lambda_{\text{C}}$        | 830   | 860  | nm                |
| Spectral width <sup>3</sup>                                      | $\Delta\lambda$             |       | 0.85 | nm <sub>rms</sub> |
| Relative intensity noise OMA                                     | $\text{RIN}_{12\text{OMA}}$ |       | -120 | dB/Hz             |
| Optical output rise time (20 - 80%)                              | $t_{\text{RO}}$             |       | 120  | ps                |
| Optical output fall time (20 - 80%)                              | $t_{\text{FO}}$             |       | 120  | ps                |
| Total jitter contributed (peak to peak) <sup>4</sup>             | TJ                          |       | 0.32 | UI                |
| Deterministic jitter contributed (peak to peak) <sup>5</sup>     | DJ                          |       | 0.14 | UI                |
| Channel to channel skew <sup>6</sup>                             | $t_{\text{SK}}$             |       | 100  | ps                |
| <i>Electrical Parameters</i>                                     |                             |       |      |                   |
| Power dissipation  | $P_{\text{D}}$              |       | 1.5  | W                 |
| Supply current   | $I_{\text{CC}}$             |       | 450  | mA                |
| Differential input voltage amplitude (peak to peak) <sup>7</sup> | $\Delta V_{\text{IN}}$      | 200   | 1800 | mV <sub>p-p</sub> |
| Differential input impedance <sup>8</sup>                        | $Z_{\text{IN}}$             | 80    | 120  | $\Omega$          |
| Electrical input rise time (20 - 80%)                            | $t_{\text{RE}}$             |       | 150  | ps                |
| Electrical input fall time (20 - 80%)                            | $t_{\text{FE}}$             |       | 150  | ps                |

1. The output optical power is compliant with IEC 60825-1 Amendment 2, Class 1M Accessible Emission Limits.

2. OMA are peak to peak values.

3. Spectral width is measured as defined in EIA/TIA-455-127 *Spectral Characterization of Multimode Laser Diodes*.

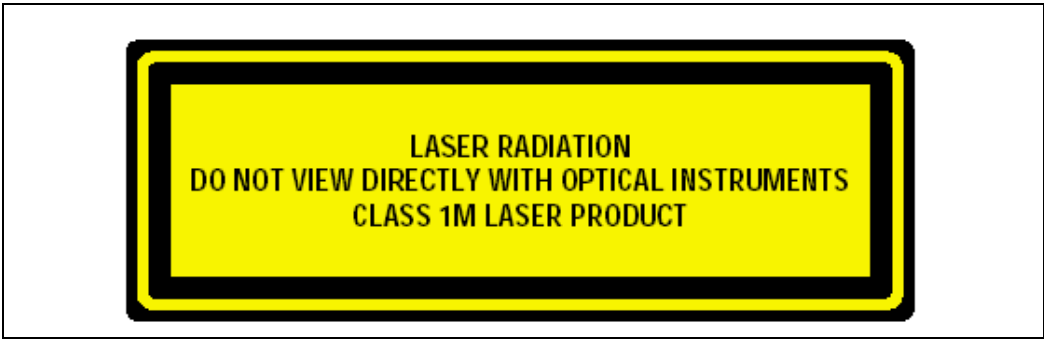
4. Total jitter is TP2 - TP1 as defined in IEEE 802.3 clause 38.6 (Gigabit Ethernet).

5. Deterministic jitter is informative. The random jitter and deterministic jitter together should be no higher than total jitter.

6. Channel skew is defined for the condition of equal amplitude, zero ps skew signals applied to the transmitter inputs.

7. Differential input voltage is defined as the peak to peak value of the differential voltage between DIN+ and DIN-. Data inputs are CML compatible.

8. Differential input impedance is measured between DIN+ and DIN-.



Classified in accordance with IEC 60825-1/A2:2001, IEC 60825-2: 2000

Class 1 M Laser Product

Emitted wavelength: 840 nm

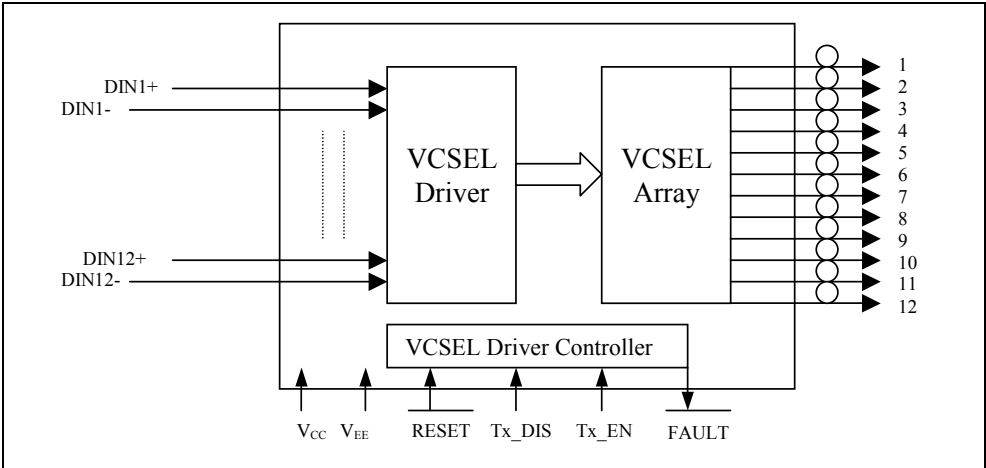


Figure 2 - ZL60113 Transmitter Block Diagram

| Front view - MTP key up |      |      |     |     |     |     |     |     |     |     |     |
|-------------------------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Ch12                    | Ch11 | Ch10 | Ch9 | Ch8 | Ch7 | Ch6 | Ch5 | Ch4 | Ch3 | Ch2 | Ch1 |
| Host circuit board      |      |      |     |     |     |     |     |     |     |     |     |

Table 1 - Transmitter Optical Channel Assignment

### Transmitter Control and Status Signals

The following table shows the timing relationships of the status and control signals of the pluggable optical transmitter.

| Parameter                                 | Symbol    | Min.        | Typ. | Max.        | Unit       |
|---|-----------|-------------|------|-------------|------------|
| Control input voltage high <sup>1</sup>   | $V_{IH}$  | $0.6V_{CC}$ |      |             | V          |
| Control input voltage low                 | $V_{IL}$  |             |      | $0.2V_{CC}$ | V          |
| Control pull-up resistor <sup>2</sup>     | $R_{PU}$  |             | 10   |             | k $\Omega$ |
| Control pull-down resistor <sup>3</sup>   | $R_{PD1}$ |             | 10   |             | k $\Omega$ |
| Status output voltage low <sup>4, 5</sup> | $V_{OL}$  |             |      | 0.4         | V          |
| Status pull-down resistor <sup>4</sup>    | $R_{PD2}$ |             | 10   |             | k $\Omega$ |
| $\overline{FAULT}$ assert time            | $T_{FA}$  |             |      | 100         | $\mu$ s    |
| $\overline{FAULT}$ lasers off             | $T_{FD}$  |             |      | 100         | $\mu$ s    |
| $\overline{RESET}$ duration               | $T_{TDD}$ | 10          |      |             | $\mu$ s    |
| $\overline{RESET}$ assert time            | $T_{OFF}$ |             | 5    | 10          | $\mu$ s    |
| $\overline{RESET}$ de-assert time         | $T_{ON}$  |             |      | 100         | ms         |
| Tx_EN assert time                         | $T_{TEN}$ |             |      | 1           | ms         |
| Tx_EN de-assert time                      | $T_{TD}$  |             | 5    | 10          | $\mu$ s    |
| Tx_DIS assert time                        | $T_{TD}$  |             | 5    | 10          | $\mu$ s    |
| Tx_DIS de-assert time                     | $T_{TEN}$ |             |      | 1           | ms         |

1. Applies to control signals  $\overline{RESET}$ , Tx\_DIS and Tx\_EN.

2. Applies to control signals  $\overline{RESET}$  and Tx\_EN. Internal pull-up resistor.

3. Applies to control signal Tx\_DIS. Internal pull-down resistor.

4. Applies to status signal  $\overline{FAULT}$ . Internal pull-down to  $V_{EE}$ .

5. With status output sink current max. 2 mA.

Transmitter Control and Status Timing Diagrams

The following figures show the timing relationships of the status and control signals of the pluggable optical transmitter.

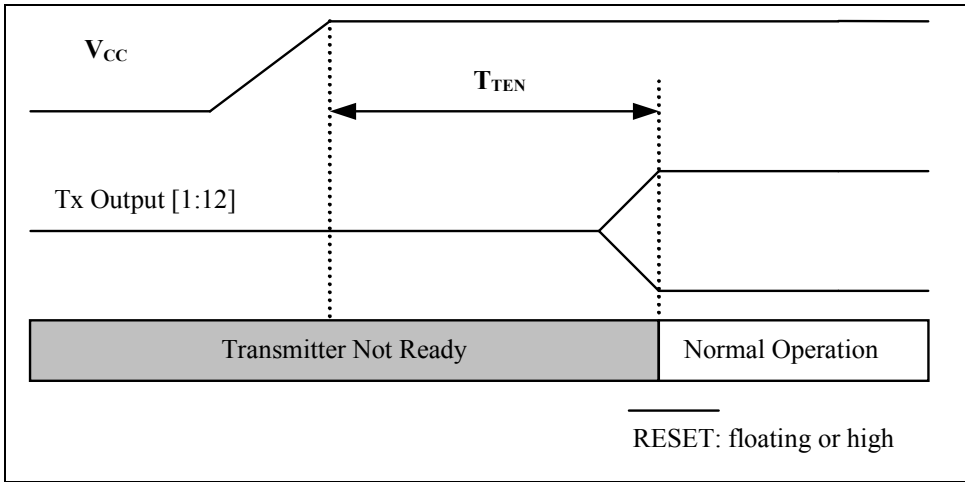


Figure 3 - Transmitter Power-up Sequence

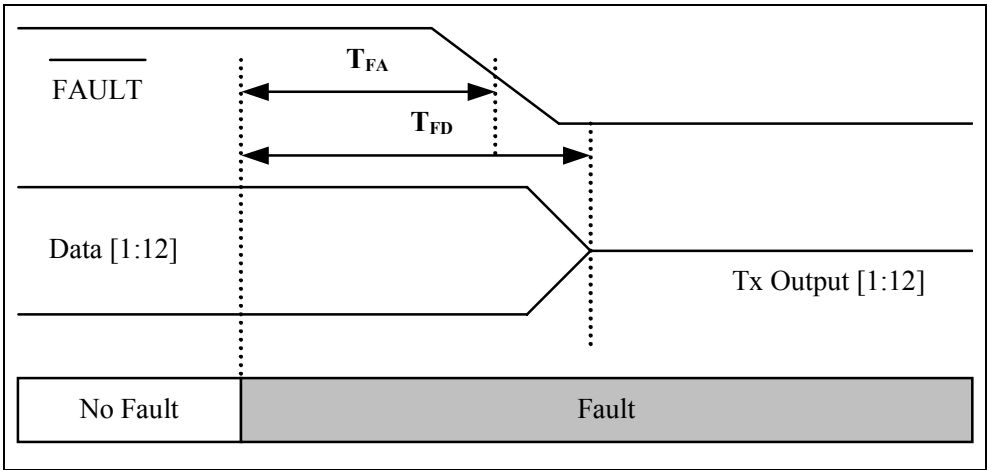


Figure 4 - Transmitter Fault Signal Timing Diagram

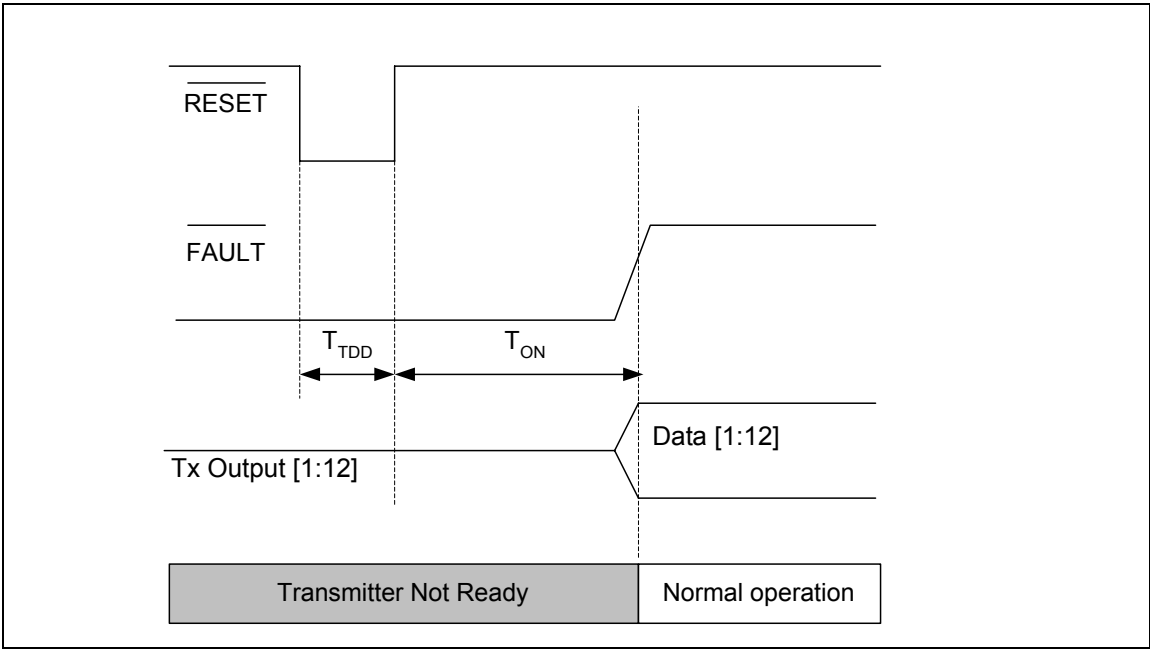


Figure 5 - Transmitter Reset Signal Timing Diagram

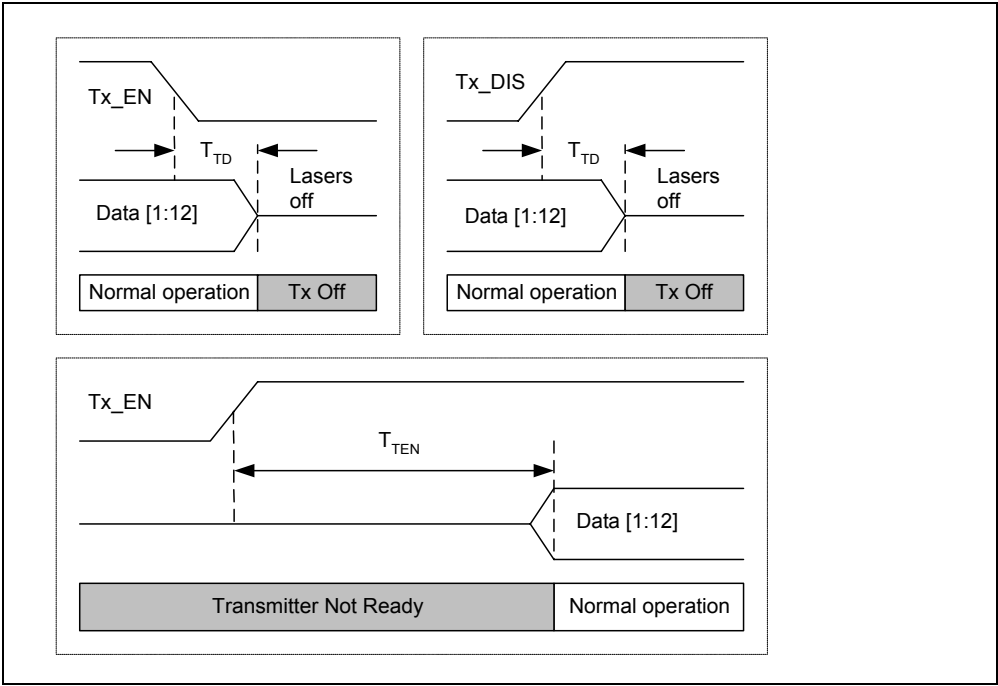


Figure 6 - Transmitter Enable and Disable Timing Diagram



|            | Tx_DIS High          | Tx_DIS Low              |
|------------|----------------------|-------------------------|
| Tx_EN High | Transmitter disabled | <b>Normal operation</b> |
| Tx_EN Low  | Transmitter disabled | Transmitter disabled    |

**Table 2 - Truth Table for Transmitter Operation (Pre-condition: RESET floating or HIGH)**

#### Transmitter Pinout Assignments

|           | <b>K</b> | <b>J</b>        | <b>H</b>        | <b>G</b>        | <b>F</b>        | <b>E</b>        | <b>D</b>        | <b>C</b>        | <b>B</b>        | <b>A</b>        |
|-----------|----------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|
| <b>1</b>  | NIC      | NIC             | NIC             | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | NIC             |
| <b>2</b>  | NIC      | NIC             | NIC             | V <sub>EE</sub> | V <sub>EE</sub> | DIN6+           | V <sub>EE</sub> | V <sub>EE</sub> | DIN9+           | V <sub>EE</sub> |
| <b>3</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | V <sub>EE</sub> | DIN5+           | DIN6-           | V <sub>EE</sub> | DIN8+           | DIN9-           | V <sub>EE</sub> |
| <b>4</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | DIN4+           | DIN5-           | V <sub>EE</sub> | DIN7+           | DIN8-           | V <sub>EE</sub> | NIC             |
| <b>5</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | DIN4-           | V <sub>EE</sub> | DIN3+           | DIN7-           | V <sub>EE</sub> | DIN10-          | V <sub>EE</sub> |
| <b>6</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | V <sub>EE</sub> | DIN2+           | DIN3-           | V <sub>EE</sub> | DIN11-          | DIN10+          | V <sub>EE</sub> |
| <b>7</b>  | NIC      | NIC             | NIC             | DIN1+           | DIN2-           | V <sub>EE</sub> | DIN12-          | DIN11+          | V <sub>EE</sub> | NIC             |
| <b>8</b>  | NIC      | RESET           | FAULT           | DIN1-           | V <sub>EE</sub> | V <sub>EE</sub> | DIN12+          | V <sub>EE</sub> | V <sub>EE</sub> | NIC             |
| <b>9</b>  | NIC      | Tx_EN           | Tx_DIS          | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | NIC             |
| <b>10</b> | NIC      | NIC             | NIC             | NIC             | NIC             | NIC             | DNC             | DNC             | DNC             | DNC             |

**Table 3 - Transmitter Host Circuit Board Layout (Top view, toward MPO/MTP™ connector end)  
(10x10 array, 1.27 mm pitch)**

**Transmitter Pin Description**

| Signal Name               | Type          | Description   | Comments   |
|---------------------------|---------------|---|--|
| DIN[1:12] +/-             | Data input    | Transmitter data in, channel 1 to 12  | Internal differential termination at 100 $\Omega$ .                              |
| V <sub>CC</sub>           |               | Transmitter power supply rail   |  |
| V <sub>EE</sub>           |               | Transmitter signal common. All transmitter voltages are referenced to this potential unless otherwise stated. | Directly connect these pads to the PC board transmitter signal ground plane.     |
| Tx_EN                     | Control input | Transmitter enable.<br>HIGH: normal operation<br>LOW: disable transmitter                                     | Active high, internal pull-up. See Table 2.                                      |
| Tx_DIS                    | Control input | Transmitter disable.<br>HIGH: disable transmitter<br>LOW: normal operation                                    | Active high, internal pull-down. See Table 2.                                    |
| $\overline{\text{FAULT}}$ | Status output | Transmitter fault.<br>HIGH: normal operation<br>LOW: laser fault detected on at least one channel             | When active, all channels are disabled. Clear by reset signal. Internal pull-up. |
| $\overline{\text{RESET}}$ | Control input | Transmitter reset.<br>HIGH: normal operation<br>LOW: reset to clear fault signal                              | Internal pull-up.  |
| DNC                       |               | Do not connect to any potential, including ground.  |  |
| NIC                       |               | No internal connection.   |  |

## ZL60114 Receiver Specifications

All parameters below require operating conditions according to “Recommended Operating Conditions,” on page 3 and a termination load of 100  $\Omega$  differential at the electrical output.

| Parameter  | Symbol                  | Min. | Max. | Unit              |
|--|-------------------------|------|------|-------------------|
| <i>Optical Parameters</i>  |                         |      |      |                   |
| Receiver sensitivity (OMA) (50/125 $\mu\text{m}$ MMF) <sup>1</sup> | S <sub>OMA</sub>        | 38   | 755  | $\mu\text{W}$     |
| Center wavelength  | $\lambda_C$             | 830  | 860  | nm                |
| Return loss <sup>2</sup>   | RL                      | 12   |      | dB                |
| Stressed receiver sensitivity (OMA) <sup>3</sup>                   | SS <sub>OMA</sub>       | 80   |      | $\mu\text{W}$     |
| Total link jitter contribution (peak to peak) <sup>4</sup>         | TJ <sub>L</sub>         |      | 0.65 | UI                |
| Deterministic link jitter contribution (peak to peak) <sup>5</sup> | DJ <sub>L</sub>         |      | 0.27 | UI                |
| Channel to channel skew <sup>6</sup>                               | t <sub>SK</sub>         |      | 100  | ps                |
| Signal detect assert   | P <sub>SA</sub>         |      | -15  | dBm               |
| Signal detect de-assert  | P <sub>SD</sub>         | -30  |      | dBm               |
| <i>Electrical Parameters</i>                                       |                         |      |      |                   |
| Power dissipation  | P <sub>D</sub>          |      | 1.5  | W                 |
| Supply current   | I <sub>CC</sub>         |      | 450  | mA                |
| Differential output voltage amplitude (peak to peak) <sup>7</sup>  | $\Delta V_{\text{OUT}}$ | 500  | 800  | mV <sub>p-p</sub> |
| Output differential load impedance                                 | Z <sub>L</sub>          | 80   | 120  | $\Omega$          |
| Electrical output rise time (20 - 80%)                             | t <sub>RE</sub>         |      | 120  | ps                |
| Electrical output fall time (20 - 80%)                             | t <sub>FE</sub>         |      | 120  | ps                |

1. Optical modulation amplitude are peak to peak values. Receiver sensitivity is measured using a source that does not degrade the sensitivity measurement, i.e. ideal source with fast rise/fall time and low RIN. Receive power for a channel is measured for a BER of  $10^{-12}$  and 6 dB extinction ratio and the other 11 channels operating with incident power of 6 dB above the channel under test.

2. Return loss is measured as defined in TIA/EIA-455-107A *Determination of Component Reflectance or Link/System Return Loss Using a Loss Test Set*.

3. Based on Gigabit Ethernet Link model. See “Link Model Parameters,” on page 17 and receiver sensitivity specified.

4. Total link jitter equals TP4 - TP1.

5. Deterministic jitter is informative. Deterministic link jitter equals TP4 - TP1.

6. Channel skew is defined for the condition of equal amplitude, zero ps skew signals applied to the receiver inputs.

7. Differential output voltage is defined as the peak to peak value of the differential voltage between DOUT+ and DOUT- and measured with a 100  $\Omega$  differential load connected between DOUT+ and DOUT-. Data outputs are CML compatible.

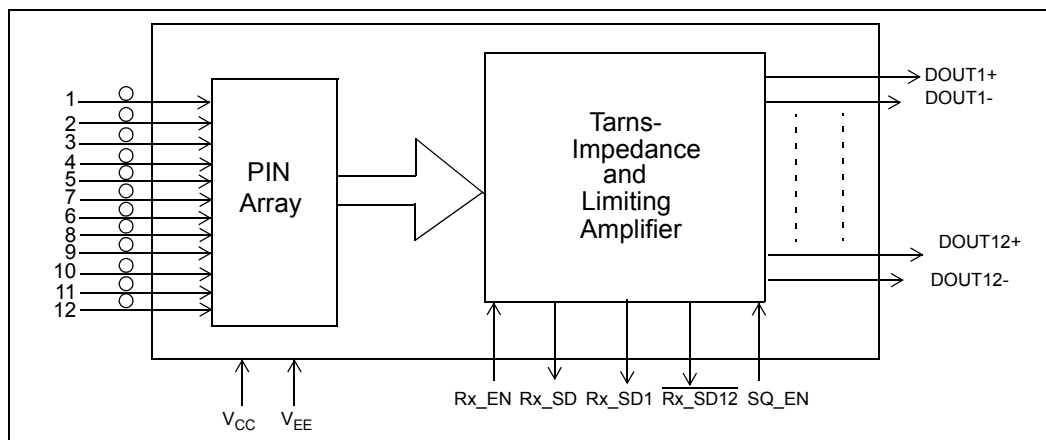


Figure 7 - ZL60114 Receiver Block Diagram

| Front view - MTP key up |      |      |     |     |     |     |     |     |     |     |     |
|-------------------------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Ch12                    | Ch11 | Ch10 | Ch9 | Ch8 | Ch7 | Ch6 | Ch5 | Ch4 | Ch3 | Ch2 | Ch1 |
| Host circuit board      |      |      |     |     |     |     |     |     |     |     |     |

Table 4 - Receiver Optical Channel Assignment

### Receiver Control and Status Signals

The following table shows the timing relationships of the status and control signals of the pluggable optical receiver.

| Parameter                                   | Symbol     | Min.        | Typ. | Max.        | Unit      |
|---|------------|-------------|------|-------------|-----------|
| Control input voltage high <sup>1</sup>     | $V_{IH}$   | $0.6V_{CC}$ |      |             | V         |
| Control input voltage low <sup>1</sup>      | $V_{IL}$   |             |      | $0.2V_{CC}$ | V         |
| Control input pull-up current <sup>1</sup>  | $ I_{IN} $ | 10          |      | 100         | $\mu A$   |
| Status output voltage low <sup>2, 3</sup>   | $V_{OL}$   |             |      | 0.4         | V         |
| Status output pull-up resistor <sup>2</sup> | $R_{PU}$   |             | 3.25 |             | $k\Omega$ |
| Receiver signal detect assert time          | $T_{SD}$   |             | 50   | 200         | $\mu s$   |
| Receiver signal detect de-assert time       | $T_{LOS}$  |             | 50   | 200         | $\mu s$   |
| Receiver enable assert time                 | $T_{RXEN}$ |             | 33   |             | ms        |
| Receiver enable de-assert time              | $T_{RXD}$  |             | 5    |             | $\mu s$   |

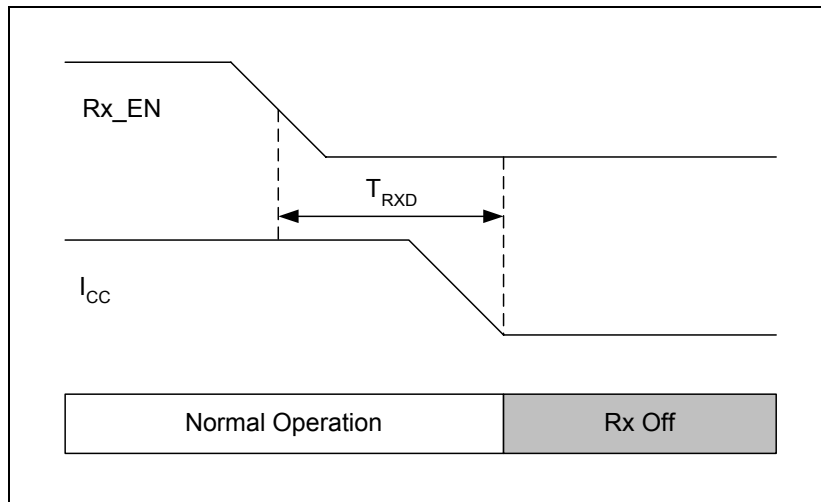
1. Applies to control signals Rx\_EN, SQ\_EN.

2. Applies to status signal Rx\_SD. Internal pull-up to  $V_{CC}$ .

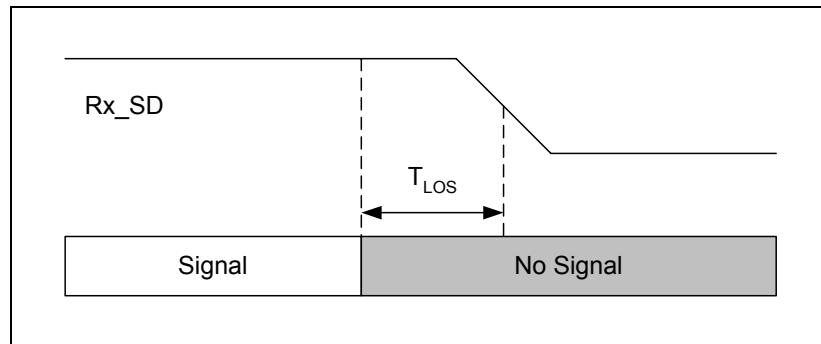
3. With status output sink current max 2 mA.

### Receiver Control and Status Timing Diagrams

The following figures show the timing relationships of the status and control signals of the pluggable optical receiver.



**Figure 8 - Receiver Enable Signal Timing Diagram**



**Figure 9 - Receiver Signal Detect Timing Diagram**

**Receiver Pinout Assignments**

|           | <b>K</b> | <b>J</b>        | <b>H</b>        | <b>G</b>        | <b>F</b>        | <b>E</b>        | <b>D</b>        | <b>C</b>        | <b>B</b>        | <b>A</b>        |
|-----------|----------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|
| <b>1</b>  | DNC      | NIC             | NIC             | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | NIC             |
| <b>2</b>  | DNC      | NIC             | NIC             | V <sub>EE</sub> | V <sub>EE</sub> | DOUT6-          | V <sub>EE</sub> | V <sub>EE</sub> | DOUT9-          | V <sub>EE</sub> |
| <b>3</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | V <sub>EE</sub> | DOUT5-          | DOUT6+          | V <sub>EE</sub> | DOUT8-          | DOUT9+          | V <sub>EE</sub> |
| <b>4</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | DOUT4-          | DOUT5+          | V <sub>EE</sub> | DOUT7-          | DOUT8+          | V <sub>EE</sub> | NIC             |
| <b>5</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | DOUT4+          | V <sub>EE</sub> | DOUT3-          | DOUT7+          | V <sub>EE</sub> | DOUT10+         | V <sub>EE</sub> |
| <b>6</b>  | NIC      | V <sub>CC</sub> | V <sub>CC</sub> | V <sub>EE</sub> | DOUT2-          | DOUT3+          | V <sub>EE</sub> | DOUT11+         | DOUT10-         | V <sub>EE</sub> |
| <b>7</b>  | NIC      | NIC             | Rx_SD           | DOUT1-          | DOUT2+          | V <sub>EE</sub> | DOUT12+         | DOUT11-         | V <sub>EE</sub> | NIC             |
| <b>8</b>  | DNC      | <u>RX_SD12</u>  | Rx_SD1          | DOUT1+          | V <sub>EE</sub> | V <sub>EE</sub> | DOUT12-         | V <sub>EE</sub> | V <sub>EE</sub> | NIC             |
| <b>9</b>  | DNC      | Rx_EN           | NIC             | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | V <sub>EE</sub> | NIC             |
| <b>10</b> | SQ_EN    | NIC             | NIC             | NIC             | NIC             | NIC             | DNC             | DNC             | DNC             | DNC             |

**Table 5 - Receiver Pinout Assignments (Top view, toward MPO/MTP™ connector end)  
(10x10 array, 1.27 mm pitch)**

**Receiver Pin Description**

| Signal Name                  | Type          | Description   | Comments  |
|------------------------------|---------------|---|---|
| DOUT[1:12] +/-               | Data output   | Receiver data out, channel 1 to 12.   |   |
| V <sub>CC</sub>              |               | Receiver power supply rail.   |   |
| V <sub>EE</sub>              |               | Receiver signal common. All receiver voltages are referenced to this potential unless otherwise stated.   | Directly connect these pads to the PC board receiver signal ground plane. |
| Rx_EN                        | Control input | Receiver enable.<br>HIGH: normal operation<br>LOW: disable receiver   | Internal pull-up.   |
| Rx_SD                        | Status output | Receiver signal detect.<br>HIGH: valid optical input on all channels<br>LOW: loss of signal on at least one channel                                 |   |
| Rx_SD1                       | Status output | Receiver signal detect channel 1.<br>HIGH: valid optical input on channel 1<br>LOW: loss of signal on channel 1                                     |   |
| $\overline{\text{RX\_SD12}}$ | Status output | Receiver signal detect channel 12.<br>HIGH: loss of signal on channel 12<br>LOW: valid optical input on channel 12                                  |   |
| SQ_EN                        | Control input | Squelch enable.<br>HIGH: squelch function enabled. Data OUT is squelched on any channels that have loss of signal<br>LOW: squelch function disabled | Internal pull-up.   |
| DNC                          |               | Do not connect to any potential, including ground.  |   |
| NIC                          |               | No internal connection.   |   |

## Handling Instructions

### Cleaning the Optical Interface

A protective connector plug is supplied with each module. This plug should remain in place whenever a fiber cable is not inserted. This will keep the optical port free from dust or other contaminants, which may potentially degrade the optical signal. Before reattaching the connector plug to the module, visually inspect the plug and remove any contamination. If the module's optical port becomes contaminated, it can be cleaned with high-pressure nitrogen (the use of fluids, or physical contact, is not advised due to potential for damage).

Before a fiber cable connector is attached to the module, it is recommended to clean the fiber cable connector using an optical connector cleaner, or according to the cable manufacturer's instructions. It is also recommended to clean the optical port of the module with high-pressure nitrogen.

### Connectors

For optimum performance, it is recommended that the number of insertions is limited to 50 for the electrical MegArray connector and 200 for the optical MPO/MTP connector.

### ESD Handling

When handling the modules, precautions for ESD sensitive devices should be taken. These include use of ESD protected work areas with wrist straps, controlled work-benches, floors etc.



## Link Reach

The following table lists the minimum reach distance of the 12 channel pluggable optical modules for different multi-mode fiber (MMF) types and bandwidths assuming worst case parameters. Each case allows for a maximum of 2 dB per channel connection loss for patch cables and other connectors.

| Fiber Type<br>[core / cladding $\mu\text{m}$ ] | Modal Bandwidth<br>@ 850 nm<br>[MHz*km] | Reach Distance<br>@ 1 Gbps<br>[m] | Reach Distance<br>@ 2.5 Gbps<br>[m] | Reach Distance<br>@ 3.125 Gbps<br>[m] |
|--|---|-----------------------------------|-------------------------------------|---------------------------------------|
| 50/125 MMF                                     | 400                                     | 650                               | 260                                 | 170                                   |
| 50/125 MMF                                     | 500                                     | 750                               | 300                                 | 200                                   |

**Table 6 - Link Reach for Different Fiber Types and Data Rates**

Significantly longer operating distance than the range specified here can be achieved using transmitters, receivers and / or cables meeting specification but performing better than worst case.

## Link Model Parameters

The link reaches above have been calculated using the following link model parameters and Gigabit Ethernet link model version 2.3.5 (filename: 5pmd047.xls).

| Parameter                         | Symbol                | Value | Unit                   |
|-----------------------------------|-----------------------|-------|------------------------|
| Mode partition noise k-factor     | k                     | 0.3   |                        |
| Modal noise                       | MN                    | 0.3   | dB                     |
| Dispersion slope parameter        | $S_O$                 | 0.11  | ps/nm <sup>2</sup> *km |
| Wavelength of zero dispersion     | $U_O$                 | 1320  | nm                     |
| Attenuation coefficient at 850 nm | $\alpha_{\text{dB}}$  | 3.5   | dB/km                  |
| Conversion factor                 | C1                    | 480   | ns.MHz                 |
| Q-factor [BER 10 <sup>-12</sup> ] | Q                     | 7.04  |                        |
| TP4 eye opening                   |                       | 0.3   | UI                     |
| DCD allocation at TP3             | DCD DJ                | 0.08  | UI                     |
| RMS baseline wander S.D.          | $\sigma_{\text{BLW}}$ | 0.025 |                        |
| RIN coefficient                   | $k_{\text{RIN}}$      | 0.70  |                        |
| Conversion factor                 | c_rx                  | 329   | ns.MHz                 |

## Trademarks

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