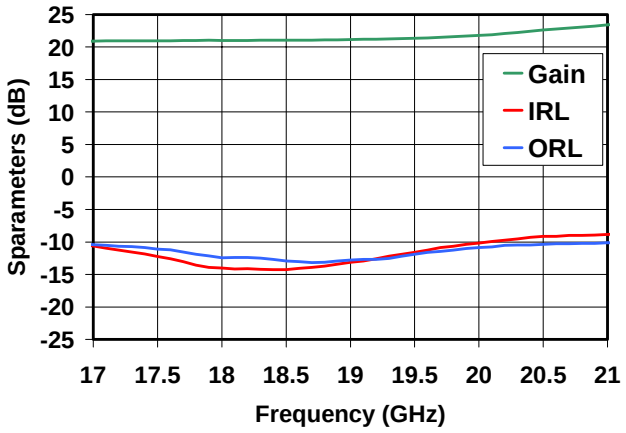
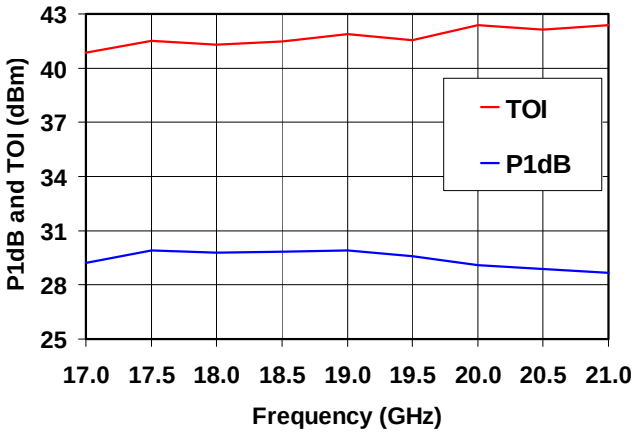
17-21 GHz High Output TOI Packaged Amplifier TGA4530-SM

Measured Fixtured Data

Bias Conditions: Vd = 6V, Id = 825mA





Key Features

- Frequency Range: 17 - 21 GHz
- 41dBm Nominal Output TOI
- 21 dB Nominal Gain
- 29 dBm Nominal P1dB
- 12 dB Nominal Return Loss
- Bias 6 V @ 825 mA
- 4 x 4 QFN with 20 leads
- Package Dimensions 4.0 x 4.0 x 1.2 mm

Primary Applications

- Point-to-Point Radio
- Point-to-Multipoint Communications
- K Band Sat-Com

Product Description

The TriQuint TGA4530 is a High Power Amplifier MMIC for 17 – 21GHz applications. The part is designed using using TriQuint's proven standard 0.25 um power pHEMT production process

The TGA4530 nominally provides 29 dBm saturated output power at the 1 dB compression point and 41dBm OTOI for bias of 6V, 825mA. The typical gain is 22dB.

The part is ideally suited for low cost emerging markets such as Point-to-Point Radio, Point-to-Multi Point Communications, and K-band Satellite Communications.

Evaluation Boards are available upon request.

Lead-Free & RoHS compliant

Note: Device is early in the characterization process prior to finalizing all electrical specifications. Specifications are subject to change without notice

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TABLE I
MAXIMUM RATINGS 1/

SYMBOL	PARAMETER	VALUE	NOTES
Vd	Positive Supply Voltage	8 V	2/
Vg	Negative Supply Voltage Range	-5 TO 0 V	
Id	Positive Supply Current	1.75 A	2/
|Ig|	Negative Supply Current	35 mA	
P_{IN}	Input Continuous Wave Power	26 dBm	
P_D	Power Dissipation	5.4 W	2/ 3/
T_{CH}	Operating Channel Temperature	150 ^{0}C	3/ 4/
T_M	Mounting Temperature (30 Seconds)	260 ^{0}C	
T_{STG}	Storage Temperature	-65 to 150 ^{0}C	

1/ These ratings represent the maximum operable values for this device.

2/ Combinations of supply voltage, supply current, input power, and output power shall not exceed P_D.

3/ When operated at this power dissipation with a base plate temperature of 70 ^{0}C, the median life time of 1.0E+6 hours.

4/ Junction operating temperature will directly affect the device median time to failure (MTTF). For maximum life, it is recommended that junction temperatures be maintained at the lowest possible levels.

TABLE II
THERMAL INFORMATION

PARAMETER	TEST CONDITIONS	T_{CH} (^{O}C)	θ_{JC} (°C/W)	T_M (HRS)
θ_{JC} Thermal Resistance (channel to backside of package)	Vd = 6 V Id = 825 mA P_{diss} = 4.95 W Small Signal T_{base} = 70°C	143	14.8	1.8E+6
θ_{JC} Thermal Resistance (channel to backside of package)	Vd = 6 V Id = 970 mA @ Psat P_{diss} = 4.4 W P_{out} = 1.4 W (RF) T_{base} = 70°C	135	14.8	3.8E+6

Note: Backside of package is the 70°C baseplate temperature. Thermal transfer is from the backside of the package through the board. The board must be designed to assure adequate thermal transfer from the package. Worst case condition is with no RF applied, 100% of DC power is dissipated.

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TABLE III

ELECTRICAL CHARACTERISTICS

(Ta = 25^0 C Nominal)

PARAMETER	TYPICAL	UNITS
Frequency Range	17 - 21	GHz
Drain Voltage, Vd	6	V
Drain Current, Id	825	mA
Gate Voltage Typical, Vg	-0.5	V
Small Signal Gain, S21	21	dB
Input Return Loss, S11	12	dB
Output Return Loss, S22	12	dB
Reverse Isolation, S12	55	dB
Saturated Output Power @ 16 dBm Pin, Psat	31	dBm
Output Power @ 1dB Gain Compression, P1dB	29	dBm
Output Third Order Intercepted Point	41	dBm
Small Signal Gain Temperature Coefficient	-0.03	dB/^{0}C

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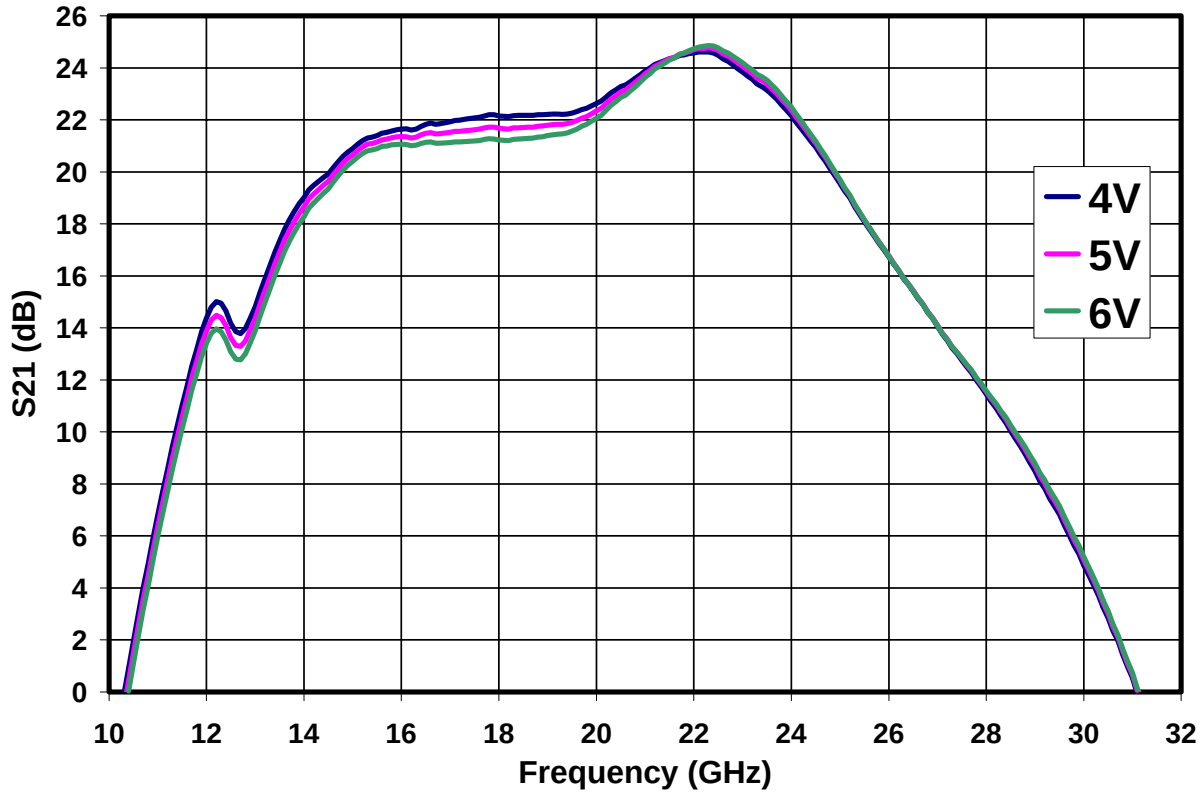
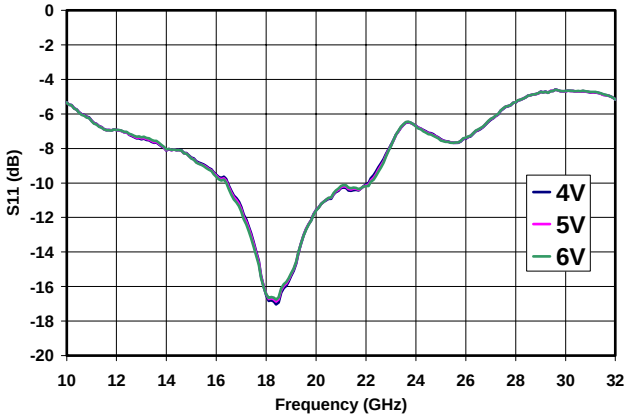
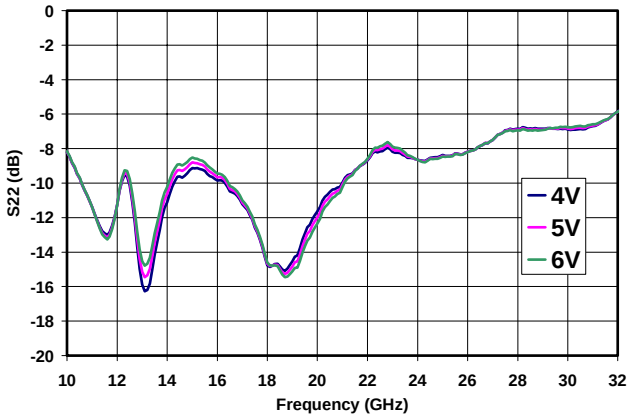
Measured Performance

Vd = 4- 6V, Idq = 825mA
Vg = -0.5 V (typ.)







Measured Performance

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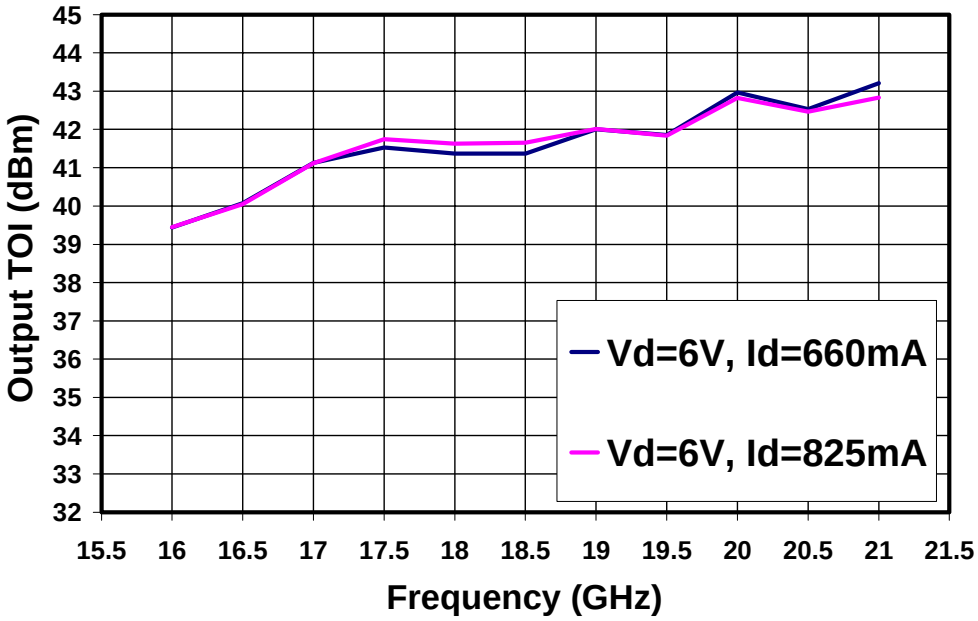
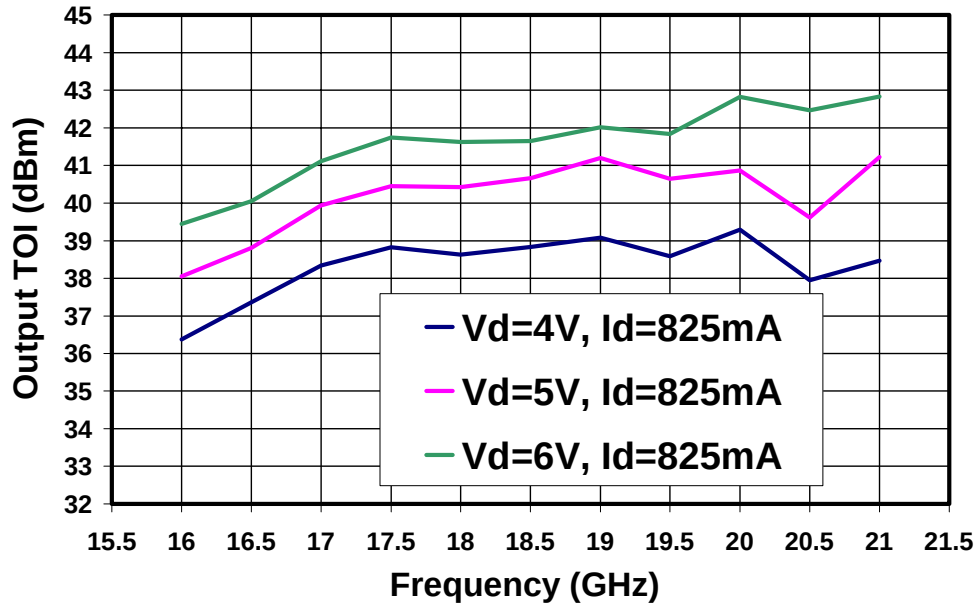
Vg = -0.5 V (typ.)



Vd = 4-6V, Id = 825mA
Vg = -0.5 V (typ.)



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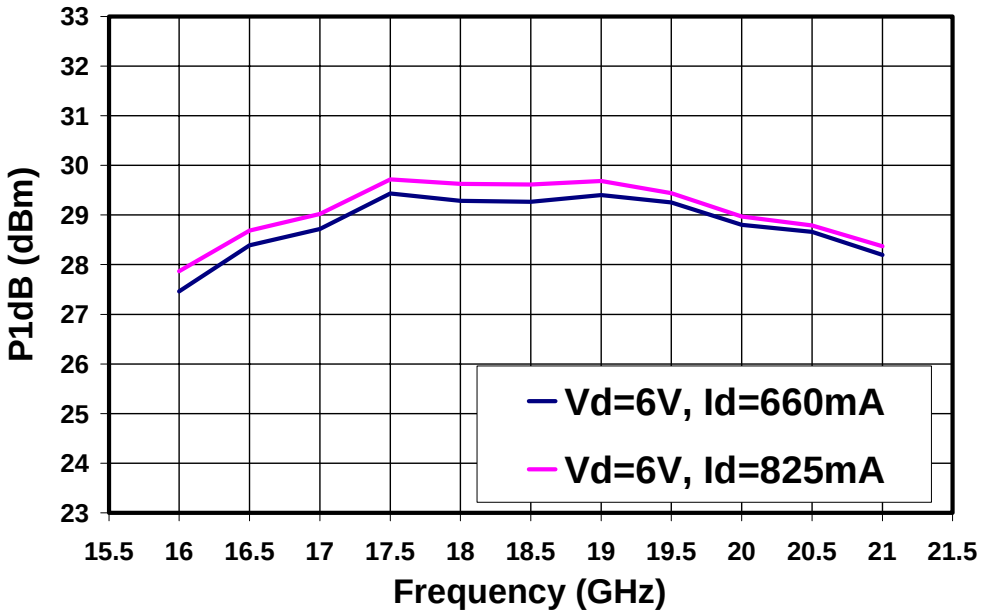
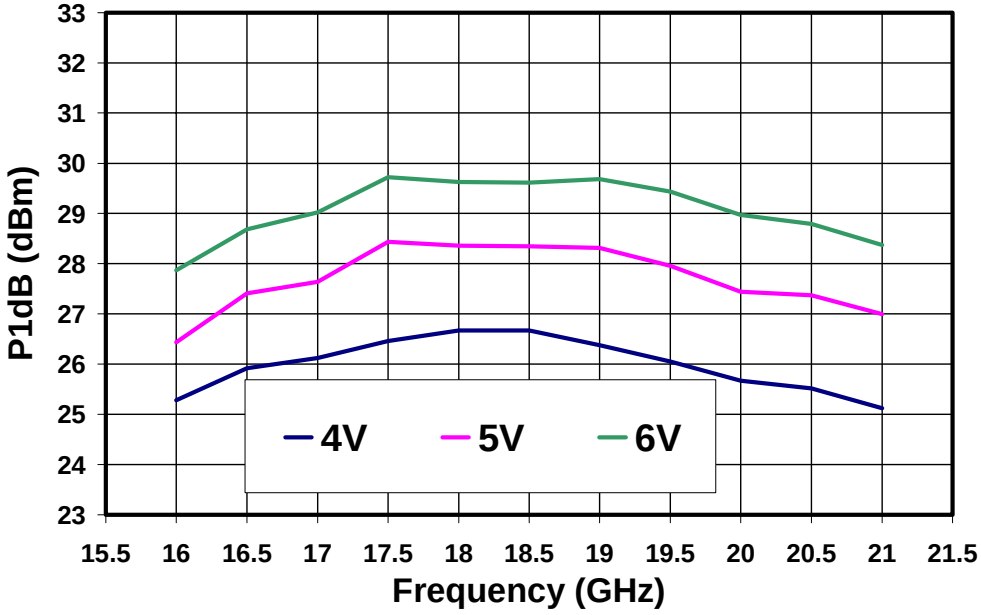
Measured Performance

Vd = 6V, Idq = 660, 825mA
Vg = -0.5 V (typ.)



Vd = 4-6V, Idq = 825mA
Vg = -0.5 V (typ.)



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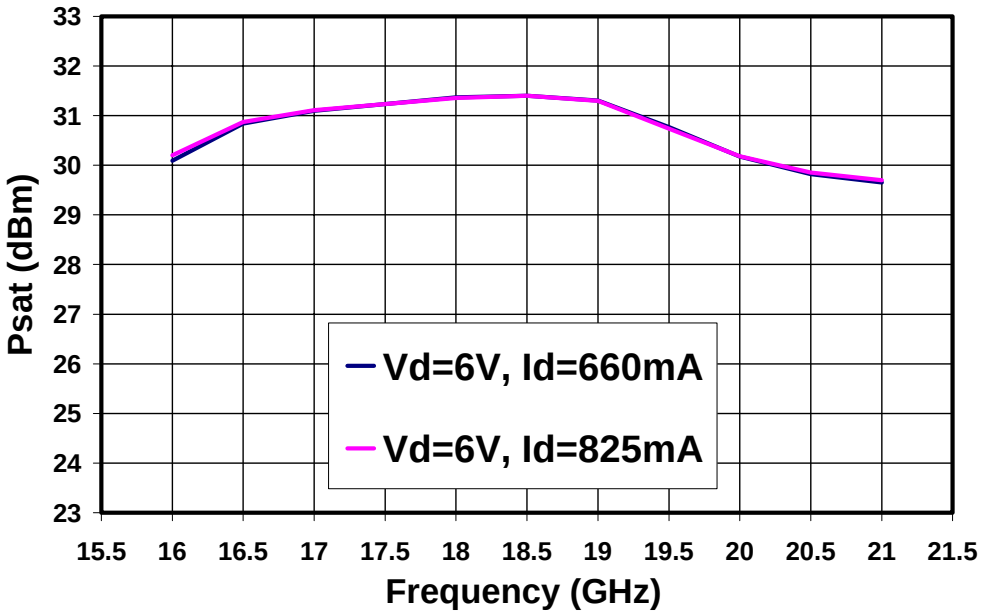
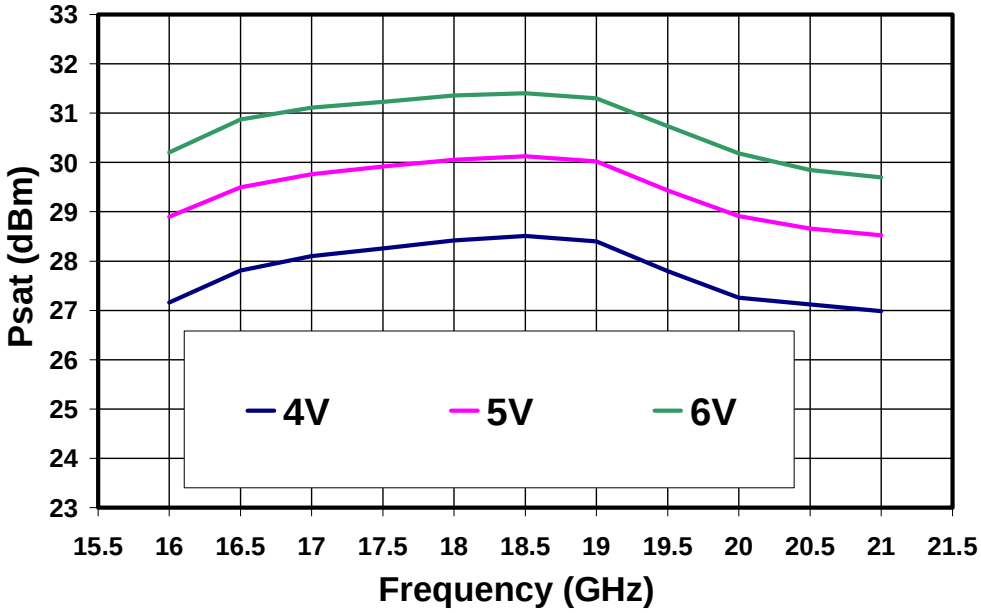
Measured Performance

Vd = 6V, Idq = 660, 825mA
Vg = -0.5 V (typ.)



Vd = 4-6V, Idq = 825mA
Vg = -0.5 V (typ.)



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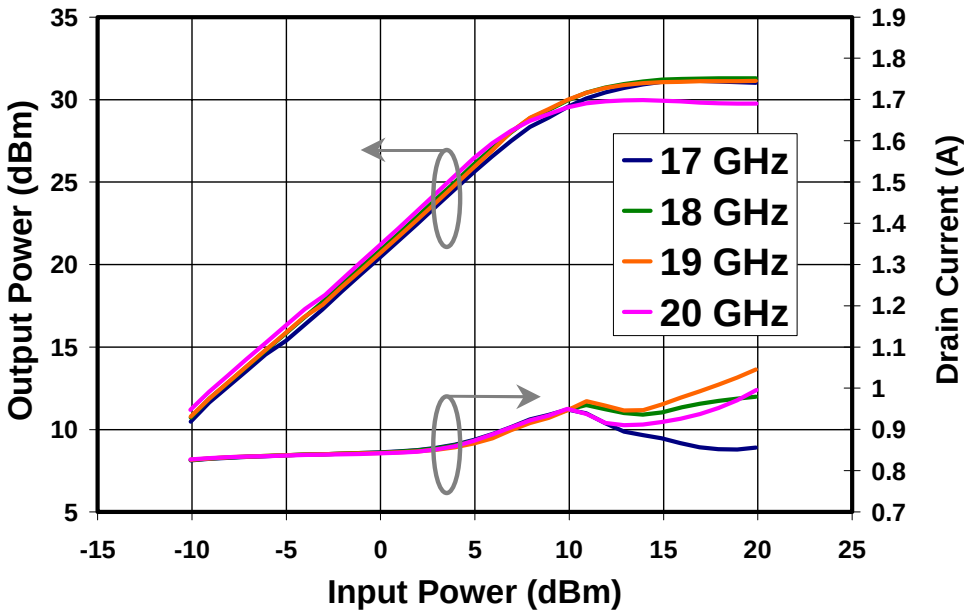
Measured Performance

Vd = 6V, Idq = 825mA
Vg = -0.5 V (typ.)



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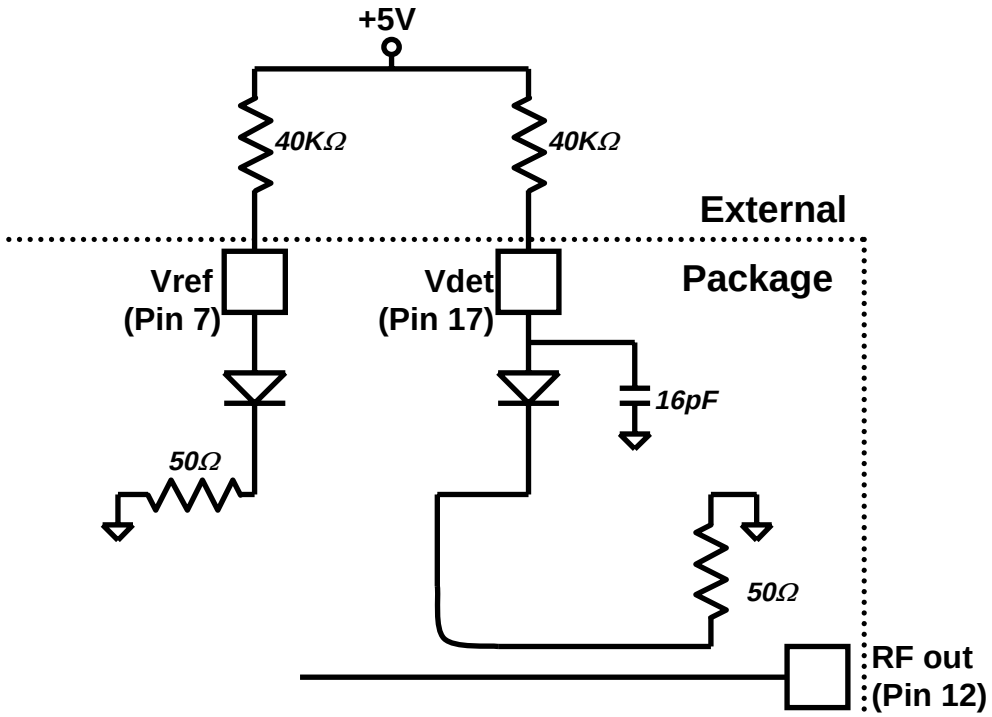
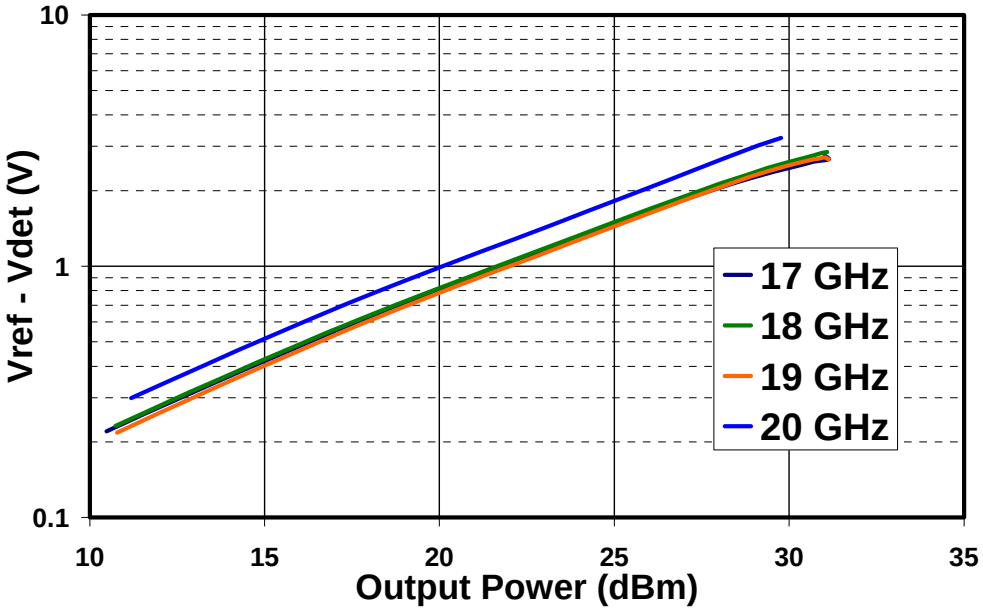
Power Detector



Vd = 6V, Idq = 825mA
Vg = -0.5 V (typ.)



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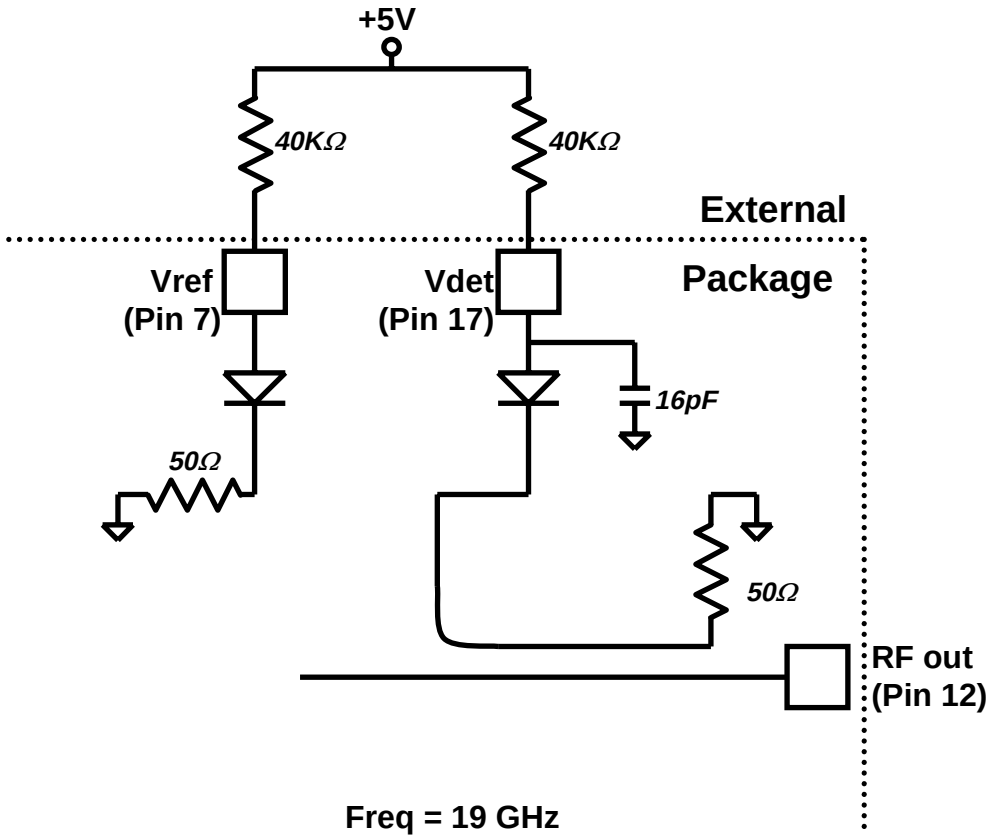
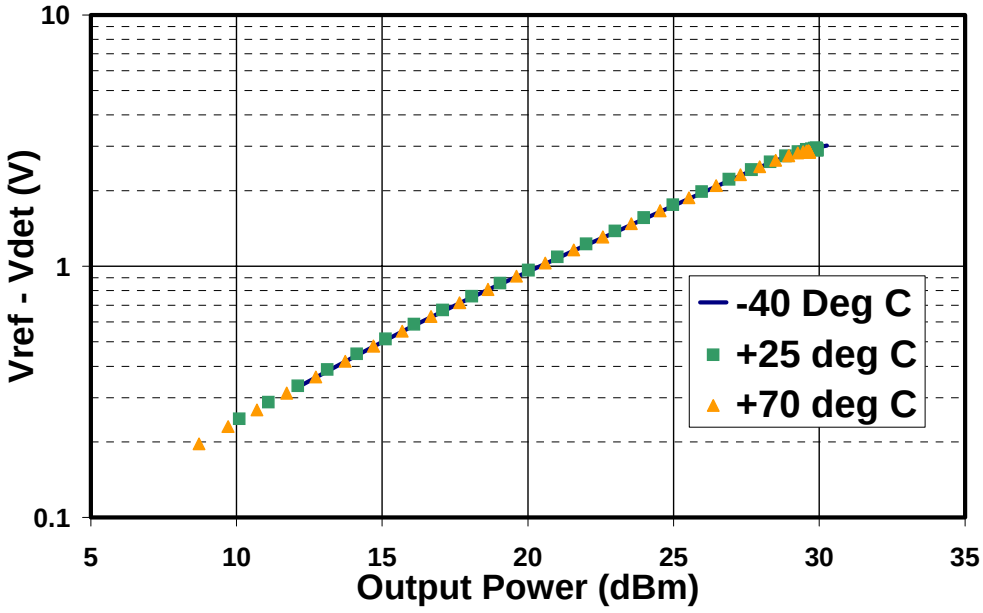
Power Detector



Freq = 19 GHz
Vd = 6V, Idq = 825mA
Vg = -0.5 V (typ.)



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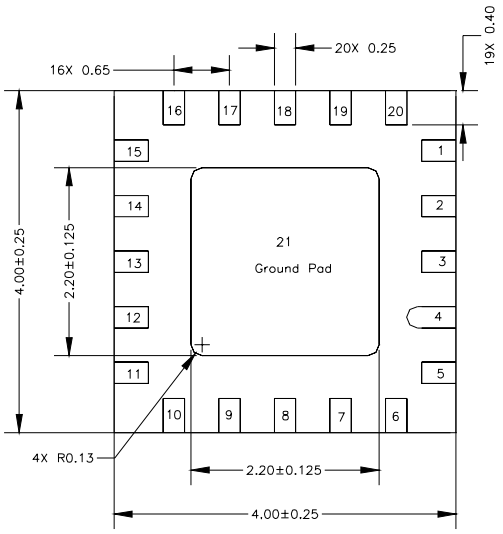
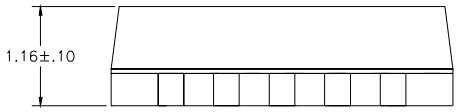
Mechanical Drawing



BOTTOM VIEW



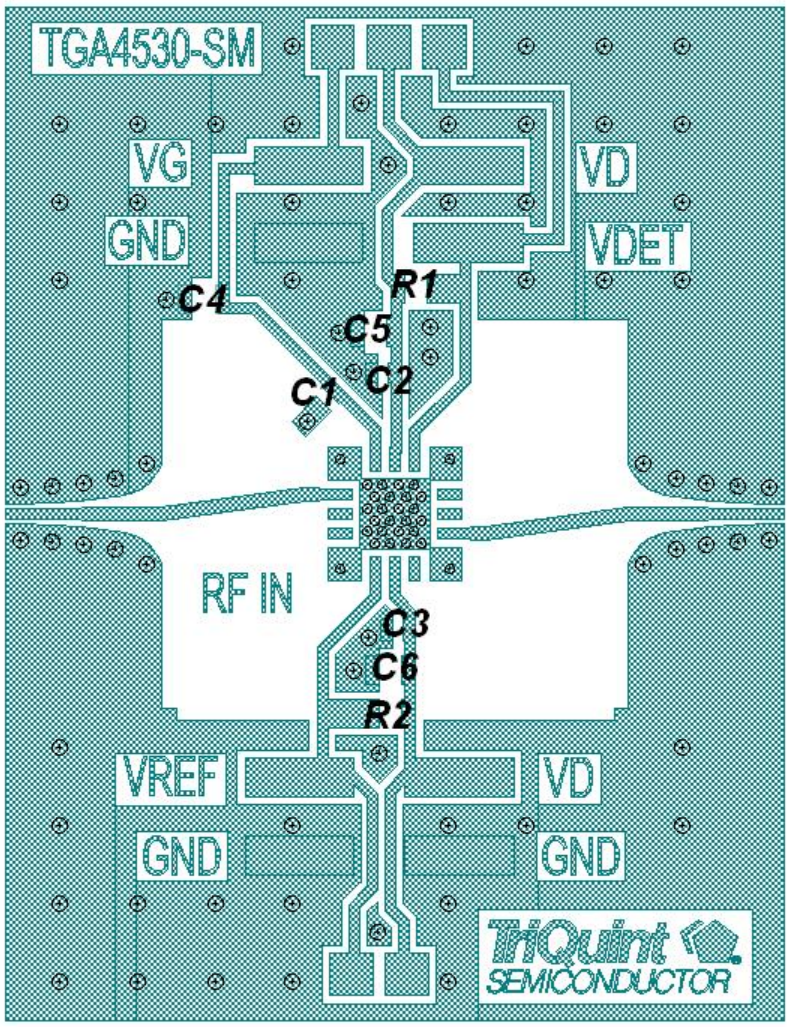
SIDE VIEW

Dimensions are in millimeters
Dot on topside indicates Pin 1
Tolerances: ±0.076, unless otherwise noted

Pin	Description
1, 5, 6, 10, 11, 15, 16, 20, 21	GND
3, 4, 9, 13,14	NC
2	RF Input
19	Vg
8 and 18	Vd
12	RF Output
17	Vdet
7	Vref

GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

Recommended Evaluation Board Layout Assembly *



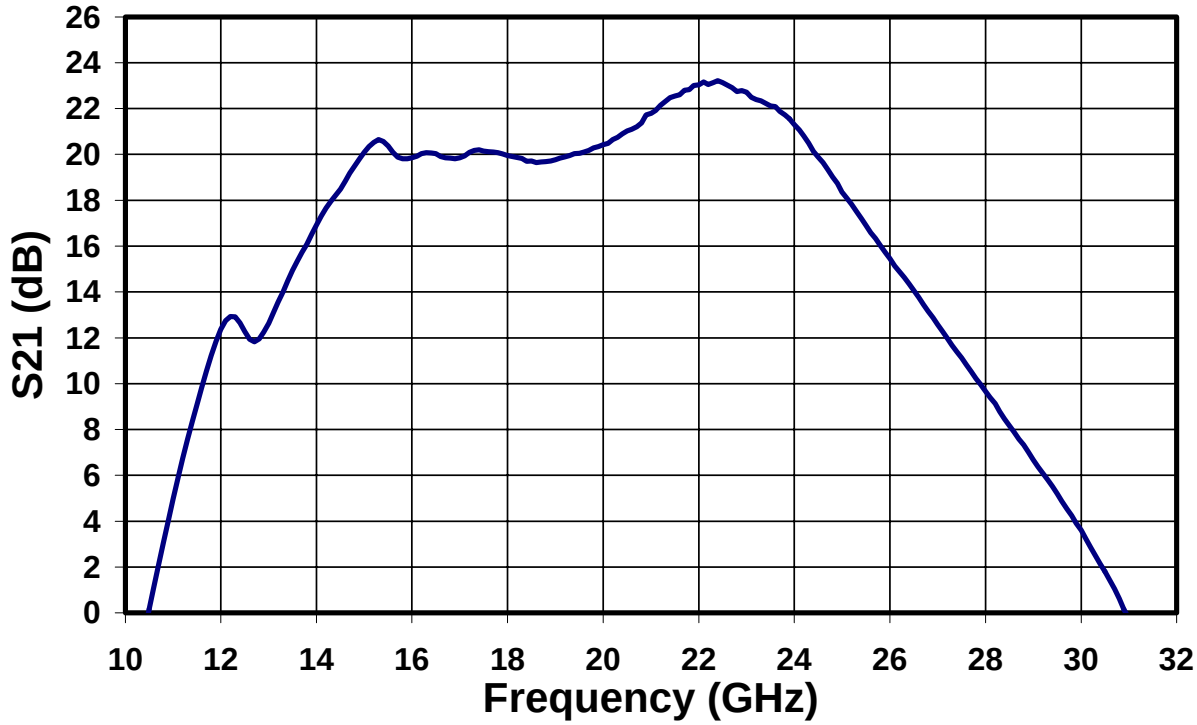
Designator	Component
C1,C2,C3	100pF Capacitor
C4,C5,C6	1uF Capacitor
R1,R2	40.2kOhm Resistor

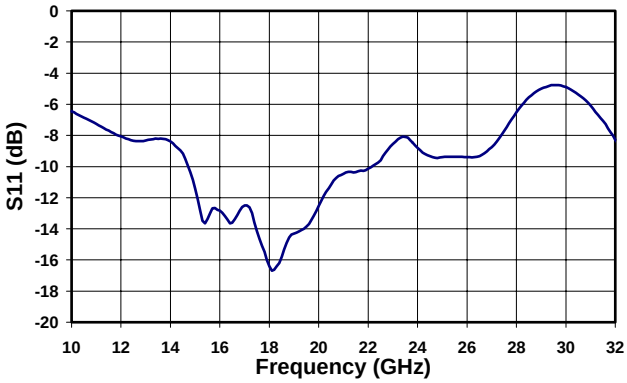
* PCB is RO4003 8 mil thickness, 0.5 oz standard copper cladding, with Er = 3.38.

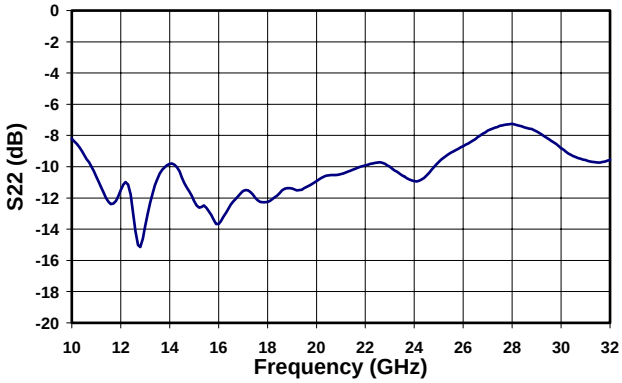
GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

Measured Performance - Evaluation Board

Vd = 6V, Id = 825mA
Vg = -0.5 V (typ.)







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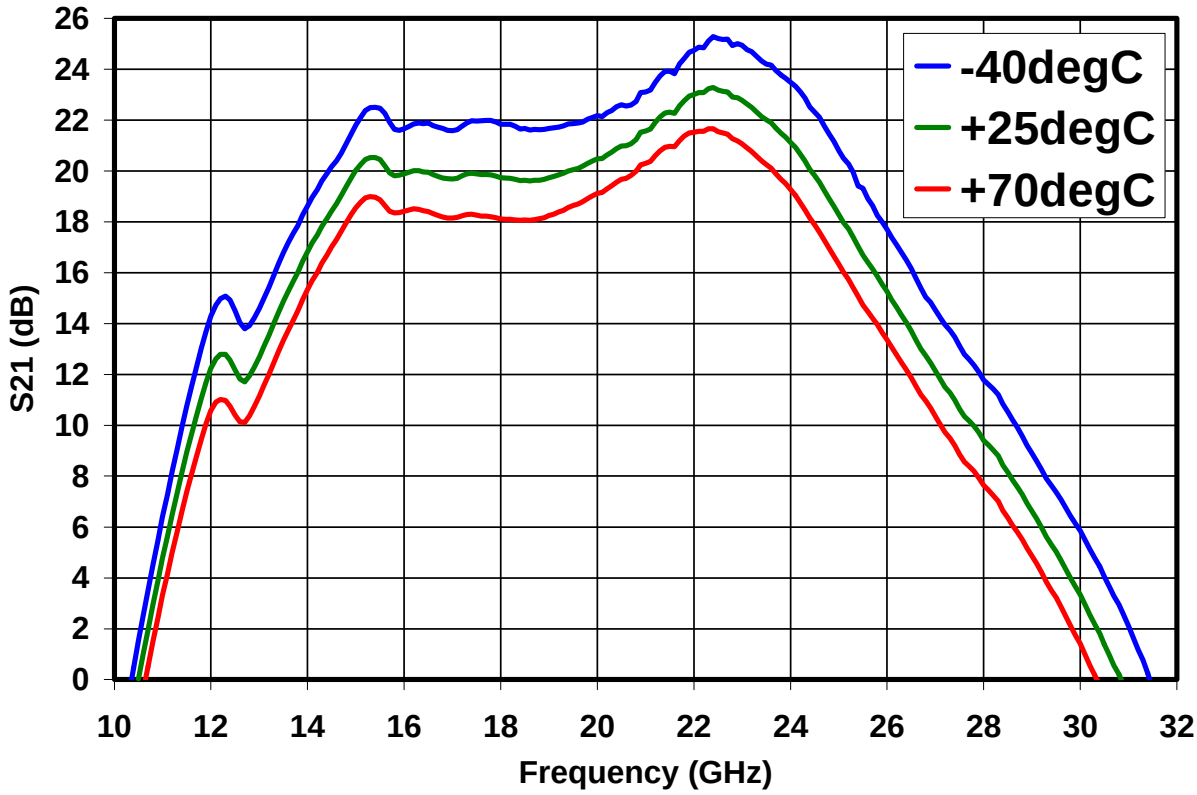
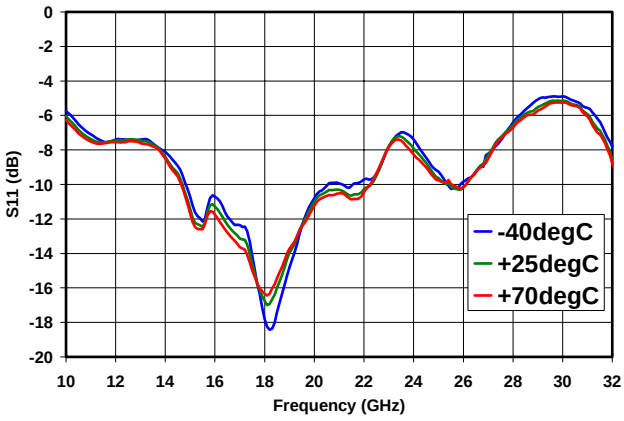
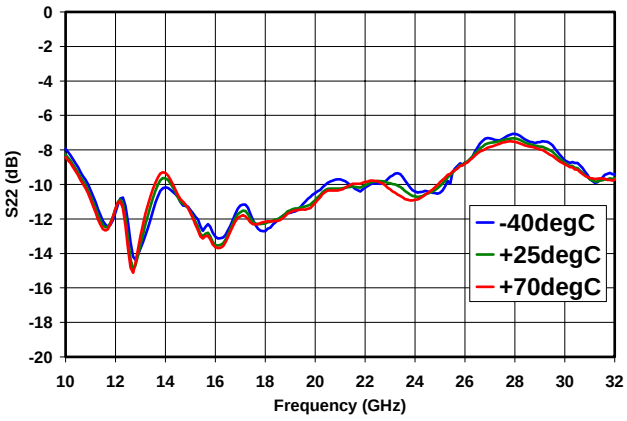
Measured Performance - Evaluation Board
Data includes connector and board losses

Vd = 6V, Id = 825mA
Vg = -0.5 V (typ.)







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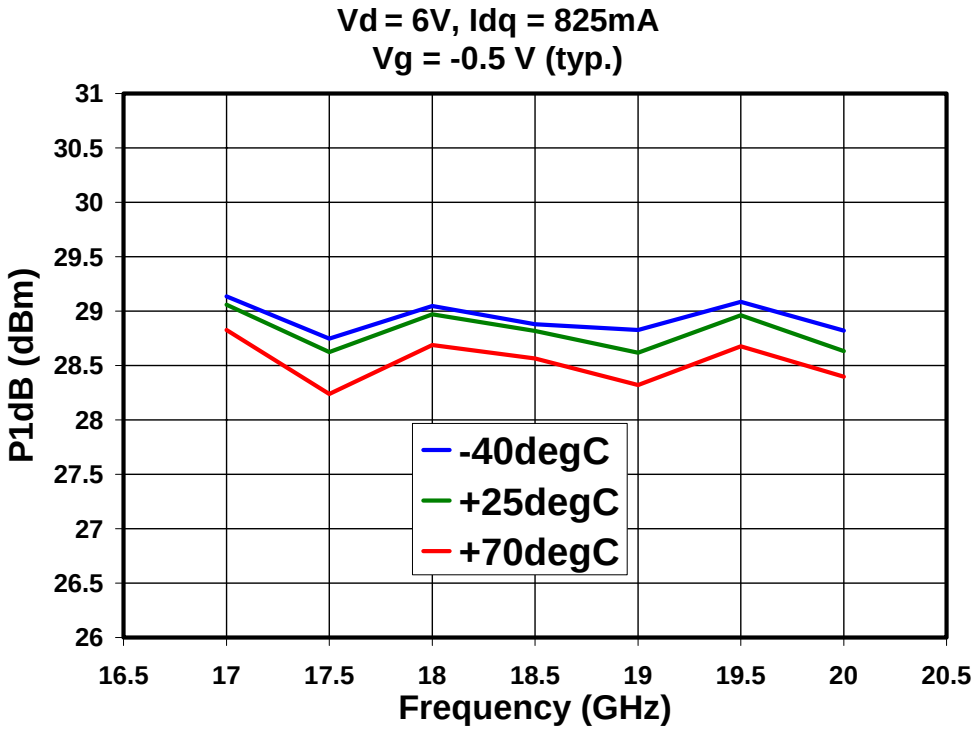
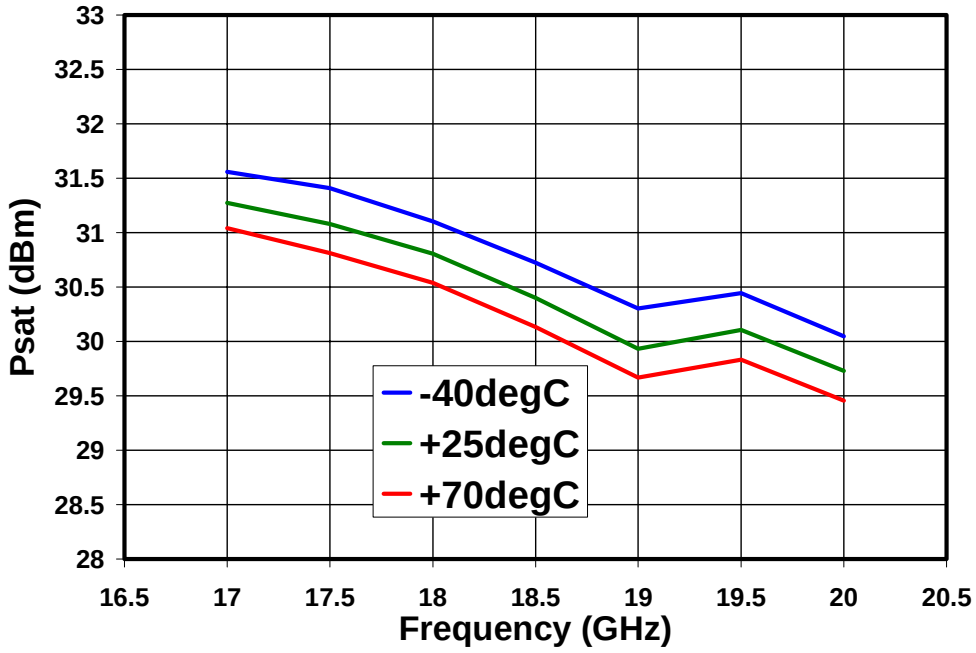
Measured Performance - Evaluation Board
Data includes connector and board losses





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Recommended Surface Mount Package Assembly

Proper ESD precautions must be followed while handling packages.

Clean the board with acetone. Rinse with alcohol. Allow the circuit to fully dry.

TriQuint recommends using a conductive solder paste for attachment. Follow solder paste and reflow oven vendors' recommendations when developing a solder reflow profile. Typical solder reflow profiles are listed in the table below.

Hand soldering is not recommended. Solder paste can be applied using a stencil printer or dot placement. The volume of solder paste depends on PCB and component layout and should be well controlled to ensure consistent mechanical and electrical performance.

Clean the assembly with alcohol.

Typical Solder Reflow Profiles

Reflow Profile	SnPb	Pb Free
Ramp-up Rate	3 °C/sec	3 °C/sec
Activation Time and Temperature	60 – 120 sec @ 140 – 160 °C	60 – 180 sec @ 150 – 200 °C
Time above Melting Point	60 – 150 sec	60 – 150 sec
Max Peak Temperature	240 °C	260 °C
Time within 5 °C of Peak Temperature	10 – 20 sec	10 – 20 sec
Ramp-down Rate	4 – 6 °C/sec	4 – 6 °C/sec

Ordering Information

Part	Package Style
TGA4530-SM	QFN 4x4 Surface Mount