HT1087 Series
0.5A General Purpose LDO

Features

- Output voltage ranges: Fixed range of 1.5V, 1.8V, 2.5V, 3.3V, 5.0V or adjustable type.
- High accuracy: ±2%
- Low voltage drop: 1.1V (typ.), V_{OUT}=5.0V at 0.5A
- Guaranteed output current: 0.5A
- Low quiescent current: 8mA (typ.)
- Integrated current limit & thermal protection circuits
- SOT-89, TO-92 packages

Applications

- Active SCSI terminations
- Post regulator for switching power supplies
- Low voltage microcontrollers
- Motherboard clock supplies
- Battery chargers

General Description

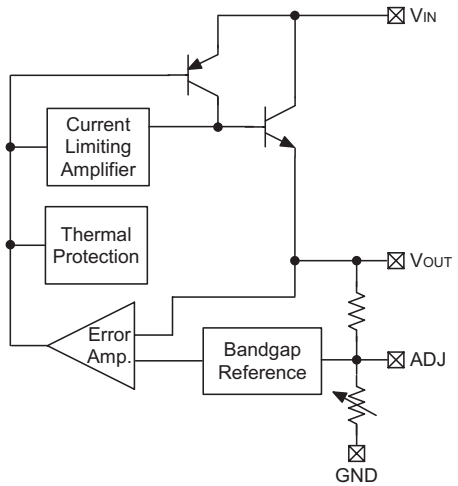
The HT1087 devices are a series of three-terminal high current low voltage regulators. They can deliver an output current of 0.5A and can accept input voltages up to 10V. The devices are available in both adjustable and fixed output voltage type with a range of 1.5V to 5.0V. Internal current limit and thermal protection circuits provide protection against overload conditions that could create excessive junction temperatures.

Selection Table

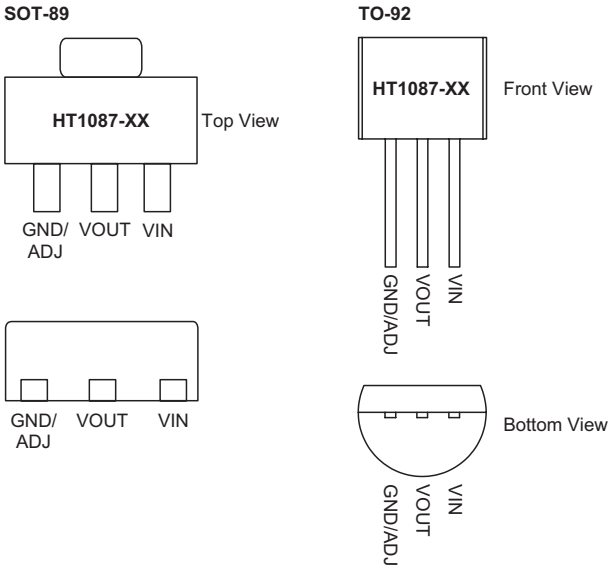
Part No.	Output Voltage	Package	Marking
HT1087-ADJ	Adjust	SOT-89 TO-92	HT1087-ADJ HT1087-15 HT1087-18 HT1087-25 HT1087-33 HT1087-50
HT1087-15	1.5V		
HT1087-18	1.8V		
HT1087-25	2.5V		
HT1087-33	3.3V		
HT1087-50	5.0V		

Note: For lead free devices, a "#" mark is suffixed at the end of the date code.

Block Diagram



Pin Assignment



Absolute Maximum Ratings*

Input Supply Voltage .....V_{SS}–0.3V to V_{SS}+12V Storage Temperature .....–50°C to 125°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

"*" Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. The guaranteed specifications apply only for the test conditions listed.

Recommended Operating Conditions

Input Supply Voltage .....V_{SS}–0.3V to V_{SS}+10V Ambient Temperature .....–40°C to 85°C

Thermal Information

Symbol	Parameter	Package	Max.	Unit
θ_{JA}	Thermal Resistance (Junction to Ambient) (Assume no ambient airflow, no heat sink)	SOT-89	200	°C/W
		TO-92	200	°C/W
P_D	Power Dissipation	SOT-89	0.5	W
		TO-92	0.5	W

Note: P_D is measured at Ta= 25°C

Electrical Characteristics

T_J=25°C, V_{IN}=V_{OUT}+1.5V, I_O=10mA, unless otherwise specified (see note 1)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{REF}	Reference Voltage	2.75V ≤ V_{IN} ≤ 10V	1.225	1.250	1.275	V
V_{OUT}	Output Voltage	HT1087-15	1.470	1.500	1.530	V
		HT1087-18	1.764	1.800	1.836	V
		HT1087-25	2.450	2.500	2.550	V
		HT1087-33	3.234	3.300	3.366	V
		HT1087-50	4.900	5.000	5.100	V
ΔV_{LOAD}	Load Regulation (see note 2)	I_{OUT}=0.5A	1	—	20	mV
ΔV_{LINE}	Line Regulation	2.75V ≤ V_{IN} ≤ 10V	—	0.015	0.15	%/V
V_{DIF}	Dropout Voltage (see note 3)	ΔV_{OUT}=2%, I_{OUT}=0.5A	—	1.1	1.3	V
I_{LIMIT}	Current Limit (see note 4)	ΔV_{OUT}=10%	0.5	1.5	—	A
I_{SS}	Quiescent Current (Fixed Version)	2.75V ≤ V_{IN} ≤ 10V	—	8	13	mA
RR	Ripple Rejection	120Hz input ripple C_{OUT}=22μF	—	60	—	dB
$\frac{\Delta V_{OUT}}{\Delta T_a}$	Temperature Coefficient	–40°C<Ta<85°C	—	±0.4	—	mV/°C

Note: 1. Specifications are production tested at room temperature, Ta. Specifications within the –40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

2. Load regulation is measured at a constant junction temperature, using pulse testing with a low ON time and is guaranteed up to the maximum power dissipation. Power dissipation is determined by the input/output differential voltage and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range. The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - Ta) / \theta_{JA}$.

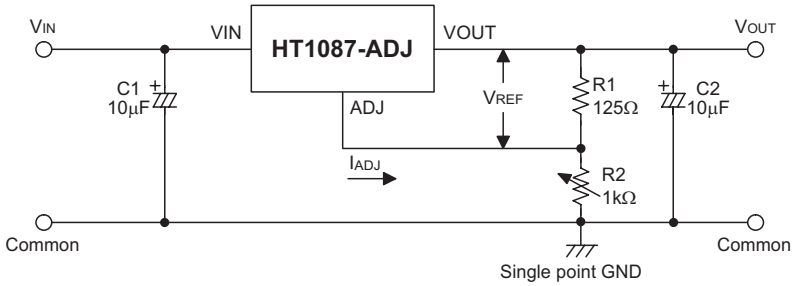
3. Dropout voltage is defined as the input voltage minus the output voltage that produces a 2% change in the output voltage from the value at V_{IN} = V_{OUT}+1.5V with a fixed load.

4. Current limit is measured by pulsing for a short time.

Application Circuits

Basic Circuits

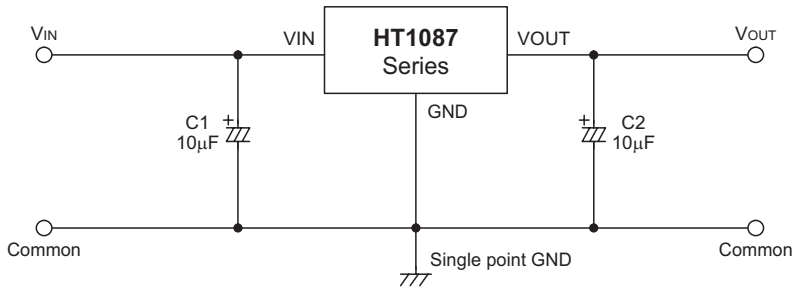
- Variable voltage type



$$V_{OUT} = V_{REF} \left(1 + \frac{R2}{R1} \right) + I_{ADJ} R2$$

Note: C1 is required if the needed if the device is located far from filter capacitors, the recommended value is 10μF.
C2 is required for stability, the recommended value is 10μF.
R1 is required for regulation, the recommended value is 125Ω.

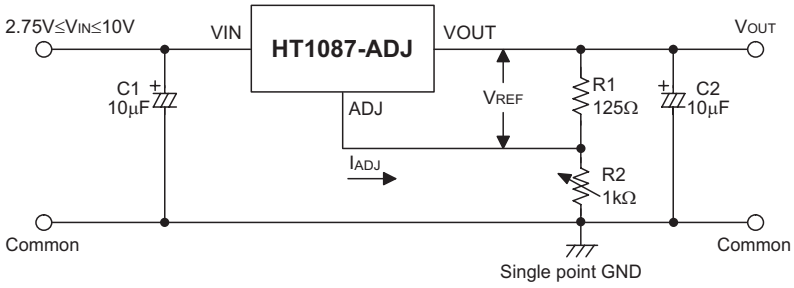
- Fixed voltage type



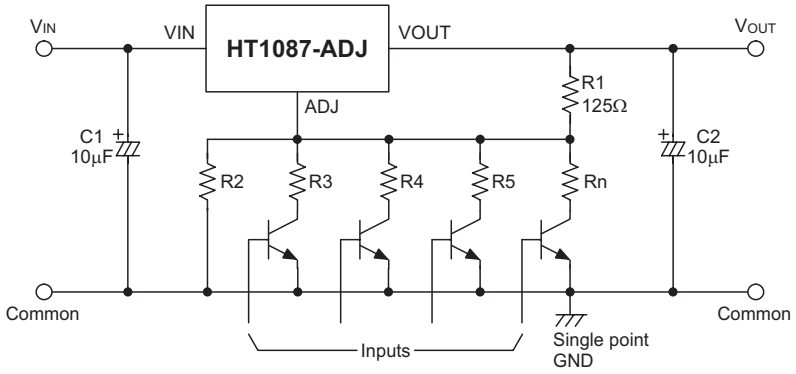
Note: C1 is required if the needed if the device is located far from filter capacitors, the recommended value is 10μF.
C2 is required for stability, the recommended value is 10μF.

Typical Application Circuits

- 1.25~9V regulator

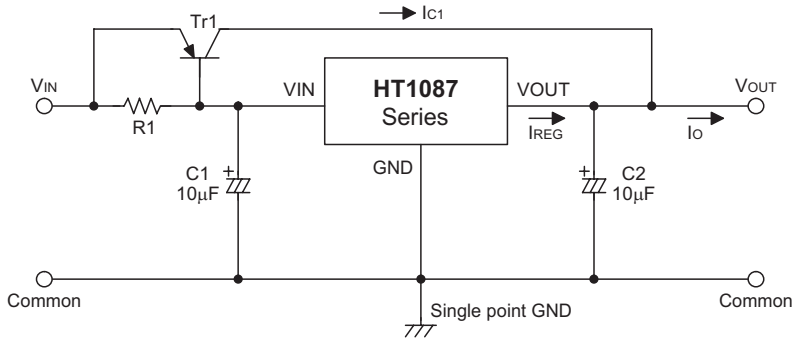


- Digitally selected outputs



Note: R2 can set the maximum voltage.

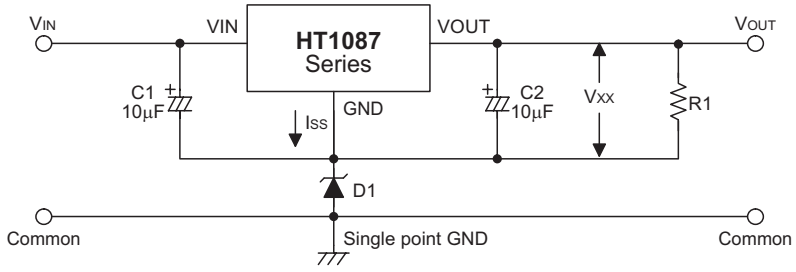
- High output current positive voltage regulator



$$R_1 = \frac{V_{BE1}}{I_{REG} - \frac{I_{C1}}{(1+\beta)}},$$

$$I_O = I_{C1} + I_{REG}$$

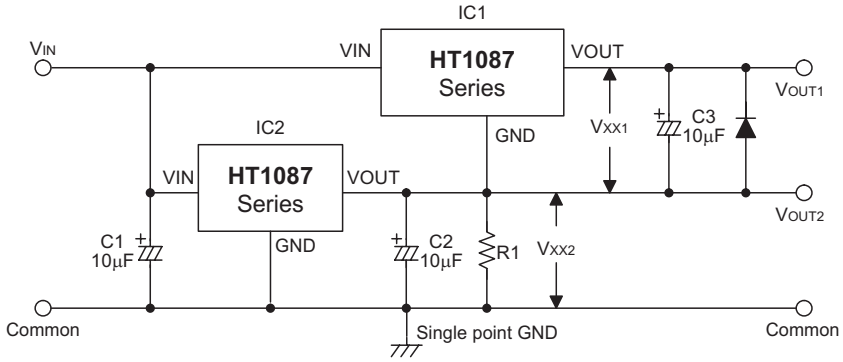
- Increased Output voltage Circuit



$$V_{OUT} = V_{XX} + V_{D1}$$

$$R_1 = \frac{V_{XX}}{I_{SS}}$$

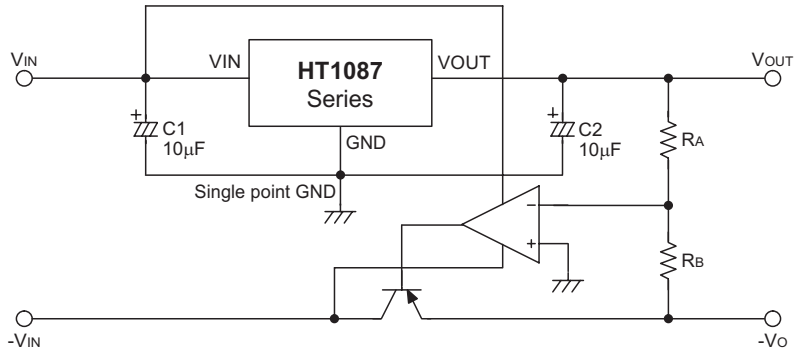
- Dual Supply Circuit



$$V_{OUT1} = V_{XX2} + V_{XX1}$$

$$V_{OUT2} = V_{XX2}$$

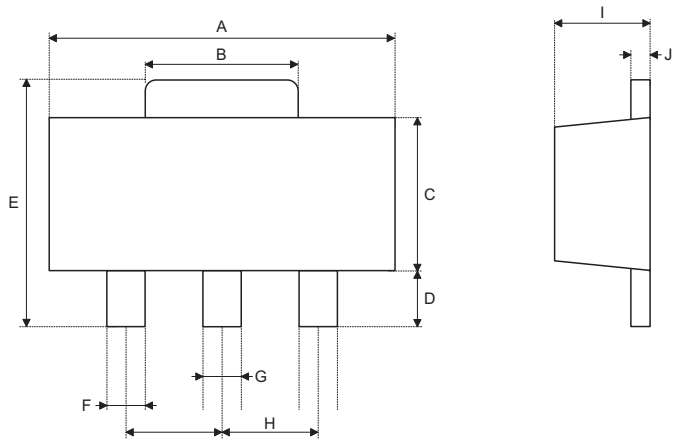
- Tracking Voltage Regulator



$$-V_O = V_O \times \frac{R_A}{R_B}$$

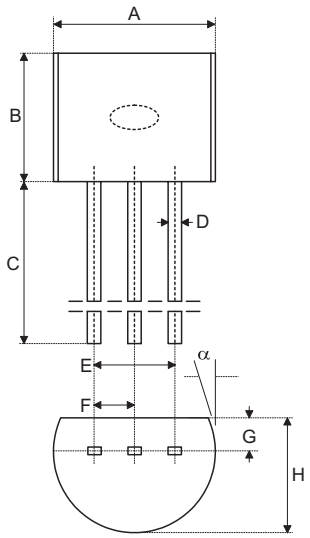
Package Information

3-Pin SOT-89 Outline Dimensions



Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	173	—	181
B	64	—	72
C	90	—	102
D	35	—	47
E	155	—	167
F	14	—	19
G	17	—	22
H	—	59	—
I	55	—	63
J	14	—	17

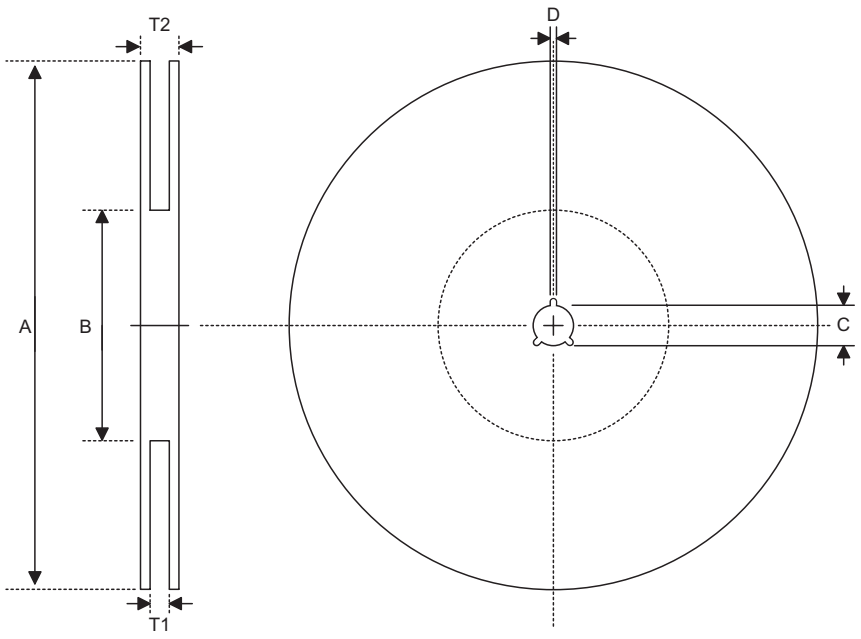
3-Pin TO-92 Outline Dimensions



Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	170	—	200
B	170	—	200
C	500	—	—
D	11	—	20
E	90	—	110
F	45	—	55
G	45	—	65
H	130	—	160
I	8	—	18
α	4°	—	6°

Product Tape and Reel Specifications

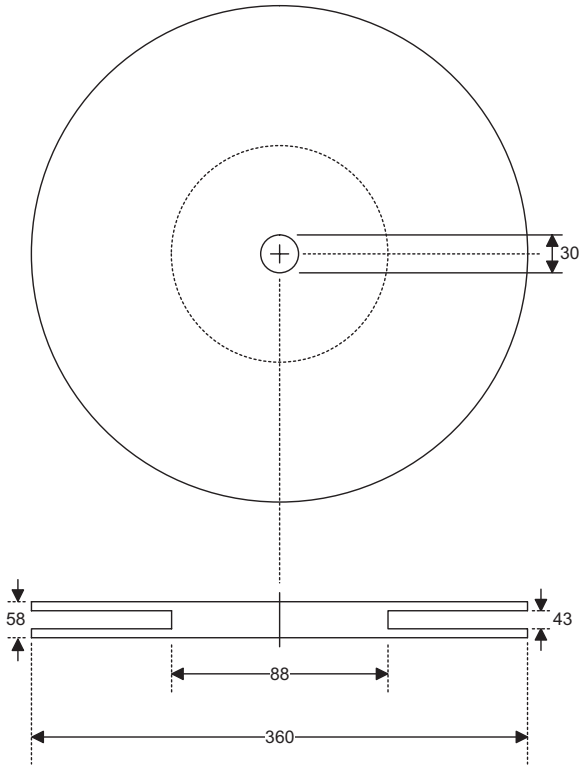
Reel Dimensions

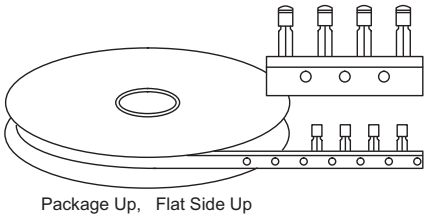


SOT-89

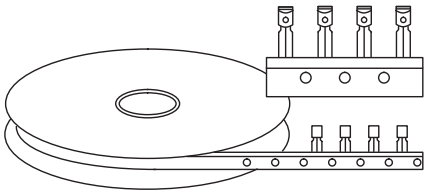
Symbol	Description	Dimensions in mm
A	Reel Outer Diameter	180±1.0
B	Reel Inner Diameter	62±1.5
C	Spindle Hole Diameter	12.75+0.15
D	Key Slit Width	1.9±0.15
T1	Space Between Flange	12.4+0.2
T2	Reel Thickness	17−0.4

TO-92 Reel Dimensions (Unit: mm)



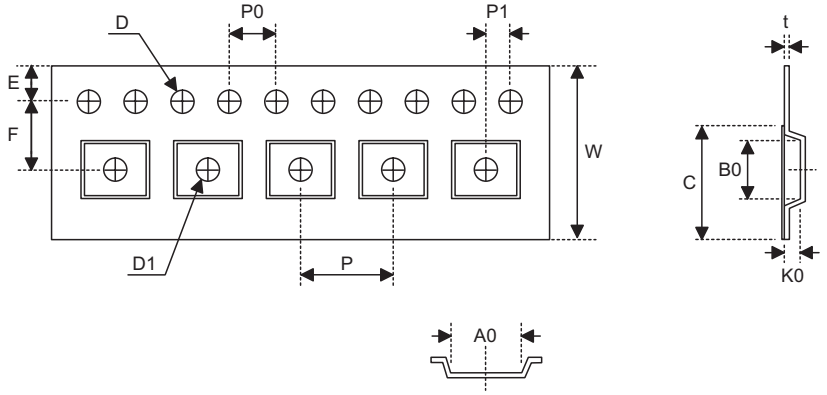


Package Up, Flat Side Up



Package Up, Flat Side Down

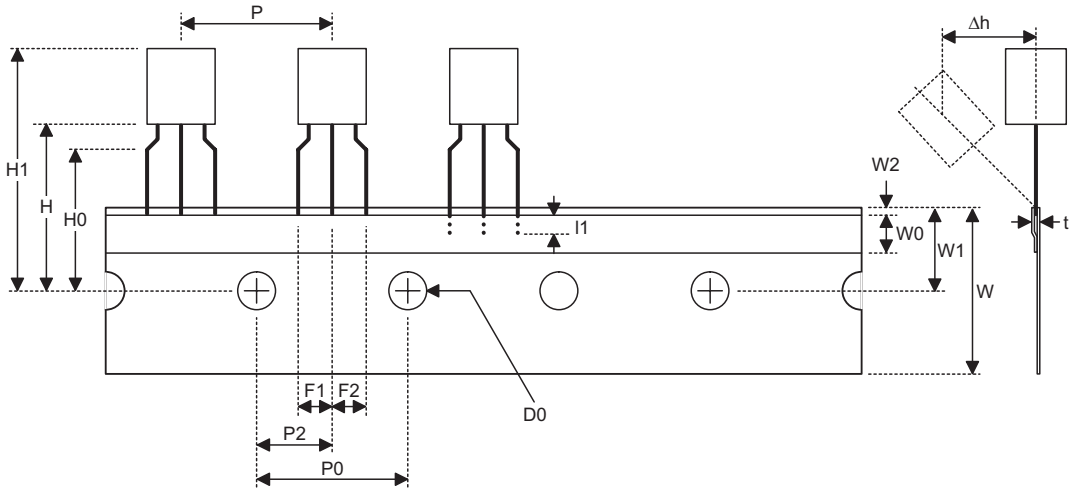
Carrier Tape Dimensions



SOT-89

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	12.0+0.3 −0.1
P	Cavity Pitch	8.0±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	5.5±0.05
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.1
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.10
A0	Cavity Length	4.8±0.1
B0	Cavity Width	4.5±0.1
K0	Cavity Depth	1.8±0.1
t	Carrier Tape Thickness	0.30±0.013
C	Cover Tape Width	9.3

TO-92 Carrier Tape Dimensions



TO-92

Symbol	Description	Dimensions in mm
I1	Taped Lead Length	(2.5)
P	Component Pitch	12.7±1.0
P_0	Perforation Pitch	12.7±0.3
P_2	Component to Perforation (Length Direction)	6.35±0.4
F_1	Lead Spread	2.5+0.4 −0.1
F_2	Lead Spread	2.5+0.4 −0.1
Δh	Component Alignment	0±0.1
W	Carrier Tape Width	18.0+1.0 −0.5
W_0	Hold-down Tape Width	6.0±0.5
W_1	Perforation Position	9.0±0.5
W_2	Hold-down Tape Position	(0.5)
H_0	Lead Clinch Height	16.0±0.5
H_1	Component Height	Less than 24.7
D_0	Perforation Diameter	4.0±0.2
t	Taped Lead Thickness	0.7±0.2
H	Component Base Height	19.0±0.5

Note: Thickness less than 0.38±0.05mm~0.5mm

P0 Accumulated pitch tolerance: ±1mm/20pitches.

( ) Bracketed figures are for consultation only

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