

Typical Applications

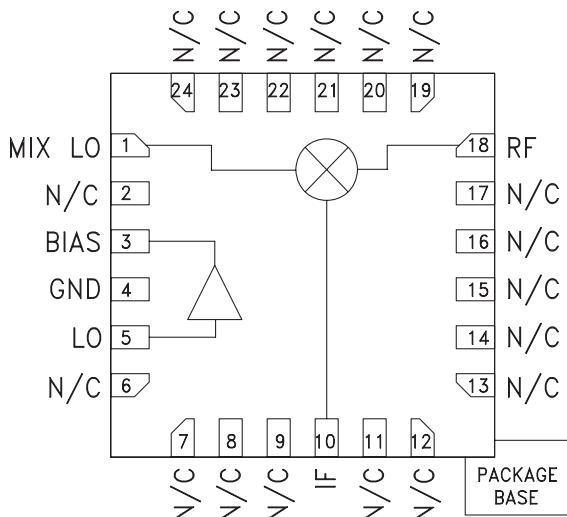
The HMC615LP4 / HMC615LP4E is ideal for:

- PCS / 3G Infrastructure
- Base Stations & Repeaters
- WiMAX & WiBro
- ISM & Fixed Wireless

Features

- High Input IP3: +35 dBm
- Low Input LO Drive: -2 to +6 dBm
- Low Conversion Loss: 10 dB
- Single Positive Supply: +5V @ 65 mA
- RoHS Compliant 4x4 mm QFN Package

Functional Diagram



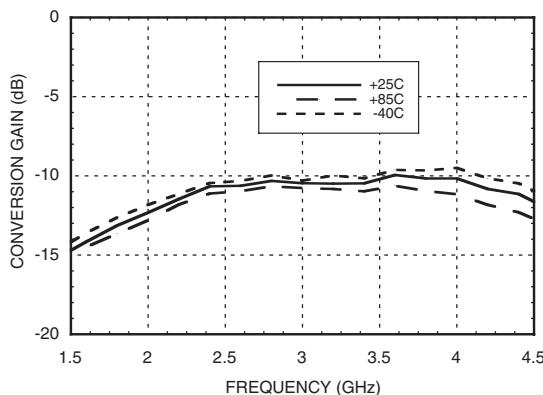
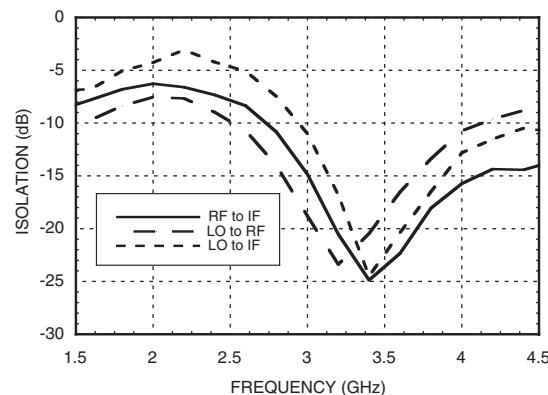
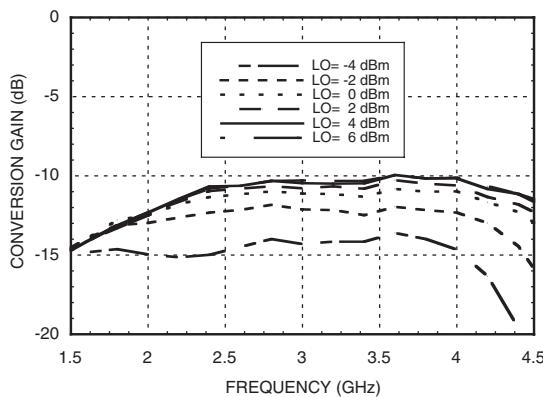
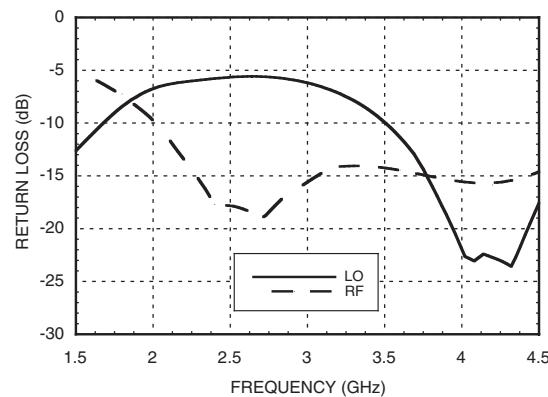
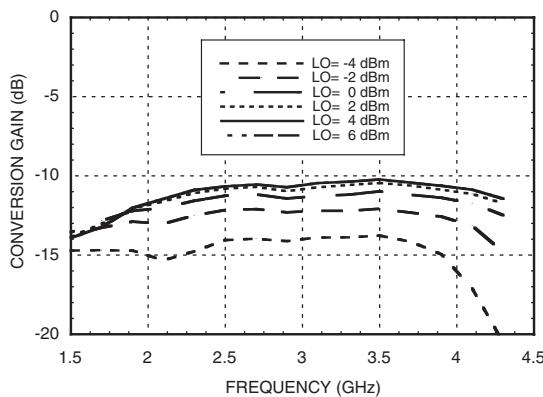
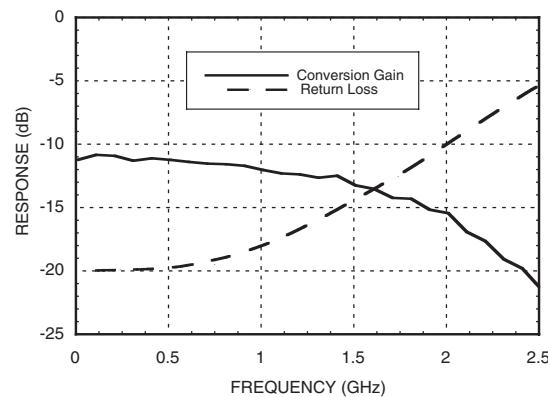
General Description

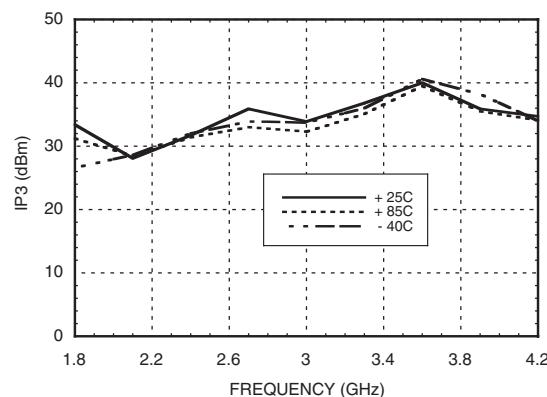
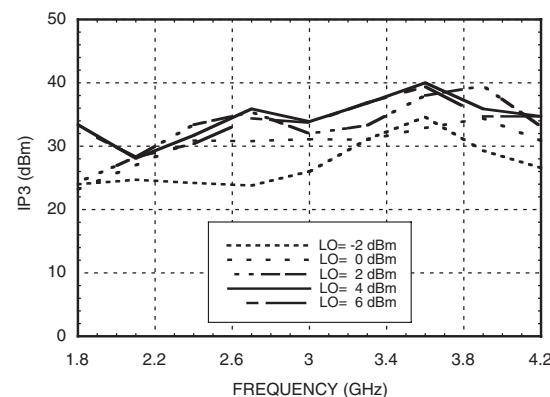
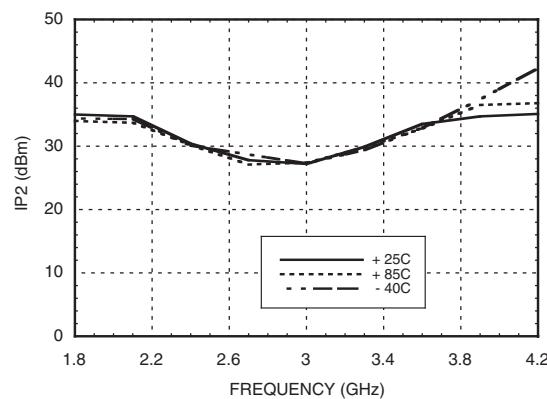
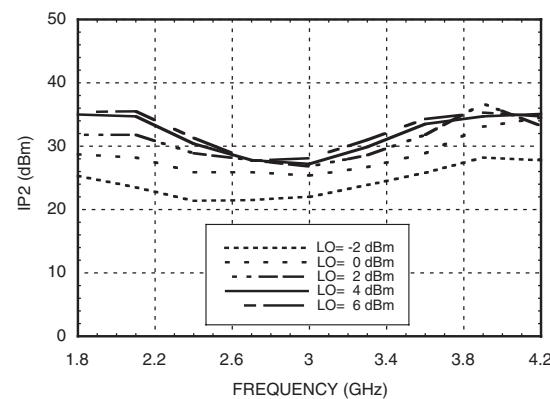
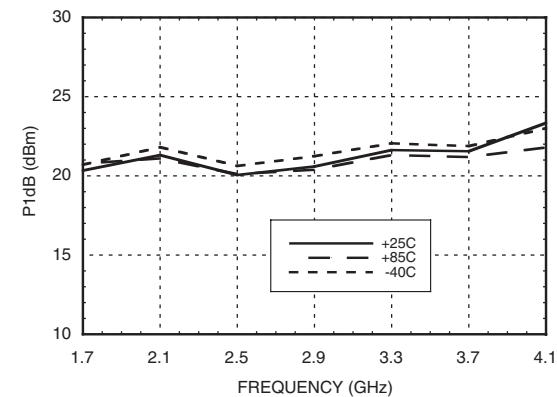
The HMC615LP4 & HMC615LP4E are high linearity, GaAs FET converter ICs that operate from 2.3 to 4.0 GHz and deliver a +35 dBm input third order intercept point. The LO amplifier output and high dynamic range mixer input are positioned so that an external LO filter can be placed in series between them. The IC operates from a single +5V supply consuming 65 mA of current and accepts a LO drive level of -2 to +6 dBm. The design requires no external baluns and supports IF frequencies between DC and 1 GHz. The HMC615LP4(E) is pin for pin compatible with the HMC551LP4(E), HMC552LP4(E) and HMC215LP4(E), which operate from 0.8 to 4.0 GHz.

Electrical Specifications, $T_A = +25^\circ C$, LO = 4 dBm, Vcc = +5V, IF = 200 MHz*

Parameter	Min.	Typ.	Max.	Units
Frequency Range, RF, LO		2.3 - 4.0		GHz
Frequency Range, IF		DC - 1.0		GHz
Conversion Loss *		10	13	dB
Noise Figure (SSB)		10		dB
LO to RF Isolation	5	15		dB
LO to IF Isolation *	1	10		dB
IP3 (Input)		35		dBm
1 dB Compression (Input)		21		dBm
LO Drive Input Level (Typical)	-2 to +6			dBm
Supply Current (Icc)	65	75		mA

*Unless otherwise noted, all measurements performed as a downconverter configured as shown in application circuit.

**Conversion Gain
vs. Temperature @ LO = 4 dBm**

**GaAs MMIC MIXER w/ INTEGRATED
LO AMPLIFIER, 2.3 - 4.0 GHz**
Isolation @ LO = 4 dBm

Conversion Gain vs. LO Drive

Return Loss @ LO = 4 dBm

Upconverter Performance (IF= 100 MHz)
Conversion Gain vs. LO Drive

IF Bandwidth @ LO = 4 dBm


Input IP3 vs. Temperature @ LO = 4 dBm

Input IP3 vs. LO Drive

Input IP2 vs. Temperature @ LO = 4 dBm

Input IP2 vs. LO Drive

Input P1dB vs. Temperature @ LO = 4 dBm




MICROWAVE CORPORATION



v00.0107

HMC615LP4 / 615LP4E

GaAs MMIC MIXER w/ INTEGRATED LO AMPLIFIER, 2.3 - 4.0 GHz

MxN Spurious @ IF Port

	nLO				
mRF	0	1	2	3	4
0	xx	-11	-1	6	10
1	5	0	16	37	36
2	58	67	55	51	60
3	98	102	103	82	91
4	99	99	104	106	107

RF Freq. = 3 GHz @ -10 dBm
LO Freq. = 2.8 GHz @ 0 dBm
All values in dBc relative to the IF power level.

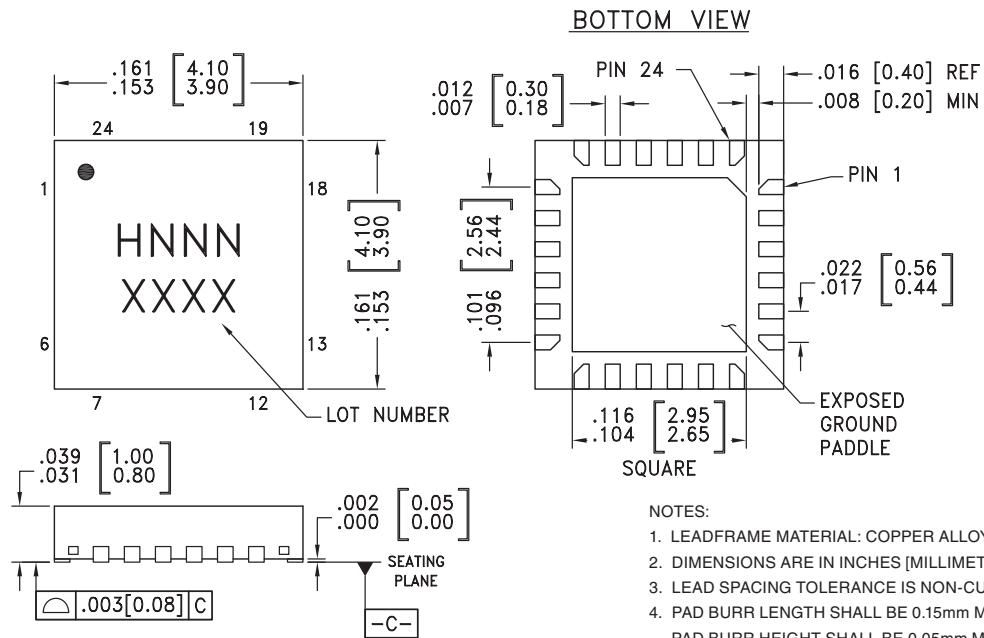
Harmonics of LO

	nLO Spur @ RF Port			
LO Freq. (GHz)	1	2	3	4
1	8	13	29	31
1.8	5	23	18	34
2.6	11	12	27	30
3.4	15	16	48	27
4.2	7	18	27	27
5	16	27	38	43

LO = 0 dBm
All values in dBc below input LO level measured at RF port.

Absolute Maximum Ratings

RF / IF Input (Vcc= +5V)	+27 dBm
LO Drive (Vcc= +5V)	+10 dBm
BIAS	+7 Vdc
Junction Temperature	150°C
Continuous Pdiss (T = 85°C) (derate 5.21 mW/°C above 85°C)	0.339 W
Thermal Resistance (junction to ground paddle)	192 °C/W
Storage Temperature	-65 to +150°C
Operating Temperature	-40 to +85°C


**ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS**
Outline Drawing

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC615LP4	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H615 XXXX
HMC615LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	H615 XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

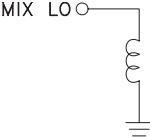
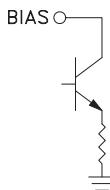
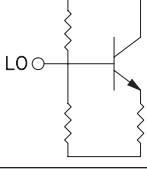
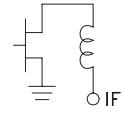
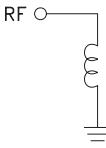
[3] 4-Digit lot number XXXX

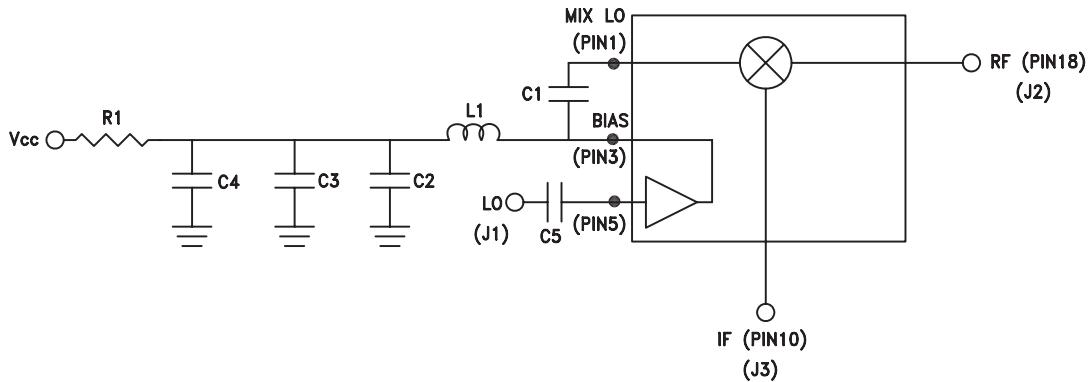
For price, delivery, and to place orders, please contact Hittite Microwave Corporation:

20 Alpha Road, Chelmsford, MA 01824 Phone: 978-250-3343 Fax: 978-250-3373

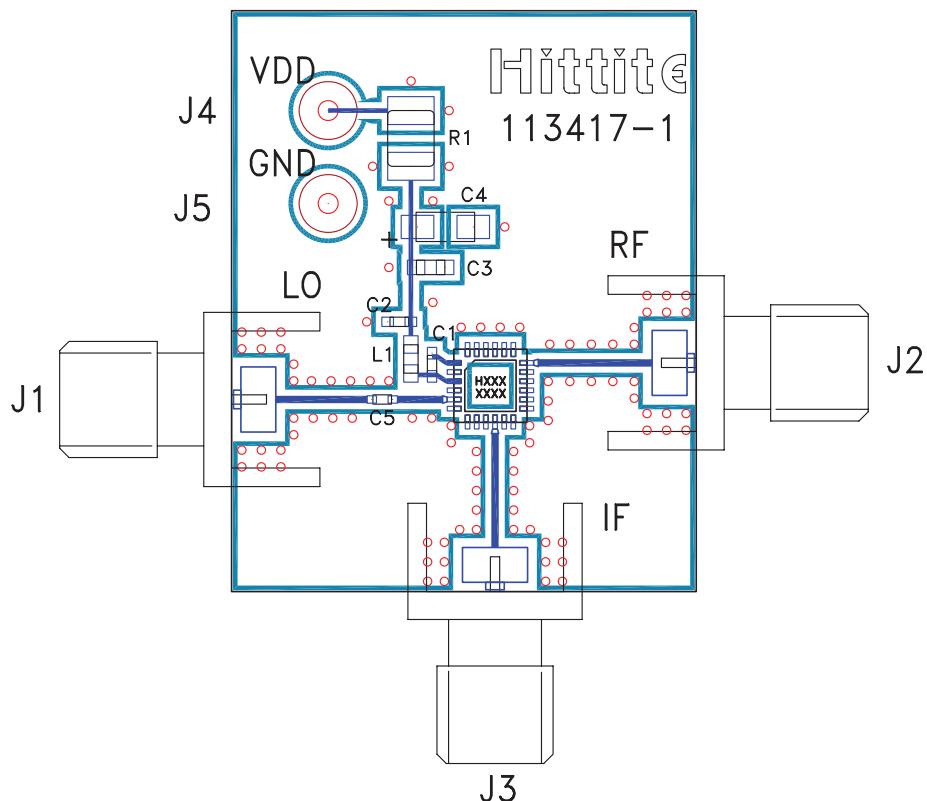
Order On-line at www.hittite.com

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	MIX LO	This pin is DC coupled and matched to 50 Ohms. An off chip DC blocking capacitor is required.	
2, 6 - 9, 11 - 17, 19 - 24	N/C	No connection. These pins may be connected to RF ground. Performance will not be affected.	
3	BIAS	Power supply for the LO amplifier. Three external bypass capacitors are recommended for optimum performance, as illustrated in the application circuit.	
4	GND	Backside of package has exposed metal ground paddle that must also be connected to ground.	
5	LO	This pin is DC coupled and matched to 50 Ohms. An off chip DC blocking capacitor is required.	
10	IF	This pin is DC coupled. For applications not requiring operation to DC, this port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary IF frequency range. For operation to DC, this pin must not source/sink more than 18 mA of current or die non-function and possible die failure will result.	
18	RF	This pin is DC coupled and matched to 50 Ohms.	

Application Circuit

Recommended Components Values (IF = DC - 300 MHz)	
C3	1000 pF
C4	2.2 μ F
C1, C2, C5	100 pF
L1	18 nH
R1	18 Ohm

Evaluation PCB**List of Materials for Evaluation PCB 113722 [1]**

Item	Description
J1 - J3	PCB Mount SMA RF Connector
J4, J5	DC Pin
C1, C2, C5	100 pF Chip Capacitor, 0402 Pkg.
C3	1000 pF Chip Capacitor, 0603 Pkg.
C4	2.2 μ F Capacitor, Tantalum
L1	18 nH Chip Inductor, 0603 Pkg.
R1	18 Ohm Resistor, 1210 1/8 watt Pkg.
U1	HMC615LP4 / HMC615LP4E
PCB [2]	113417 Evaluation Board

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350