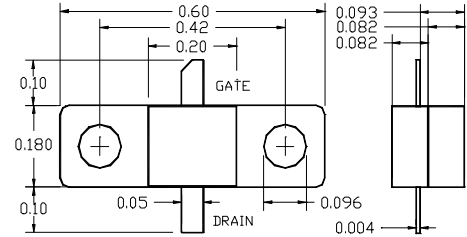


TENTATIVE DATA SHEET
5.0-6.0GHz, 1W Internally Matched Power FET

- 5.0-6.0GHz BANDWIDTH AND INPUT/OUTPUT IMPEDANCE MATCHED TO 50 OHM
- FEATURES HIGH PAE(35% TYPICAL)
- 31.0dBm TYPICAL P_{1dB} OUTPUT POWER
- 13dB TYPICAL G_{1dB} POWER GAIN
- NON-HERMETIC 180 MIL METAL FLANGE PACKAGE


ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

SYMBOLS	PARAMETERS/TEST CONDITIONS	EIA506-1P180F						UNIT
		MIN	TYP	MAX				
P_{1dB}	Output Power at 1dB Compression $f=5.0\text{-}6.0\text{GHz}$ $V_{ds}=8\text{V}$, $I_{dsq}=0.5 I_{dss}$	29.5	31.0					dBm
G_{1dB}	Gain at 1dB Compression $f=5.0\text{-}6.0\text{GHz}$ $V_{ds}=8\text{V}$, $I_{dsq}=0.5 I_{dss}$	11	13					dB
PAE	Power Added Efficiency at 1dB compression $f=5.0\text{-}6.0\text{GHz}$ $V_{ds}=8\text{V}$, $I_{dsq}=0.5 I_{dss}$		35					%
I_{d1dB}	Drain Current at 1dB Compression		440					mA
IP3	Output 3 rd Order Intercept Point $f=5.0\text{-}6.0\text{GHz}$ $V_{ds}=8\text{V}$, $I_{dsq}=0.5 I_{dss}$		37					dBm
I_{dss}	Saturated Drain Current $V_{ds}=3\text{V}$, $V_{gs}=0\text{V}$	550	720	850				mA
G_m	Transconductance $V_{ds}=3\text{V}$, $V_{gs}=0\text{V}$		760					mS
V_p	Pinch-off Voltage $V_{ds}=3\text{V}$, $I_{ds}=6\text{mA}$		-1.0	-2.5				V
BV_{gd}	Drain Breakdown Voltage $I_{gd}=4.8\text{mA}$	-13	-15					V
R_{th}	Thermal Resistance (Au-Sn Eutectic Attach)		16					$^\circ\text{C/W}$

MAXIMUM RATINGS AT 25°C

SYMBOLS	PARAMETERS	ABSOLUTE ¹	CONTINUOUS ²
V_{ds}	Drain-Source Voltage	12V	8V
V_{gs}	Gate-Source Voltage	-8V	-3V
I_{ds}	Drain Current	I_{dss}	I_{dss}
I_{gsf}	Forward Gate Current	90mA	15mA
P_{in}	Input Power	29dBm	@ 3dB Compression
T_{ch}	Channel Temperature	175 $^\circ\text{C}$	150 $^\circ\text{C}$
T_{stg}	Storage Temperature	-65/175 $^\circ\text{C}$	-65/150 $^\circ\text{C}$
P_t	Total Power Dissipation	8.5W	7.1W

Note: 1. Exceeding any of the above ratings may result in permanent damage.

2. Exceeding any of the above ratings may reduce MTTF below design goals.

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